

**AUTOMOTIVE PRODUCT AEC-Q100G Qualification Test Plan**

**Objective: Qualification of SOIC 54LD 6.4EP Cu Wire (G630AY MC)**

Qual Vehicle PN: <b>Product A</b> Qual Vehicle Name: :	Customer Name(s): <b>General Market</b> PN(s):	Test Program ID: VARIOUS Test Program Rev:	Report Type: QUAL RESULTS Revision #: 1 Date: 03Apr14
Technology: <b>SMSAP</b> Package Description: <b>SOIC 54 300ML 6.4EP</b>	Mask set#: <b>XXXX</b> Revision #:	Rel. Circuits Doc. #: CAB #: 13352188M FSL Qual Quartz Tracking #: 224470	Rel. Engr. Approval Signature: Tian Meng Date: 03Apr14
Fab site: <b>CHD-Fab</b> Assembly site: <b>FSL-TJN-FM</b> Final Test site: <b>FSL-TJN-FM</b> Rel Test site:	Product Engr: <b>Gary Gavlick</b> Packaging Engr: <b>Li Bo</b> Reliability Engr: <b>Tian Meng</b>	Target Dates: NA Test Start: Test Finish: PPAP target date:	CAB Approval Signature: Yanil Cruz Date: 03APR14
Die Size (in mm) <b>6.083x3.599x0.356</b> W x L x T	Part Operating <b>-40 to 135</b> Temp. Range: AEC Grade:	Freescall Contact: Bai Yun Phone Number: +86-85684236	Customer Approval Signature: Date:

**PRE-STRESS REQUIREMENTS/OPTIONS**

Stress	JEDEC22 Reference	Test Conditions	End Point Requirements	Minimum Sample Size per lot	# of Lots	Total Units including spares (Note 1)	Results					Comments (Generic Data: Note 2)
							Lot A nominal	Lot B nominal	Lot C nominal	Lot D HH	Lot E LL	
PC	A113 J-STD-020	<b>Preconditioning (PC)</b> MSL 3 at 260°C, +5/-0°C ( Change as needed) CSAM: Note 3	TEST at RH (add C if PC before HTOL); CSAM	All surface mount devices prior to THB/HAST, AC/UHST, TC, PC+PTC, or as required per individual stress Test Conditions.								PC is performed and results reported as part of the individual stress tests.

**GROUP A - ACCELERATED ENVIRONMENTAL STRESS TESTS**

HAST	A110	<b>Highly Accelerated Stress Test (HAST):</b> PC before HAST if required. HAST = 110°C/85%RH for 264 hrs,528hrs FIO. Bias: per HAST schematic Timed RO of 48hrs. MAX	TEST @ RH; CSAM	77	3	240	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	
UHST	A118	<b>Unbiased HAST (UHST):</b> PC before UHST if required. UHST = 110°C/85%RH for 264 hrs,528hrs FIO. Timed RO of 48hrs. MAX	TEST @ R; CSAM	77	3	240	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	Precond: 0/77 264hrs: 0/77 528hrs: 0/77	
TC	A104 AEC Q100-Appendix 3	<b>Temperature Cycle (TC):</b> PC before TC if required. TC = -50°C to 150°C for 1000 cycles,2000cycles FIO WBP after qual readpoint on 5 devices from each lot; 2 bonds per corner and one mid-bond per side on each device. Record which pins were used.	TEST @ H WBP => 3 grams CSAM	77	3	240	Precond: 0/77 1000cyc: 0/77 2000cyc: 0/77 WP: pass	Precond: 0/77 1000cyc: 0/77 2000cyc: 0/77 WP: pass	Precond: 0/77 1000cyc: 0/77 2000cyc: 0/77 WP: pass	Precond: 0/77 1000cyc: 0/77 2000cyc: 0/77 WP: pass	Precond: 0/77 1000cyc: 0/77 2000cyc: 0/77 WP: pass	
HTSL	A103	<b>High Temperature Storage Life (HTSL):</b> HTSL = 150°C for 1008 hrs,2016hrs FIO Timed RO = 96hrs. MAX	TEST @ RH	45	1	48	1008hrs: 0/48 2016 hrs: 0/48					

**TEST GROUP B - ACCELERATED LIFETIME SIMULATION TESTS**

HTOL	A108	<b>High Temperature Operating Life (HTOL):</b> HTOL = 125°C for 1008 hrs,2016hrs FIO Bias:40V Timed RO of 96hrs. MAX	TEST @ RHC;	77	3	240	1008hrs: 0/77 2016hrs: 0/77	1008hrs: 0/77 2016hrs: 0/77	1008hrs: 0/77 2016hrs: 0/77			For HTOL drift analysis requirements, see Notes 5 & 6. 3 lots with MM condition
ELFR	AEC Q100-008	<b>Early Life Failure Rate (ELFR):</b> ELFR = 125°C for 48 hrs; Timed RO of 48 hrs MAX	TEST @ RH	800	3	2409	0/800	0/800	0/800			

**TEST GROUP C - PACKAGE ASSEMBLY INTEGRITY TESTS**

Full Assy. CZ + Cu WB Cz	FSL Internal Requirement	Full assembly process CZ Data collection per FSL CZ template (for Cu WB) for 3 tech cert lots with nominal Cu WB process. Perform Wire Bond CZ specifically for Copper Wire for 1 HH and 1 LLTech Cert lots.			3		Pass			Pass	Pass	
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<b>WBS</b>	AEC Q100-001	<b>Wire Bond shear (WBS)</b>	Cpk = or > 1.67	30 bonds from minimum 5 units	3	15	Pass			Pass	Pass	Performed by Assembly Site during qual lot builds - PE to include this requirement in the qual lot build ERF.
<b>WBP</b>	MilStd883-2011	<b>Wire Bond Pull (WBP):</b> Cond. C or D	Cpk = or > 1.67	30 bonds from minimum 5 units	3	15	Pass			Pass	Pass	Performed by Assembly Site during qual lot builds - PE to include this requirement in the qual lot build ERF.

**TEST GROUP D - DIE FABRICATION RELIABILITY TESTS**

**TEST GROUP E - ELECTRICAL VERIFICATION TESTS**

<b>TEST</b>	Freescall 48A	<b>Pre- and Post Functional / Parametrics (TEST):</b> Test software shall meet requirements of AEC-Q100-007. Testing performed to the limits of device specification in temperature and limit value.	0 Fails	All	All	All						TEST results is shown for each individual stress test in the qual results report generated upon qual completion. FSL SQA release required for qual test program.
<b>ED</b>	AEC-Q100-009, Freescall 48A spec	<b>Electrical Distribution (ED)</b>	TEST @ RHC Cpk = or > 1.67	30	3	90	See Cab presentation			See Cab presentation	See Cab presentation	

**General Notes:**

- 1- Optional Spare Units: 'xx+y' indicates additional units have been added to the sample size to be used ONLY in the event of lost or mechanically damaged units so as to have the required number of samples at test completion; these 'spares' shall not be used to replace failing units; ALL failures found in the total sample size shall be recorded and acted upon accordingly.
- 2- Generic Data: Document source of all generic data in the Generic Data Reference List below.
- 3- CSAM SS=11 units for each stress test for each lot when required.
- 5- HTOL Drift Analysis (%drift): Drift analysis will be performed on all 3 HTOL lots, 80 units per lot, and will consist of evaluating the T0 to Treadpoint parametric means of the populations at each readpoint looking for drift that exceeds 10%; [% of drift = (Average value @ Treadpoint - Average value @ T0) / (Limit Max- Limit Min)]
- 6- HTOL Drift Analysis (Cpk): Any parameter with a Cpk less than 1.67 after the final readpoint will be identified.

**Generic Data Reference List:**

References shall include (as applicable) Part Number, PPAP#, QUARTZ#, and date data generated. Generic data can not be older than 2 years and must not have ANY valid rejects. CofDC's must be available for all devices used as generic data in order to show applicability to the part being qualified.

Notes: This Data will be used to qualify the following devices: