

AN14546

基于MCX N23x的双向USB音频主机

第1.0版—2025年1月23日

应用笔记

文档信息

信息	内容
关键词	AN14546、音频、USB主机、USB音频主机、双向音频、MCX N23x、MCX N236
摘要	本应用笔记介绍了如何在MCX N23x上实现双向USB音频主机，以及如何对其进行测试。



1 介绍

MCX N23x系列MCU是MCX N系列的第二款MCU。与MCX Nx4x系列相比，该系列在成本、内存和功耗方面进行了优化。它可以被视为MCX Nx4x系列的一个精简版本，但仍然支持丰富的外设接口。图1所示为MCX N23x的框图。

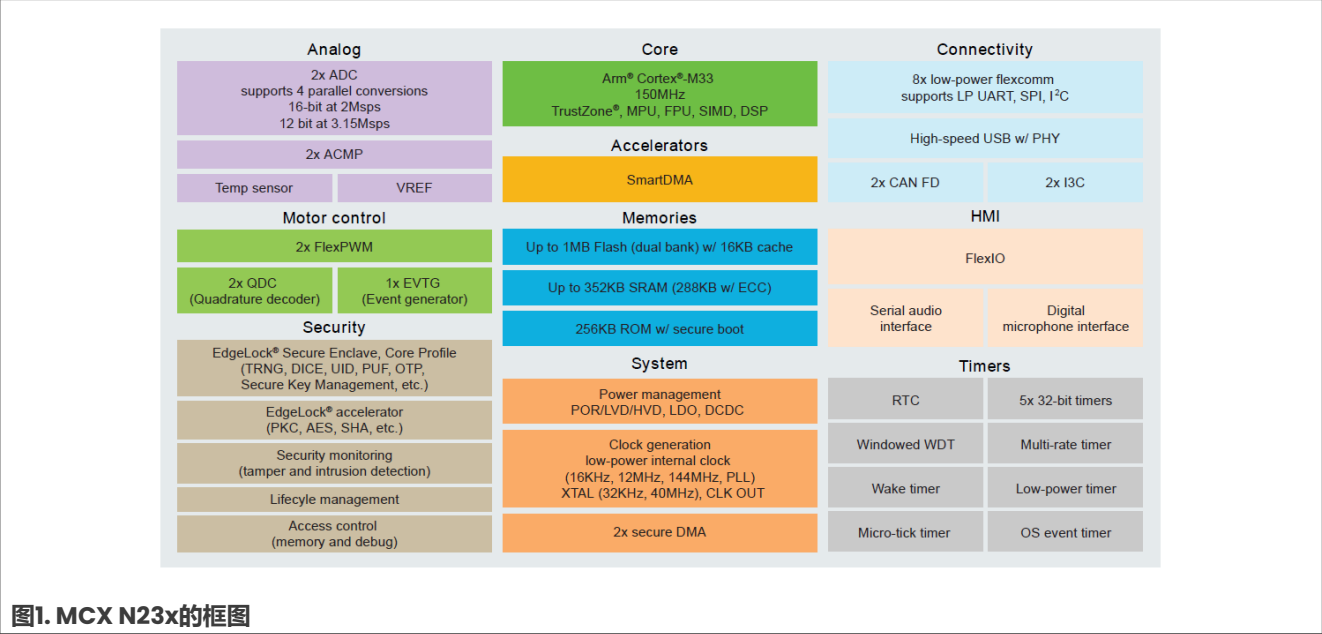


图1. MCX N23x的框图

如图1所示，MCX N23x具有一个高速（HS）USB，可支持主机和设备模式，并配备两个SAI模块，因此适用于USB音频应用。图2所示为MCX N23x上的SAI模块的框图。

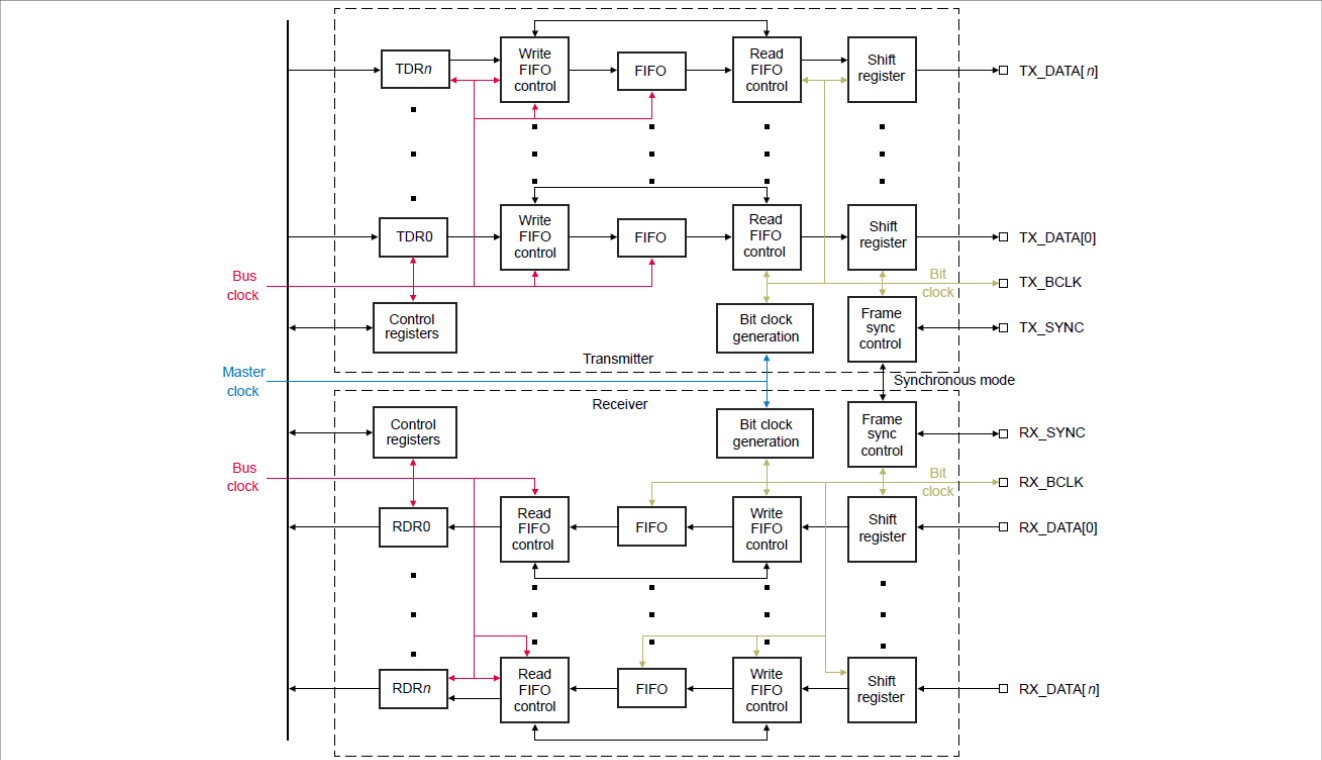


图2. MCX N23x上的SAI的框图

- MCX N23x的SAI模块能够支持以下功能：
- 带有独立位时钟和帧同步的发送器，支持两条数据线
 - 带有独立位时钟和帧同步的接收器，支持两条数据线
 - 每条数据线最大帧大小为32个字
 - 可分别配置帧内第一个字和其余字的字长
 - 每条发送和接收数据线配备异步8×32位FIFO，支持：
 - 发生FIFO错误后平稳重启。
 - 发生FIFO错误后自动重启，无需软件干预。
 - 将8位和16位数据打包到每个32位的FIFO字中。
 - 将多数据线的FIFO合并为单数据线的FIFO。

FRDM-MCXN236开发板可用于评估MCX N23x的各种外设，但默认情况下，该开发板上并未焊接音频编解码器DA7212和音频插座。需要将这些组件焊接到开发板上，以支持音频功能的评估。此外，FRDM-MCXN236 SDK v2.16还提供了丰富的SAI示例，如表1所示。

表1. FRDM-MCXN236 SDK v2.16中的SAI示例

1	sai_edma_record_playback
2	sai_edma_transfer
3	sai_interrupt
4	sai_interrupt_record_playback
5	sai_interrupt_transfer

此外，该SDK还提供了一些与USB音频相关的示例，这些示例都位于usb_examples目录中，如表2所示。

表2. FRDM-MCXN236 SDK v2.16中的USB音频示例

1	usb_device_audio_generator
2	usb_device_audio_generator_lite
3	usb_device_audio_speaker
4	usb_device_audio_speaker_lite
5	usb_device_composite_hid_audio_unified
6	usb_device_composite_hid_audio_unified_lite
7	usb_host_audio_speaker

对于USB音频主机，FRDM-MCXN236 SDK v2.16仅提供了一个单独的USB主机音频扬声器示例，该示例仅支持USB扬声器设备。本应用笔记作为对SDK示例的补充，介绍了如何在MCX N23x上实现双向USB音频主机，使其能够同时支持集成了USB音频扬声器和USB音频录音机的复合设备。

2 双向USB音频主机的实现

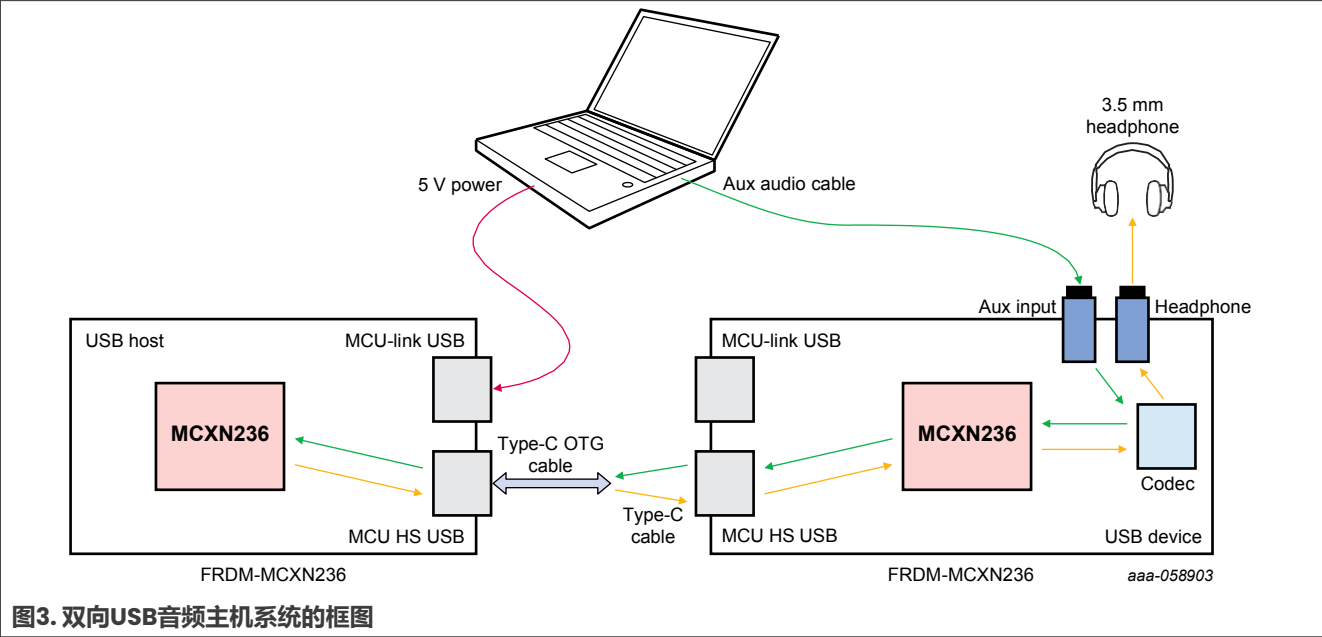
本节介绍了如何实现双向USB音频主机及其测试所需的硬件。

2.1 硬件

测试双向USB音频主机需要以下硬件：

- 两块FRDM-MCXN236 Rev C
- 两根Type-C USB数据线
- 一根Type-C OTG数据线
- 一个3.5 mm耳机
- 一根3.5 mm辅助（Aux）音频数据线

图3所示为硬件连接的框图。



2.2 软件

可以基于FRDM-MCXN236 SDK v2.16中的usb_host_audio_speaker示例来实现双向USB音频主机。LPCXpresso5516 SDK v2.16提供了一个usb_host_audio_unified_bm示例，支持集成了USB音频扬声器、USB音频录音机和HID类的复合设备。图4所示为LPCXpresso5516 SDK v2.16中usb_host_audio_unified_bm示例的结构。

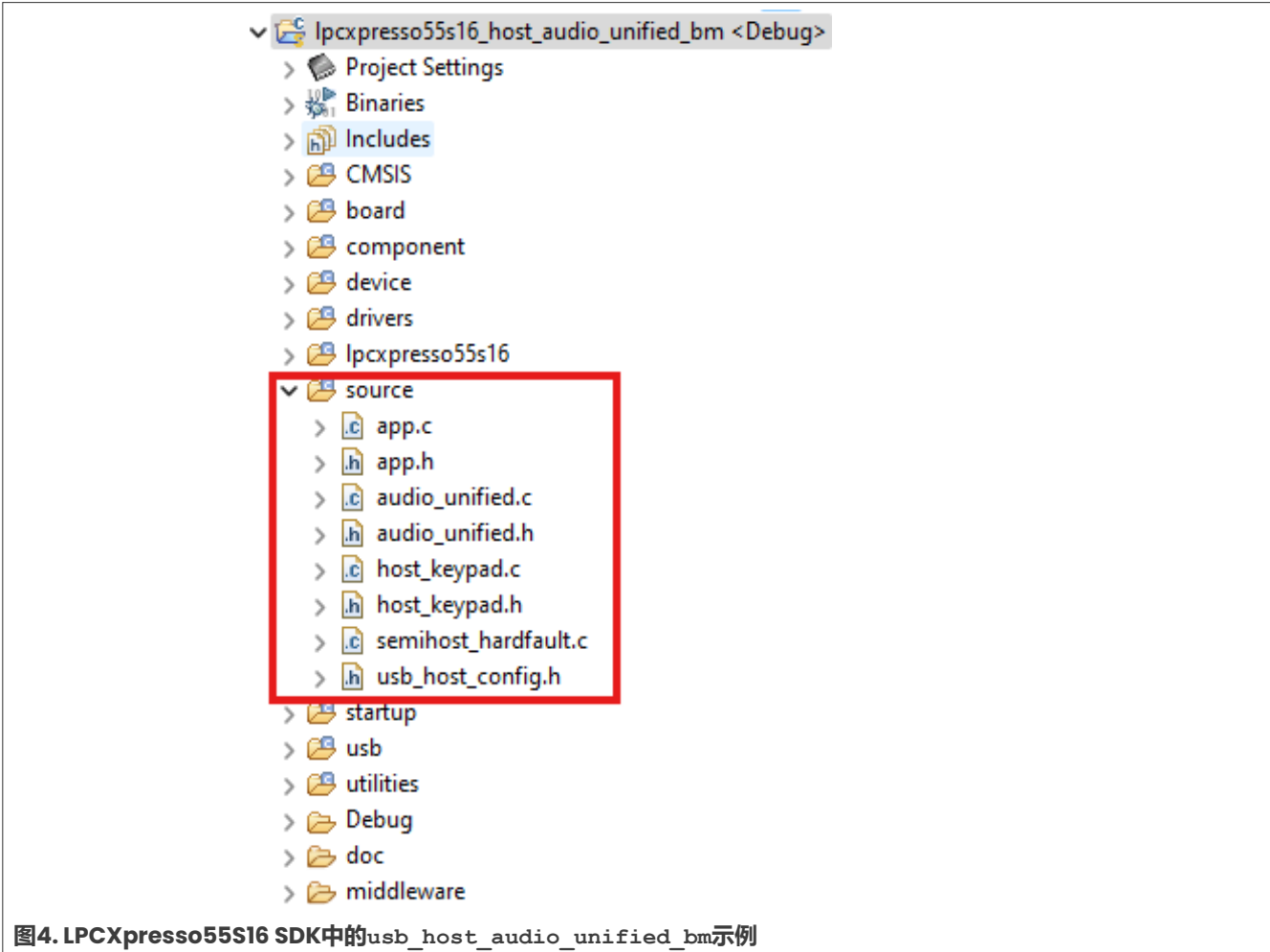


图4. LPCXpresso55S16 SDK中的usb_host_audio_unified_bm示例

因此，必须将相关的源文件移植到usb_host_audio_speaker示例，以在MCX N23x上实现双向USB音频主机。本应用笔记中使用的IDE为MCUXpresso IDE 11.9.1。

移植步骤如下：

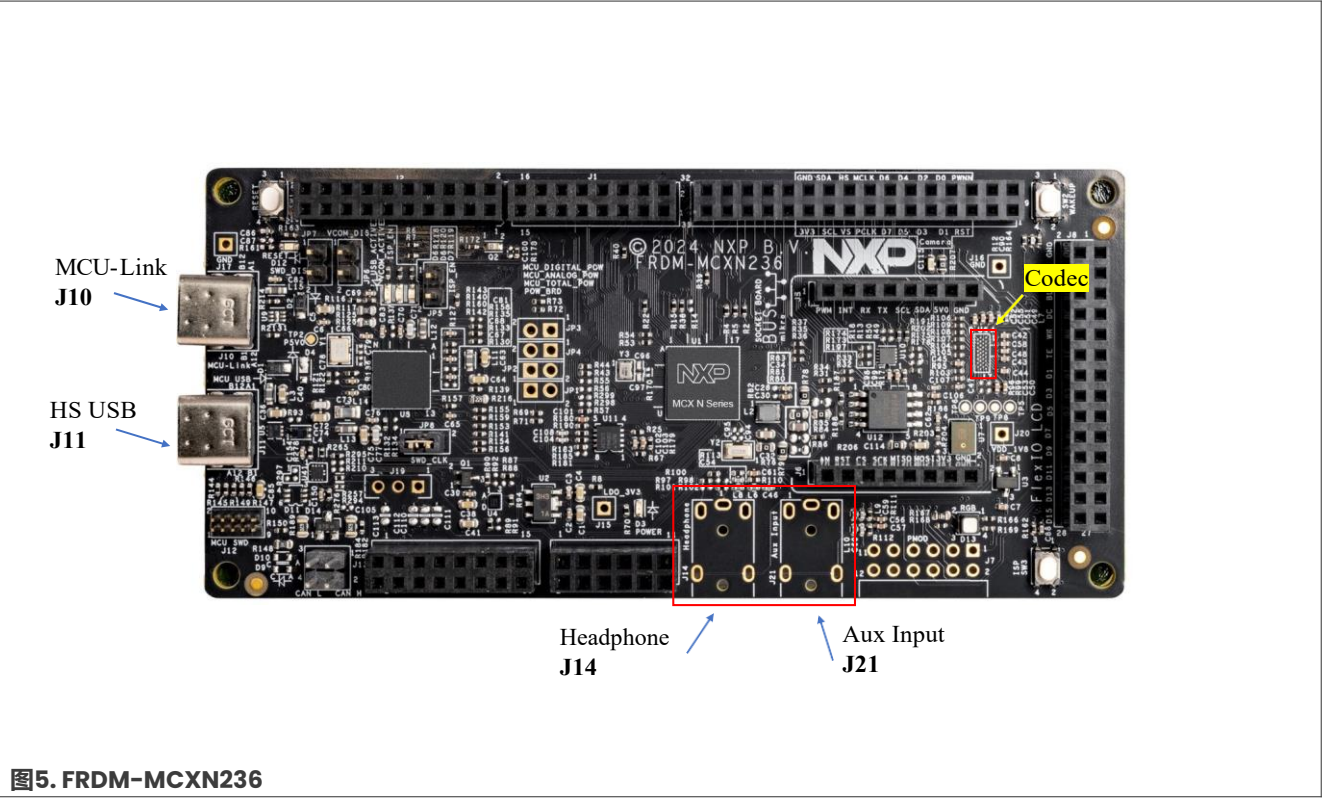
- 1. 复制audio_unified.c和audio_unified.h文件，以替换audio_speaker.c和audio_speaker.h文件。
- 2. 修改app.c文件中的头文件，将#include "audio_speaker.h"更改为#include "audio_unified.h"。
- 3. 修改usb_host_config.h文件中的宏定义，将USB_HOST_CONFIG_AUDIO宏的值从1改为2，以支持两个音频类。

至此，已经快速实现了一个双向USB音频主机，它实现了一个功能，即从USB录音机通道接收音频数据，并通过USB扬声器通道播放接收到的音频数据。

可以使用FRDM-MCXN236 SDK中的usb_device_composite_hid_audio_unified示例作为USB音频设备，来测试该双向USB音频主机。下一节将介绍如何测试该双向USB音频主机。

3 双向USB音频主机的测试

本节介绍了如何测试双向USB音频主机。首先，需要对FRDM-MCXN236 Rev C 开发板进行改装，因为默认情况下，该开发板上的音频编解码器和两个音频插座并未焊接，如图5所示。需要焊接一个音频编解码器和两个3.5 mm音频插座，然后将此开发板用作USB音频设备，来测试SAI模块。音频编解码器的型号为DA7212，音频插座的型号为54-00174。



改装完开发板后，编译FRDM-MCXN236 SDK v2.16中的usb_device_composite_hid_audio_unified示例，然后通过板载调试器（MCU Link）接口J10将其下载到MCXN236。

在该示例中，USB录音机通道的音频数据来自辅助（Aux）输入接口J21，因此需要使用一根辅助（Aux）音频数据线将J21连接至计算机上的3.5 mm模拟音频输出接口，或连接至其它具有3.5 mm音频输出接口的设备，并将一个3.5 mm耳机连接至耳机接口J14。

对于USB设备，如果不希望改装FRDM-MCXN236开发板，可以选择其它带有音频编解码器的开发板，如LPCXpresso55S16和LPCXpresso55S69，并将其对应SDK中的usb_device_composite_hid_audio_unified示例下载到该开发板上。

对于USB音频主机：

- 编译第2.2节中修改过的USB音频主机示例，然后使用Type-C数据线将板载调试器接口J10连接至PC，并将编译好的固件下载到MCX N236。
- 将Type-C OTG数据线连接至MCXN236的HS USB接口J11，断开USB设备端MCU-Link与PC之间的连接，然后使用这条Type-C USB数据线来连接USB设备的HS USB端口J11。

- 按下USB主机开发板上的复位按钮**SW1**，以运行USB音频主机程序，即可看到以下日志信息。该USB音频主机已成功识别出一个包含USB音频扬声器、USB音频录音机和USB HID类的复合设备。

```
COM10 - Tera Term VT
File Edit Setup Control Window Help
host init done
hid keypad attached:pid=0xa4vid=0x1fc9 address=1
hid audio attached:pid=0xa4vid=0x1fc9 address=1
keypad attached
USB audio unified device attached
AUDIO 2.0 device
AUDIO_GET_VOLUME_RANGE
Audio recorder information:
- Frequency device support frequency rang is :MIN 48000 Hz, MAX 48000 Hz, RES attributes 0Hz,
- Bit resolution : 16 bits
- Number of channels : 2 channels
- Transfer type : Isochronous
- Sync type : Synchronous
- Usage type : Data endpoint
Audio speaker information:
- Frequency device support frequency rang is :MIN 48000 Hz, MAX 48000 Hz, RES attributes 0Hz,
- Bit resolution : 16 bits
- Number of channels : 2 channels
- Transfer type : Isochronous
- Sync type : Synchronous
- Usage type : Data endpoint
USB host unified example is recording 48k 16bit 2ch format audio, then loop playback 48k 16bit 2ch format recorded audio.
```

图6. 来自USB音频主机的日志信息

最后，测试音频功能。在计算机上播放任意音频文件。音频通过辅助（Aux）输入接口被USB设备端的编解码器接收，然后通过USB录音机通道传输至USB音频主机端。USB音频主机通过USB扬声器通道播放接收到的音频数据，可以通过USB音频设备端上的耳机听到计算机播放的音频。

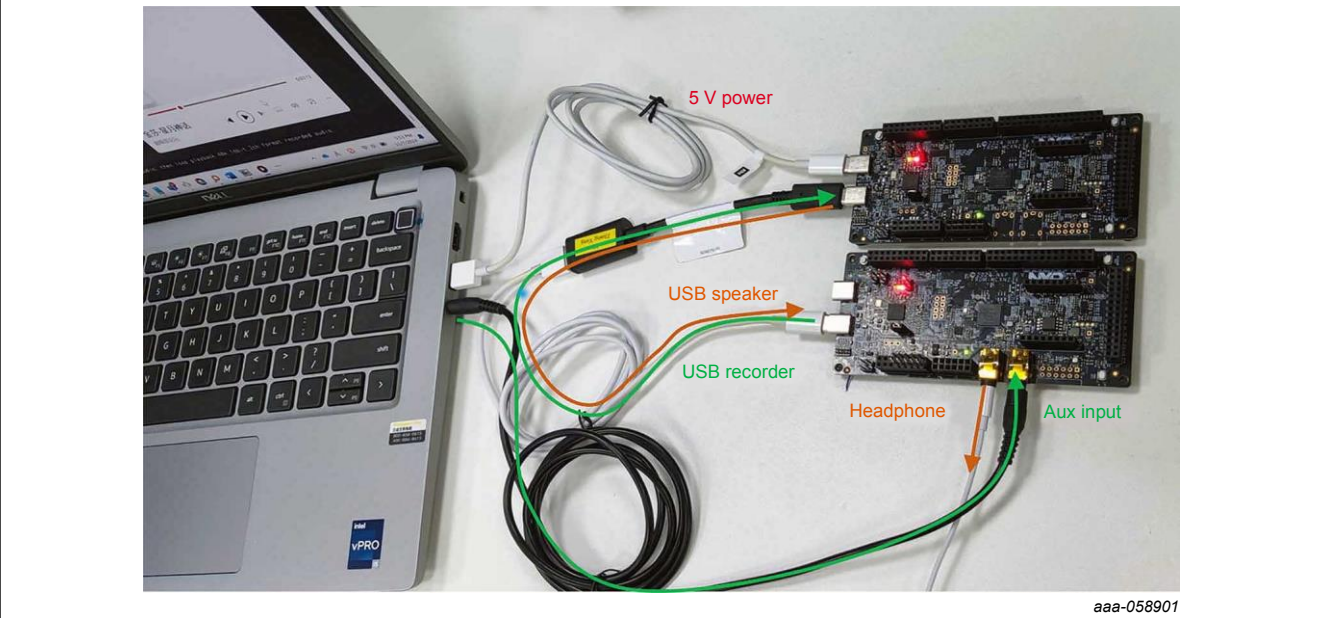


图7. 双向USB音频主机的测试

4 结论

本应用笔记介绍了如何在MCX N23x上实现双向USB音频主机，以及如何对其进行测试。通过本应用笔记，用户可以了解到MCX N23x适用于USB音频应用。它不仅可用作USB音频设备，还能够实现USB音频主机的功能。FRDM-MCXN236 SDK v2.16还提供了丰富的USB音频相关例程，客户可以基于这些例程快速开发自己的USB音频应用。

5 参考资料

- 《MCX N23x参考手册》（文档[MCXN23XRM](#)）

6 关于本文中源代码的说明

本文中所示的示例代码具有以下版权和BSD-3-Clause许可：

2025年恩智浦版权所有；在满足以下条件的情况下，可以源代码和二进制文件的形式重新分发和使用本源代码（无论是否经过修改）：

1. 重新分发源代码必须保留上述版权声明、这些条件和以下免责声明。
2. 以二进制文件形式重新分发时，必须在文档和/或随分发提供的其他材料中复制上述版权声明、这些条件和以下免责声明。
3. 未经事先书面许可，不得使用版权所有者的姓名或参与者的姓名为本软件的衍生产品进行背书或推广。

本软件由版权所有者和参与者“按原样”提供，不承担任何明示或暗示的担保责任，包括但不限于对适销性和特定用途适用性的暗示保证。在任何情况下，无论因何种原因或根据何种法律条例，版权所有或参与者均不对因使用本软件而导致的任何直接、间接、偶然、特殊、惩戒性或后果性损害（包括但不限于采购替代商品或服务；使用损失、数据损失或利润损失或业务中断）承担责任，无论是因合同、严格责任还是侵权行为（包括疏忽或其他原因）造成的，即使事先被告知有此类损害的可能性也不例外。

7 修订历史

[表3](#)汇总了本文档的修订情况。

表3. 修订历史

文档ID	发布日期	说明
AN14546 v1.0	2025年1月23日	首次公开发布

Legal information

Definitions

Draft — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <https://www.nxp.com.cn/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Suitability for use in non-automotive qualified products — Unless this document expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

HTML publications — An HTML version, if available, of this document is provided as a courtesy. Definitive information is contained in the applicable document in PDF format. If there is a discrepancy between the HTML document and the PDF document, the PDF document has priority.

Translations — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

NXP B.V. — NXP B.V. is not an operating company and it does not distribute or sell products.

Trademarks

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners.

NXP — wordmark and logo are trademarks of NXP B.V.

Microsoft, Azure, and ThreadX — are trademarks of the Microsoft group of companies.

目录

1 介绍..... 2

2 双向USB音频主机的实现..... 4

2.1 硬件..... 4

2.2 软件..... 5

3 双向USB音频主机的测试..... 7

4 结论..... 9

5 参考资料..... 9

6 关于本文中源代码的说明..... 9

7 修订历史..... 9

法律声明..... 10

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.