
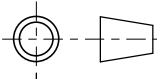


DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	SIZE	PLATED	QTY
+	0.31	PLATED	206
⊙	1.0	PLATED	3
•	1.1	PLATED	8
⊙	1.4	PLATED	16

ARTWORKS	LAYER Nb	PCB	REV.	TYPE
TOP LAYER	1	OM5578	00	311
INT. 1 LAYER	2	OM5578	00	311
INT. 2 LAYER	3	OM5578	00	311
INT. 3 LAYER	-	-	-	311
INT. 4 LAYER	-	-	-	311
INT. 5 LAYER	-	-	-	311
INT. 6 LAYER	-	-	-	311
INT. 7 LAYER	-	-	-	311
INT. 8 LAYER	-	-	-	311
BOTTOM LAYER	4	OM5578	00	311
POWER_1	-	-	-	311
POWER_2	-	-	-	311
POWER_3	-	-	-	311
POWER_4	-	-	-	311
POWER_5	-	-	-	311
SOLDER MASK TOP	5	OM5578	00	312
SOLDER MASK BOT	6	OM5578	00	312
SILKSCREEN TOP	7	OM5578	00	380
SILKSCREEN BOT	8	OM5578	00	380
MILLING	9	OM5578	00	313

Date	Draw by	Verify	Modifications		IND.
Date : 18-11-2015 Draw by : ASCO_SR Checked by: J.GESLIN Project : PN7150S		Material:FR4 TG150 LOW CTEz 16/10 17.5 uM of basic	Marging : +/-0.1	Nbr of layers : 4	
		Finition : Ni/Au chemical	Manufact.level : 5	Silkscreen TOP :YES BOTTOM:NO	
		Protection : SOLDERMASK TOP BOTTOM	Scale : 1/1		
NXP Semiconductors 2,Esplanade Anton Philips Campus Effiscience Colombelles BP 2000 - 14906 CAEN CEDEX 9					
DRILL MANUFACTURING PN7150S NFC Controller Board			SIZE A3	OM5578-300	REV. 00 PL. 1 / 1