Target External Vin 5V

Open use P1_6 for PCD7_SCK

2.2μF recommended to ensure USB 1μF minimum requirement is met for all conditions.

JS2

LPC5411x Current measurement

Vsense (1) Voltage 1-lsb

JP3 open LPC5411x Current 1-lsb

JP3 shunted LPC5411x Current 1-lsb

maximum current

ADC input 1-lsb

32μV 3.88μA 7.77μA 16mA

800μV

(1) Vsense voltage is between U16 RS+ to RS-. Total Rsense = R19 + (US10 || JP3).

JP3 Setting

<table>
<thead>
<tr>
<th>VDD LPC5411x (volts)</th>
<th>Total Rsense (ohms)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.3V open</td>
<td>8.24</td>
</tr>
<tr>
<td>1.8V shunted</td>
<td>4.12</td>
</tr>
</tbody>
</table>
Note: 2.2k on BRIDGE_T_INTR-ISP1 (P0_4) is required for SPI / I2C boot over host interface.