

深圳昊邦电子

Engineering Confirmation Note Layer stackup & impedance

ect :Questions about the stack and the impedance value

From : HaoBang Circuit Tech Co., Ltd

Writer :

Check :

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Stackup Information:

Layer	Info	Thickness
TOP	=====	0.5 OZ+Plating
	TU768-PP 3313	3.48 (mil)
L2	=====	1 Oz
	TU768-Core 0.1MM	3.94 (mil)
L3	=====	1 Oz
	TU768-PP 1080*2	5.19(mil)
	TU768-Core (No copper)	43.3(mil)
	TU768-PP 1080*2	5.19(mil)
L4	=====	1 Oz
	TU768-Core 0.1MM	3.94 (mil)
L5	=====	1 Oz
	TU768-PP 3313	3.48 (mil)
BOT	=====	0.5 OZ+Plating

Finished:	77.15(7.8/-7.8) mil	1.96+0.16/-0.16) MM
Designed:	78.75 mil	2.0 MM
Material:	TU768	

Impedance Information:

Ctrl	Ref	Imp_type	Cust_req	Imp_req	HB_des	Imp_des	mask	H1	Er1	H2	Er2
L1	L2	Single-Ended	5.0	50+/-10%	5.0	50.71		3.48	4.15		
L1	L2	Single-Ended	5.5	50+/-10%	5.5	48.58		3.48	4.15		
L1	L2	Differential	4.5/5.2	90+/-10%	4.2/5.2	89.15		3.48	4.15		

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L1	L2	Differential	3.7/6.5	100+/-10%	3.7/6.5	100.28		3.48	4.15		
L3	L2/L4	Single-Ended	5.0	50+/-10%	5.0	50.22		3.94	4.3	54.88	3.8
L3	L2/L4	Single-Ended	5.5	50+/-10%	5.5	48.14		3.94	4.3	54.88	3.8
L6	L5	Single-Ended	5.5	50+/-10%	5.5	48.58		3.48	4.15		
L6	L5	Single-Ended	5.0	50+/-10%	5.0	50.71		3.48	4.15		
L6	L5	Differential	4.5/5.2	90+/-10%	4.2/5.2	89.15		3.48	4.15		

Remark :

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