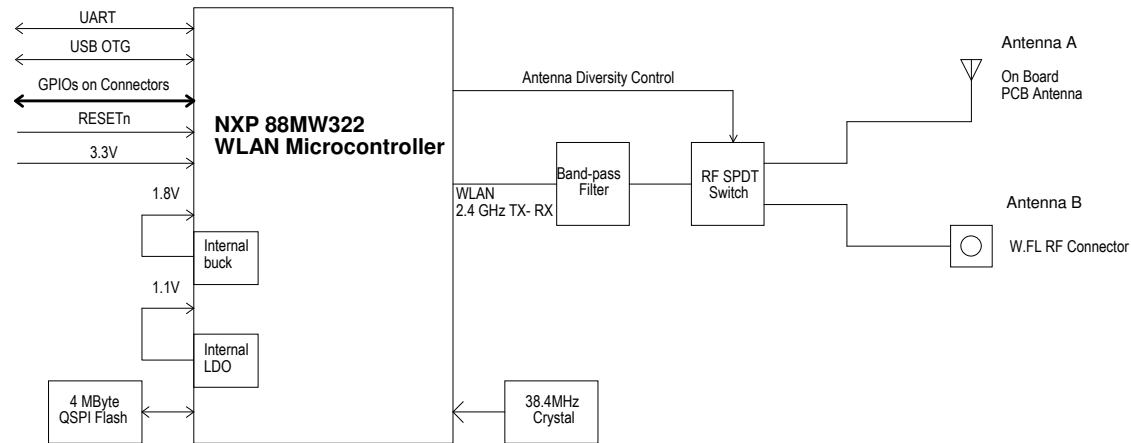


# NXP SEMICONDUCTORS

## Block Diagram




### Table of Contents

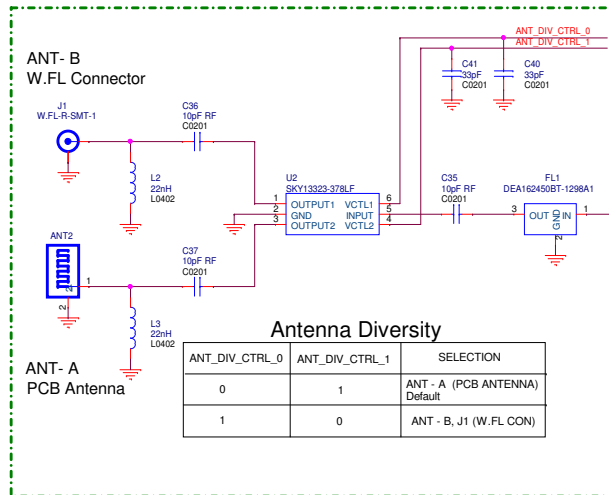
1. Block Diagram
2. 88MW322 QFN-88 SoC , Power, RF
3. Revision History

## WLAN Microcontroller, IEEE 802.11n/g/b RD-88MW322-QFN-1B-2A v1.0 Schematic

NOTE: THIS SCHEMATIC IS A PRELIMINARY DESIGN.  
NXP RESERVES THE RIGHT TO MAKE CHANGES  
TO THE SCHEMATIC AT ANY TIME WITHOUT NOTICE.

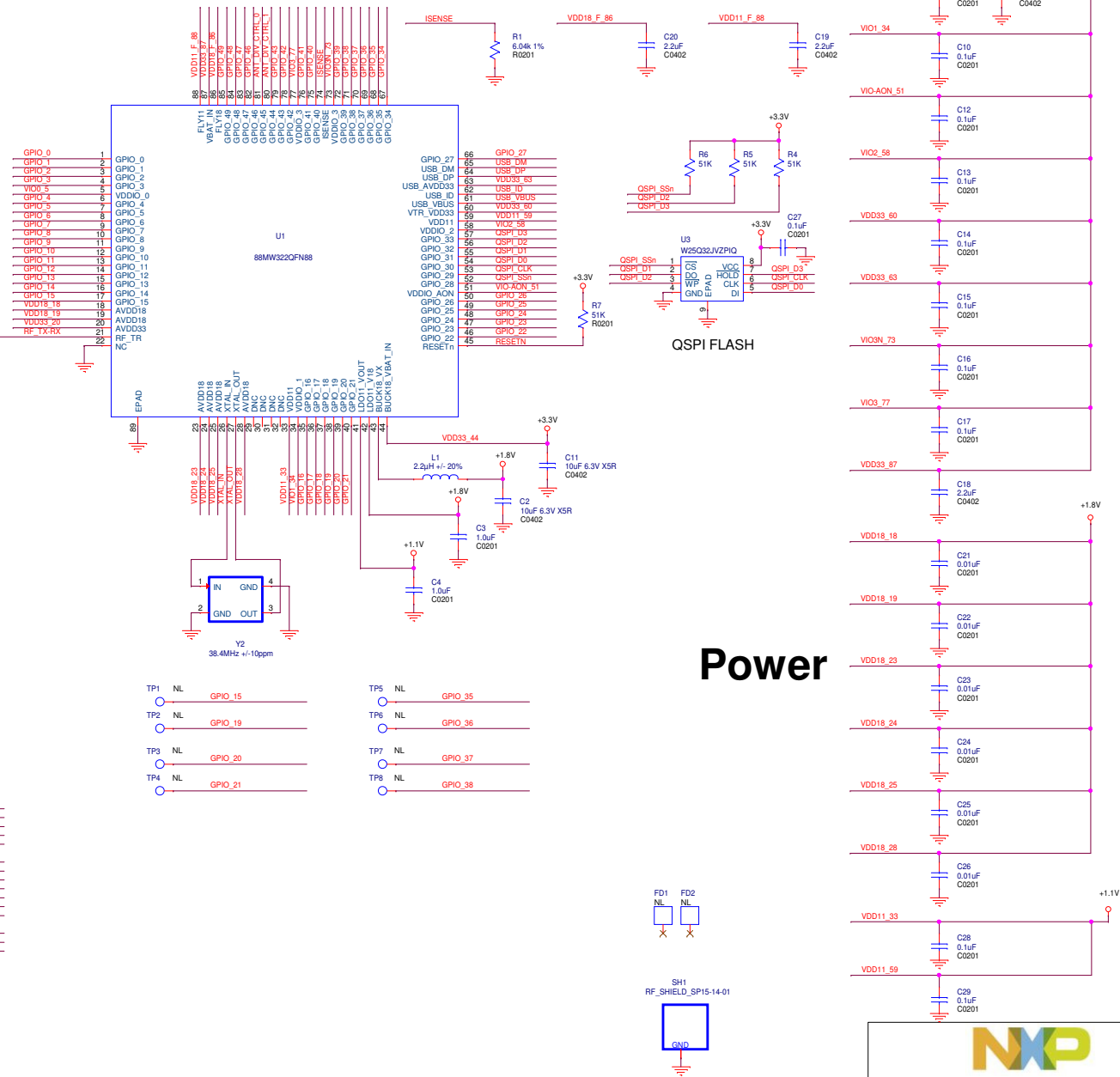
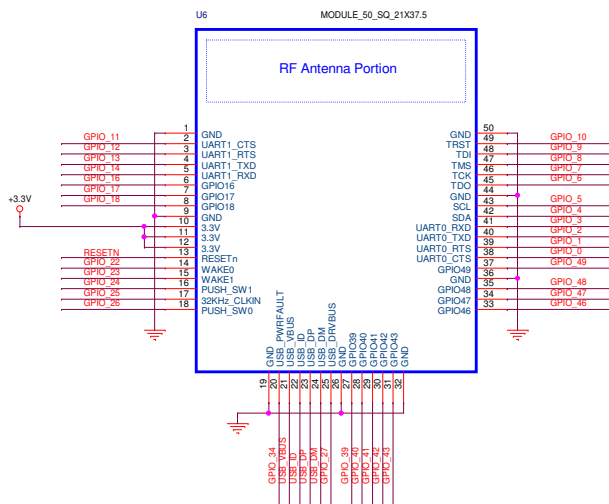
 NXP SEMICONDUCTORS	
Company	NXP SEMICONDUCTORS
Title	RD-88MW322-QFN-1B-2A V1.0 Schematic
Size	C
Date	Wednesday, July 06, 2020
Sheet	1 of 3
Rev	1

# WLAN RF Front End



ANT_DIV_CTRL_0	ANT_DIV_CTRL_1	SELECTION
0	1	ANT - A (PCB ANTENNA) Default
1	0	ANT - B, J1 (W.FL CON)


## Stamp Module Pin-out



NOTE: THIS SCHEMATIC IS A PRELIMINARY DESIGN.  
NXP RESERVES THE RIGHT TO MAKE CHANGES  
TO THE SCHEMATIC AT ANY TIME WITHOUT NOTICE.

Revision History		
Design Version	Document Revision	Change List
V1.0	Rev 1	Initial release

NOTE: THIS SCHEMATIC IS A PRELIMINARY DESIGN.  
NXP RESERVES THE RIGHT TO MAKE CHANGES  
TO THE SCHEMATIC AT ANY TIME WITHOUT NOTICE.

  
NXP SEMICONDUCTORS

Company	NXP SEMICONDUCTORS		
Title	RD-88MW322-QFN-1B-2A V1.0 Schematic		
Size	C	Rev	1
Date:	Wednesday, July 08, 2020	Sheet	3 of 3