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Revisions

Rev	Description	Date
A	Original Release	10-24-15

FRDMSTBC-A8471

		RF, Analog & Sensor Group 6501 William Cannon Drive West Austin, TX 78735-8089	
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Designer: Aswin Sivakumar		ICAP Classification: FOP: FIUQ: X PUBI:	
Drawing Title: Aswin Sivakumar		FRDMSTBC-A8471	
Drawn by: Aswin Sivakumar		Page Title: TITLE PAGE	
Approved: Team	Size C	Document Number SCH-28984 PDF: SPF-28984	Rev A
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1. Unless Otherwise Specified:

All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.
All capacitors are in uF, some are 10% or 20%
All voltages are DC
All polarized capacitors are tantalum

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Unpopulate R7 and populate R6 to use die heater

4. Device's default communication -SPI Interface. Populate R8,R9,R1(or R3)-SA1 ,R2(or R4)-SA0 for I2C interface.

5. Populate C4 if FXOS8700 is used instead of FXLS8471

6. Populate J7 and J8 for I2C Communication

7. Device type number is for reference only. The number varies with the manufacturer.

8. Special signal usage:

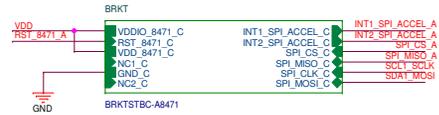
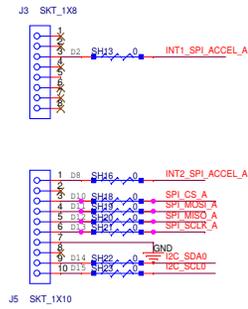
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals

9. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

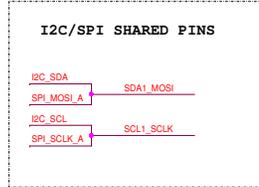
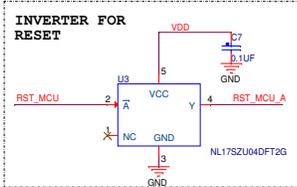
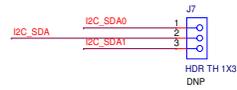
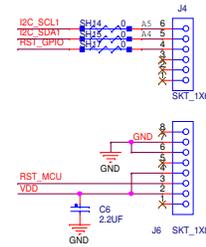


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SHIELD BOARD ARDUINO HEADER



SHIELD BOARD ARDUINO HEADER



ICAP Classification: FCP: FUC: X PUB:			
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