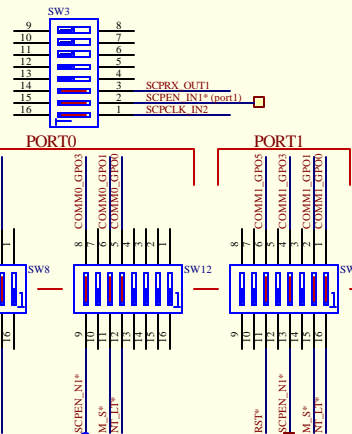


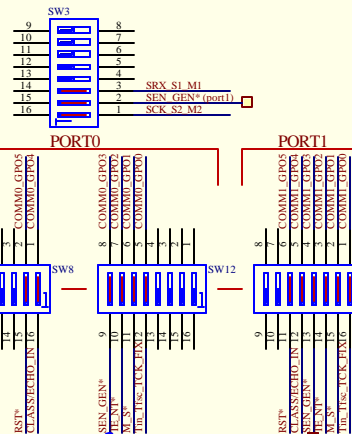
MC145572



- M_S* = 1 Master Mode
M_S* = 0 Slave Mode
- RST* = 1 normal operation
RST* = 0 HARDWARE RESET
- NT_LT* = 1 NT operating mode
NT_LT* = 0 LT operating mode
- SCPEN used with SCP IN1* with GCI mode
- SCPRX used with SCP OUT1 with GCI mode but not used
- SCPCLK used with SCP IN2 with GCI mode

□ according to JP35 position
● according to JP33 position

MC145574A

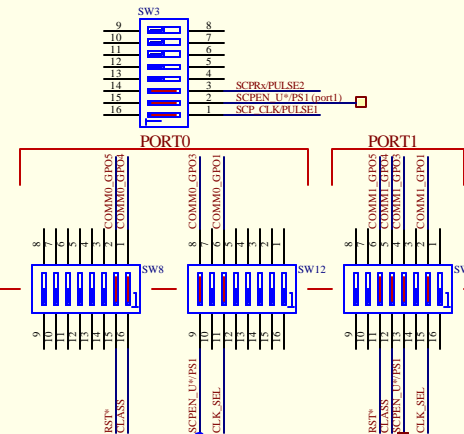


- M_S* : see MC145572
- RST* : see MC145572
- TE_NT* = 1 TE operating mode
TE_NT* = 0 NT operating mode
- TIN_TFSC_TCK_FIX :
FIX = 1 in NT modes : fixed timing mode
FIX = 0 in NT modes : adaptative timing mode
TIN in NT Terminal mode
TFSC in TE Slave mode
TCLK via SCP
HIGH-Z in TE Master mode
- SCPEN_GCIEN* : used in SCP
- SRX used with SCP S1 with GCI mode M1 with GCI Master mode
- SCK used with SCP S2 with GCI mode M2 with GCI Master mode

CLASS/ECHO_IN :

CLASS input in TE Master mode
ECHO_IN input in NT1 Star mode
In other modes: put to 0

MC145576



- RST* : see MC145572
- CLK_SEL=1 for DCL=2.048MHz
CLK_SEL=0 for DCL=512KHz
- CLASS in NT Terminal mode if held low CLASS=NR2(b0)
if NR2(b0)=0 CLASS input
- SCPEN_U* in IDL-SCP mode : =0 to select SCP
PS1 in GCI mode : =M4 maintenance bit
- SCPRx in IDL-SCP mode PULSE2* in GCI mode : SET TO 1 IN NORMAL MODE
- SCPCLK in IDL-SCP mode PULSE1* in GCI mode : SET TO 1 IN NORMAL MODE
- LED1 and LED2 label inverted on silkscreen

LED CONTROL COMBINATIONS (corrected)

GCI LED1	GCI LED2	INDICATION
Off	Off	Deactivated
Off	Flash (1 Hz)	Activation in progress or U-only activation
Flash (1 Hz)	On	U-interface active, S/T-interface not active (in progress or problem activating S/T)
Off	On	Fully active (S/T and U)
On	Off	Error indication (state before automatic reset)
On	On	Pulse test mode

Motorola Confidential Proprietary			Component values are provisional	
Title <i>EVB switches configuration for U and S/T DB</i>			MOTOROLA CTAS Division Europe	
Date: 26-Feb-2001	Author: Ewan BINEAU	Revision: 0.3	B	
File: C:\PROGRAM FILES\DESIGN EXPLORER 99\SE\EXAMPLES\SWITCH-1.D00				