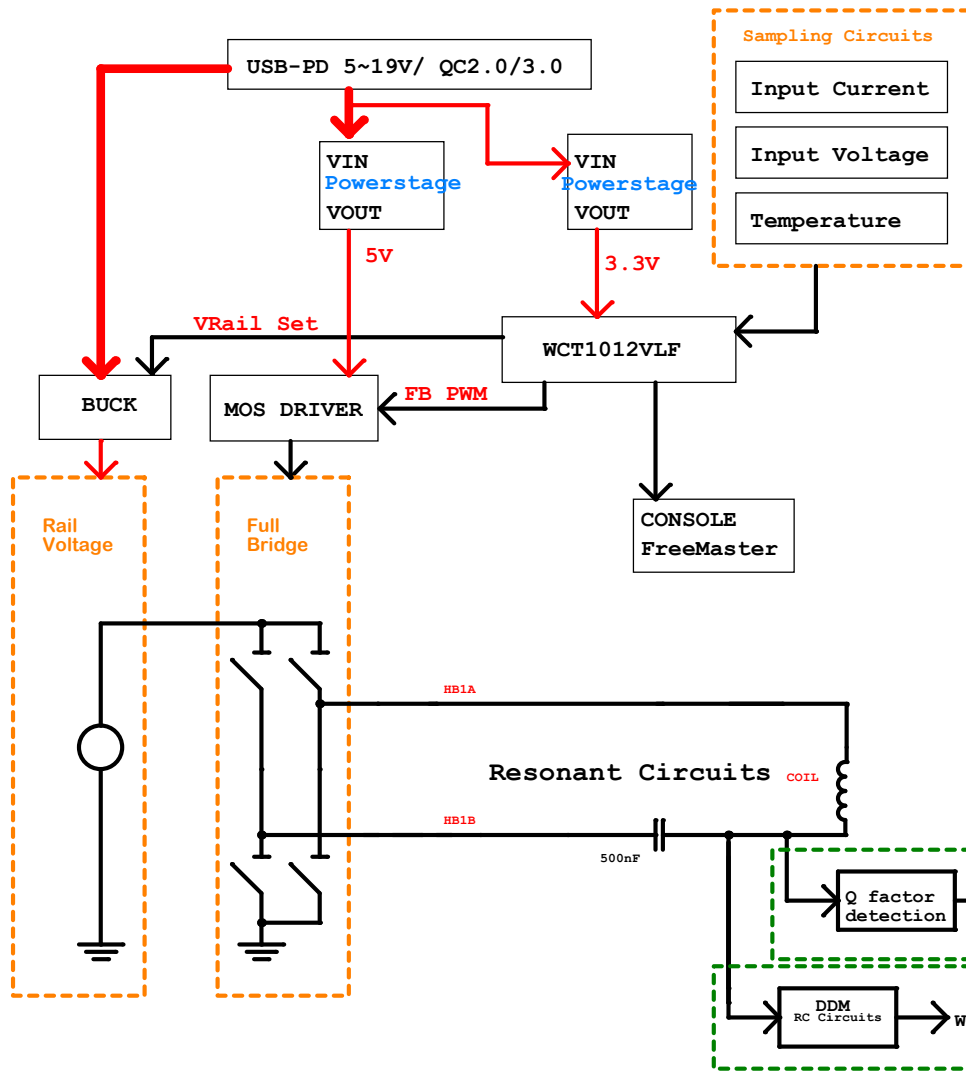


System Block Diagram

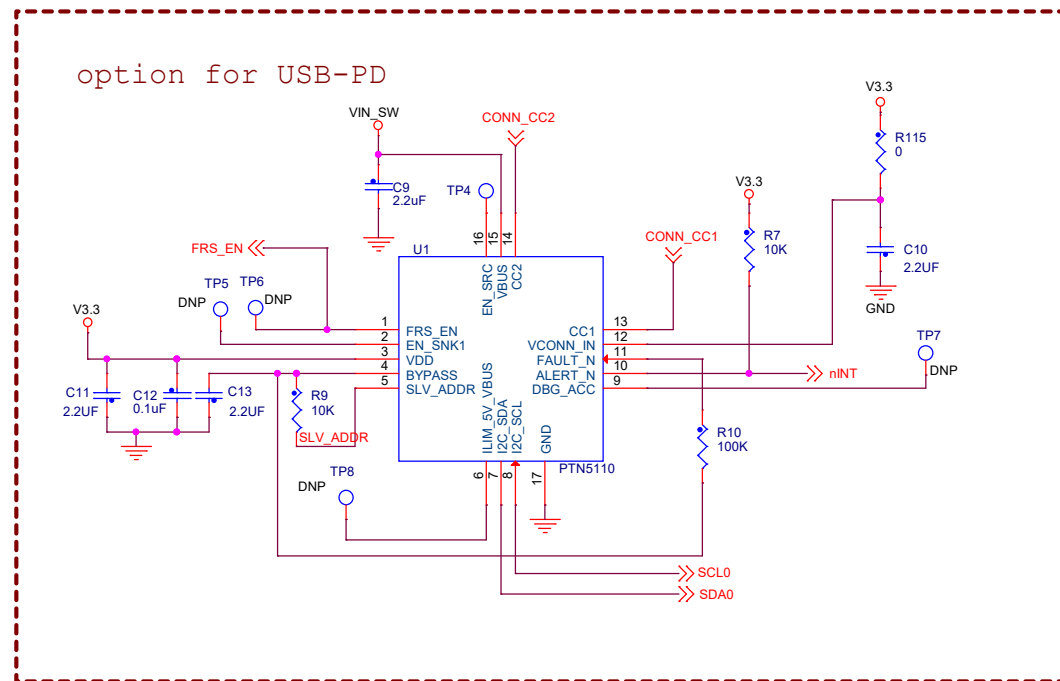
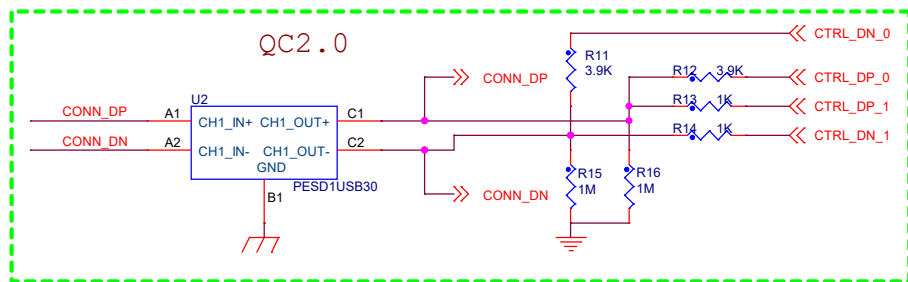
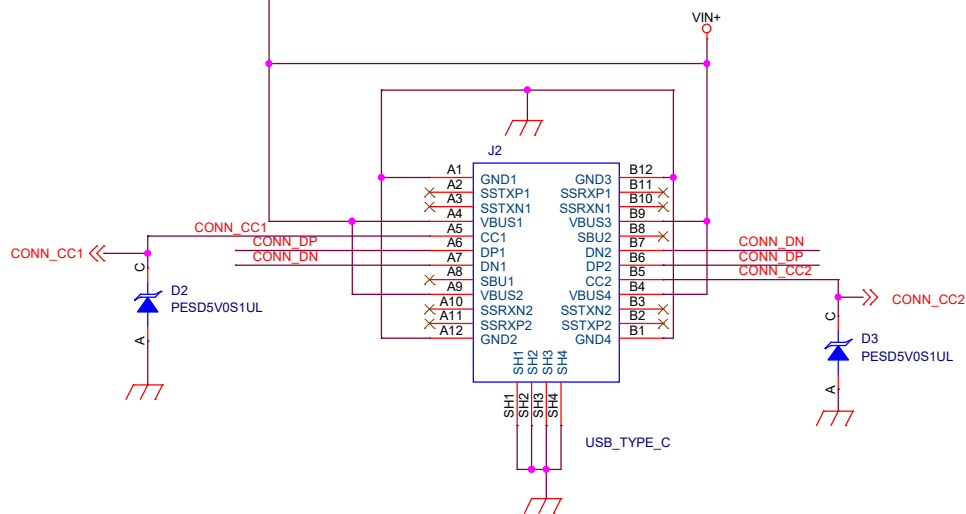
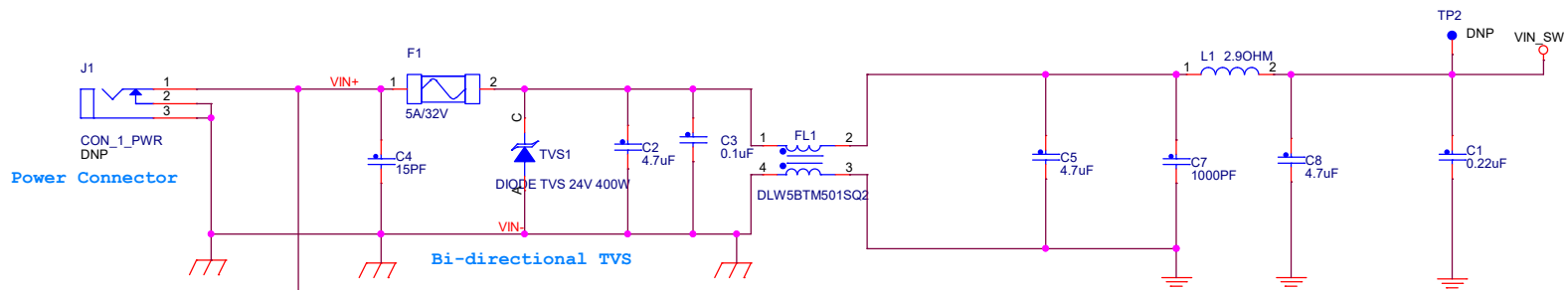


REVISION HISTORY

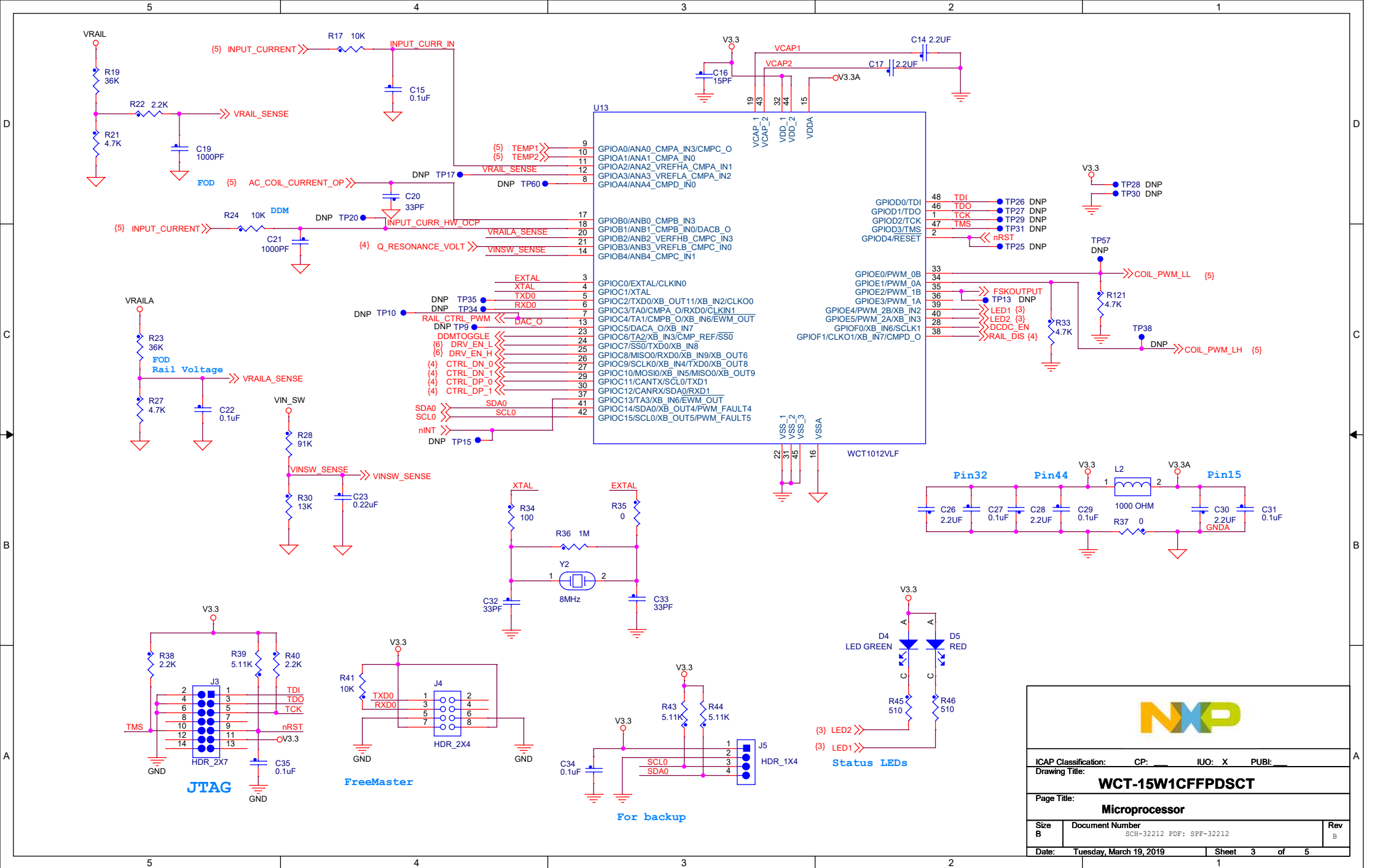
DATE	REV	CHANGE DESCRIPTION
04/11/2018	V0.9	- initial design
19/03/2019	B	SCT power stage application




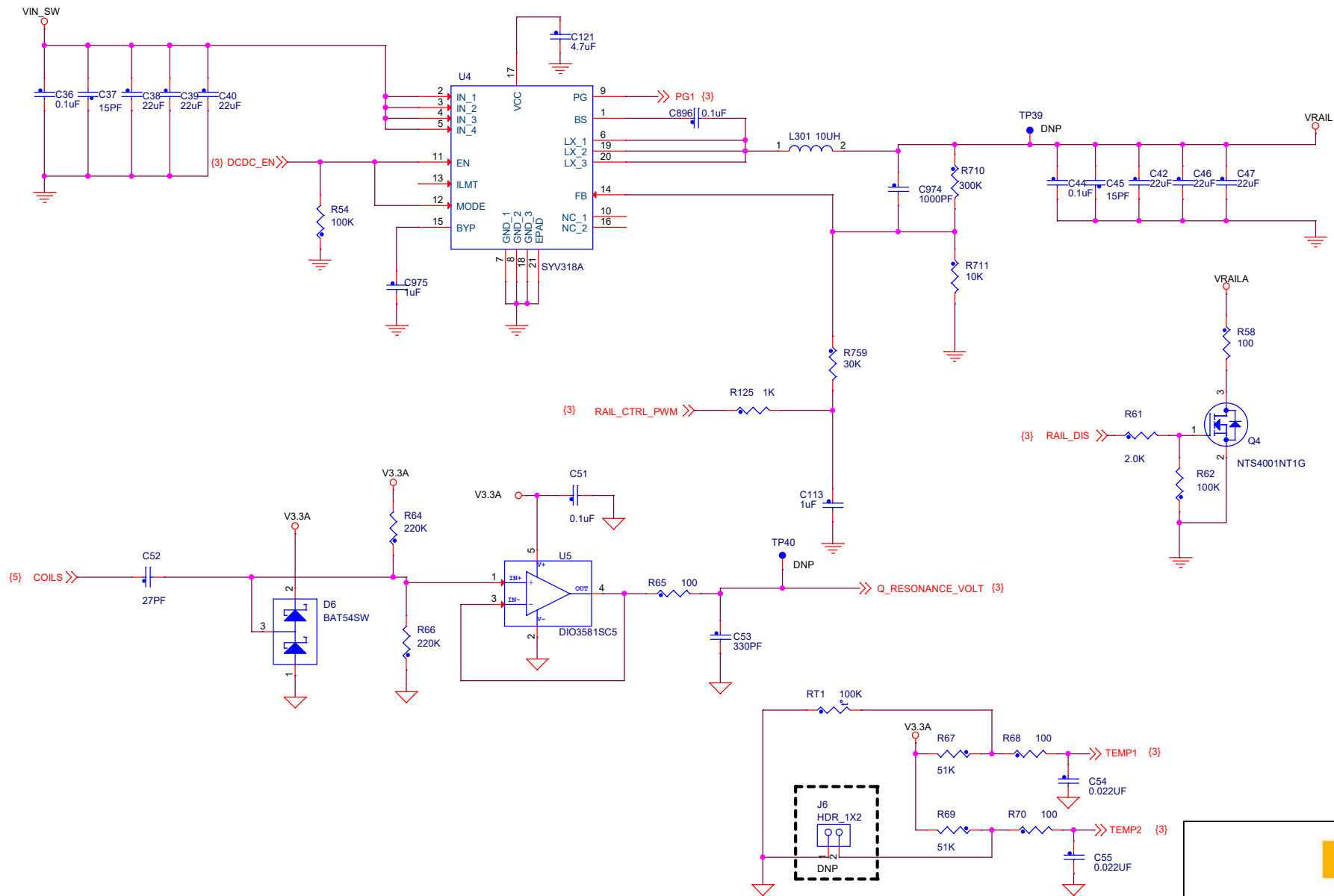
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Page Title: Revision History		
Size B	Document Number SCH-32212 PDF: SPF-32212	Rev B
Date: Tuesday, March 19, 2019	Sheet 1 of 5	



ICAP Classification:		CP:	IUO: X	PUBI:
Drawing Title:		WCT-15W1CFFPDSC		
Page Title:		Input Circuits		
Size B	Document Number	SCH-32212 PDF: SPF-32212		Rev B
Date:	Tuesday, March 19, 2019	Sheet	2 of 5	



			
ICAP Classification: CP: IJO: X PUBI:			
Drawing Title: WCT-15W1CFFPDSC2			
Page Title: Microprocessor			
Size B	Document Number SCH-32212 PDF: SPF-32212	Rev B	
Date: Tuesday, March 19, 2019	Sheet 3 of 5		



TX surface temperature sensing



ICAP Classification: CP: IUO: X PUBI:		
Drawing Title: WCT-15W1CFFPDSC7		
Page Title: Power Input VRail		
Size B	Document Number SCH-32212 PDF: SPF-32212	Rev B
Date: Wednesday, May 22, 2019	Sheet 4 of 5	

