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1	COVER SHEET
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3	BOARD CIRCUIT

REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	ORIGINAL RELEASE	02/25/04	DK
	B	UPDATES PER DVT	05/03/04	DK

D

D

C

C

B

B

A

A

DRAWING NO.
SPF-20289REV.
B

FREESCALE SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FREESCALE SEMICONDUCTOR DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN.



DESIGNED BY: Clifford Brown	DATE: 05/03/04
DRAWN BY: Daniel Kruzek	DATE: 05/03/04
PROJECT LEAD: Daniel Kruzek	DATE: 05/03/04
PRODUCT SPECIALIST: Clyde Lovelace	DATE: 05/03/04

6501 WILLIAM CANNON DRIVE AUSTIN, TEXAS 78735-8598 USA

TITLE:
 DEMO9S12NE64
 MC9S12NE64 Demo Board - SRC NO. SCH-20289

SIZE A	NAME: rev_b ROOT: rev_b	DRAWING NO. SPF-20289	REV. B
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Mon May 03 15:51:12 2004 SHEET 1 OF 3

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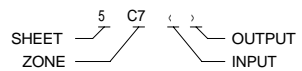
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NOTES:

- 1. UNLESS OTHERWISE SPECIFIED:
 ALL RESISTORS ARE IN OHMS, 5%, 1/8 WATT.
 ALL CAPACITORS ARE IN UF, 20%, 50V.
 ALL VOLTAGES ARE DC.
 ALL POLARIZED CAPACITORS ARE ALUMINUM ELECTROLYTIC.
- 2. INTERRUPTED LINES CODED WITH THE SAME LETTER OR LETTER COMBINATIONS ARE ELECTRICALLY CONNECTED.
- 3. DEVICE TYPE NUMBER IS FOR REFERENCE ONLY. THE NUMBER VARIES WITH THE MANUFACTURER.
- 4. SPECIAL SYMBOL USAGE:
 _B DENOTES - ACTIVE LOW SIGNAL.
 <> DENOTES - VECTORED SIGNALS.
- 5. INTERPRET DIAGRAM IN ACCORDANCE WITH AMERICAN NATIONAL STANDARDS INSTITUTE SPECIFICATIONS, CURRENT REVISION, WITH THE EXCEPTION OF LOGIC BLOCK SYMBOLOLOGY.
- 6. CODE FOR SHEET TO SHEET REFERENCES IS AS FOLLOWS:



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C

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A

DWG. NO.
SPF-20289

REV.
B

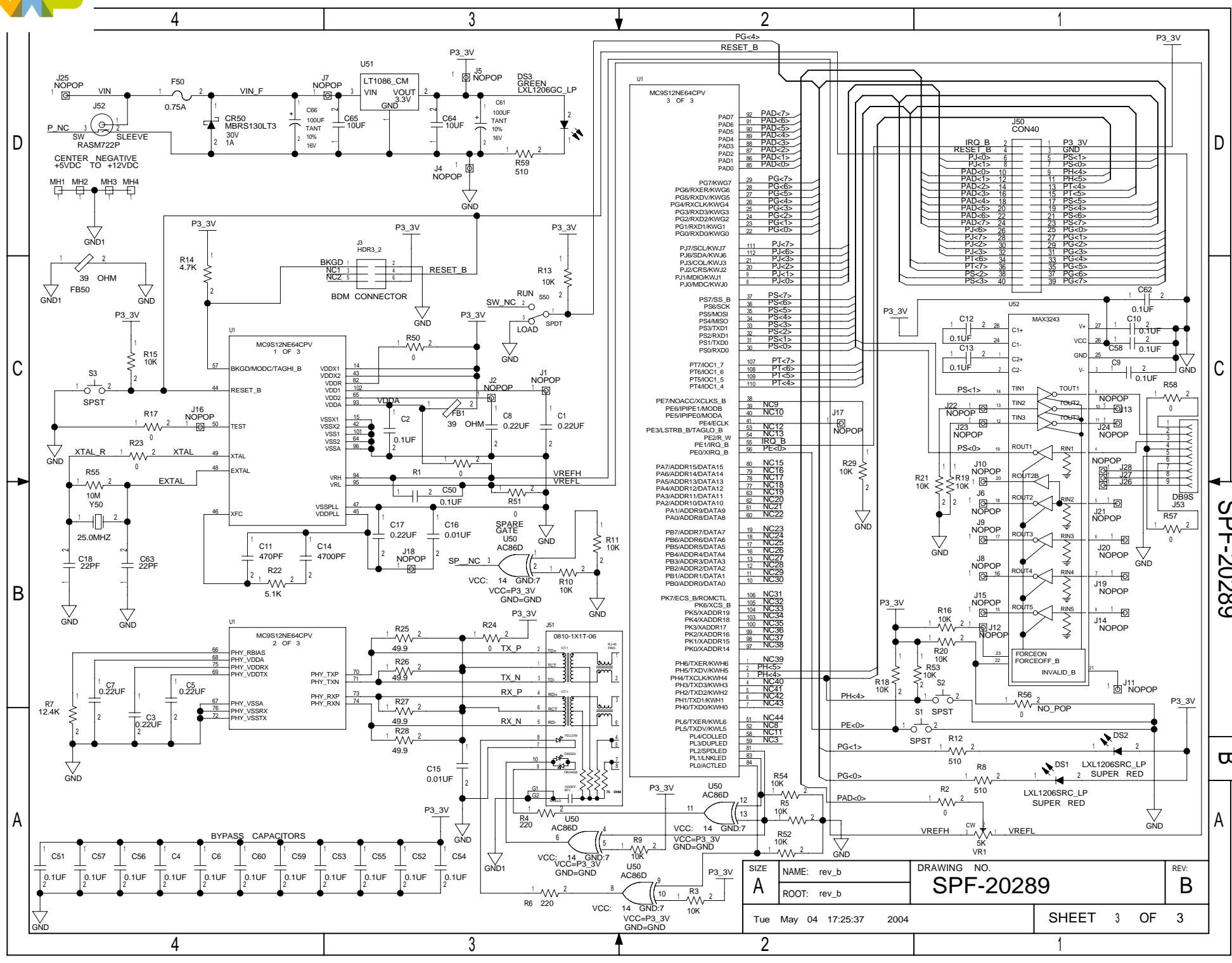
SIZE A	NAME: rev_b	DRAWING NO. SPF-20289	REV. B
	ROOT: rev_b		
Fri Mar 05 16:46:52 2004			SHEET 2 OF 3

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DWG. NO. SPF-20289

REV. B

SIZE A	NAME: rev_b ROOT: rev_b	DRAWING NO. SPF-20289	REV. B
Tue May 04 17:25:37 2004		SHEET 3 OF 3	