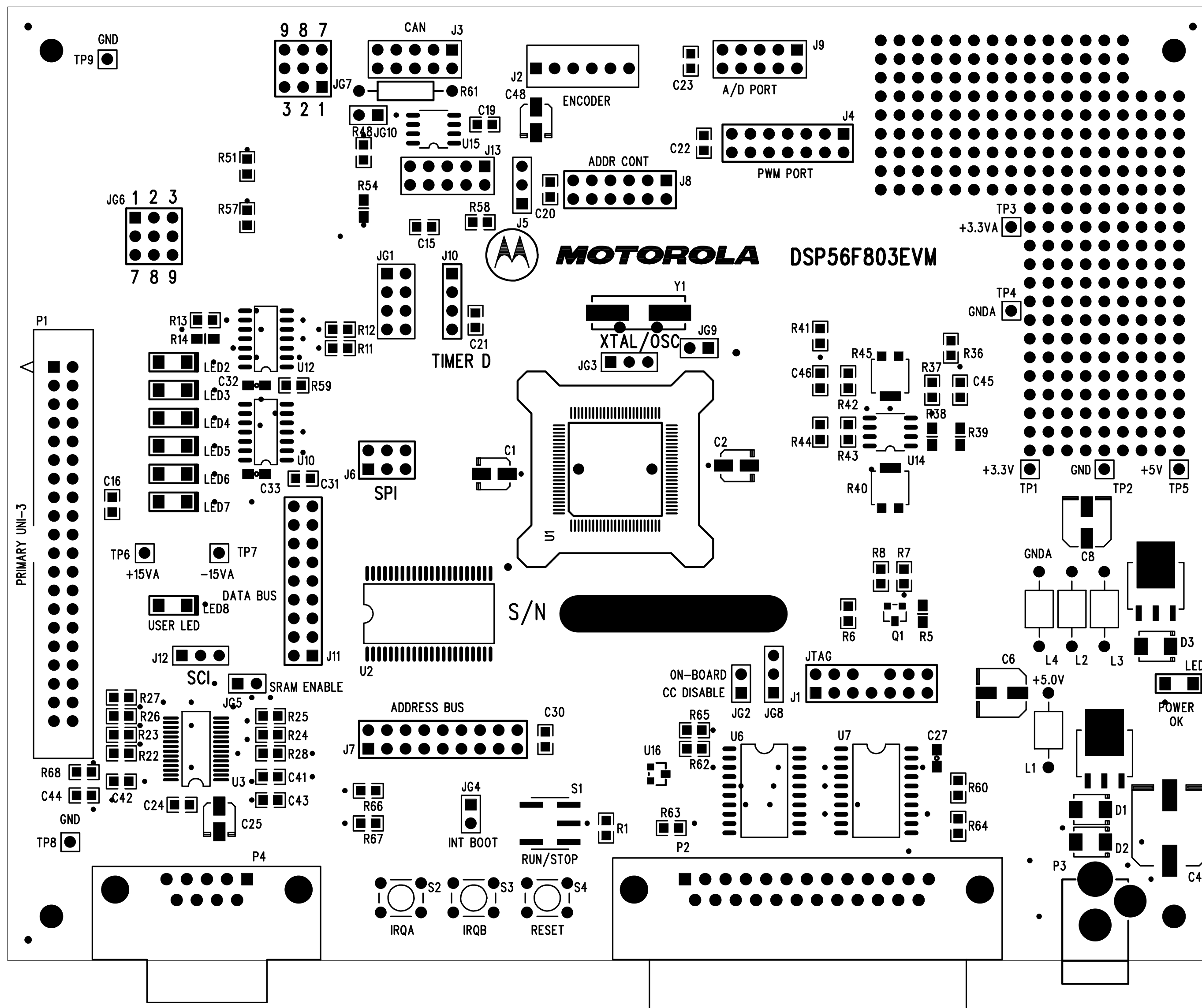
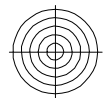


1. MINIMUM CLEARANCE ON THE BOARD SHALL BE .007 INCH.
2. INNER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1 OZ. OUTER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1.5 OZ AFTER PLATING.
3. MINIMUM THICKNESS OF COPPER ON THE WALL OF A PLATED HOLE SHALL BE .001 INCH.
4. HOLE SIZE TOLERANCES SHALL BE:
PLATED THRU HOLES =.015 SHALL BE +/- .005 INCH.
PLATED THRU HOLES =.020 SHALL BE +/- .005 INCH.
PLATED THRU HOLES >.020 SHALL BE +/- .003 INCH.
UNPLATED THRU HOLES SHALL BE +/- .002 INCH.
5. SOLDER MASK ON BOTH SIDES OF THE BOARD BE S.M.O.B.C, LPI, COLOR GREEN.
6. ALL MATERIAL SHALL BE NEMA GRADE FR-4 OR EQUIVALENT.
7. TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES.
8. DRC'S MUST BE RAN ON THE GERBERS BEFORE BUILDING BOARD UNLESS PRIOR APPROVAL IS GIVEN IN WRITING.
9. SILKSCREEN BOTH SIDES WITH NON-CONDUCTIVE EPOXY-BASED WHITE INK.
10. MANUFACTURE TO IPC-A-600D SPECIFICATIONS.
11. MINIMUM ANNULAR RING SHALL BE .001".
12. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE .003".
13. BOW AND TWIST SHALL NOT EXCEED .010" PER INCH.

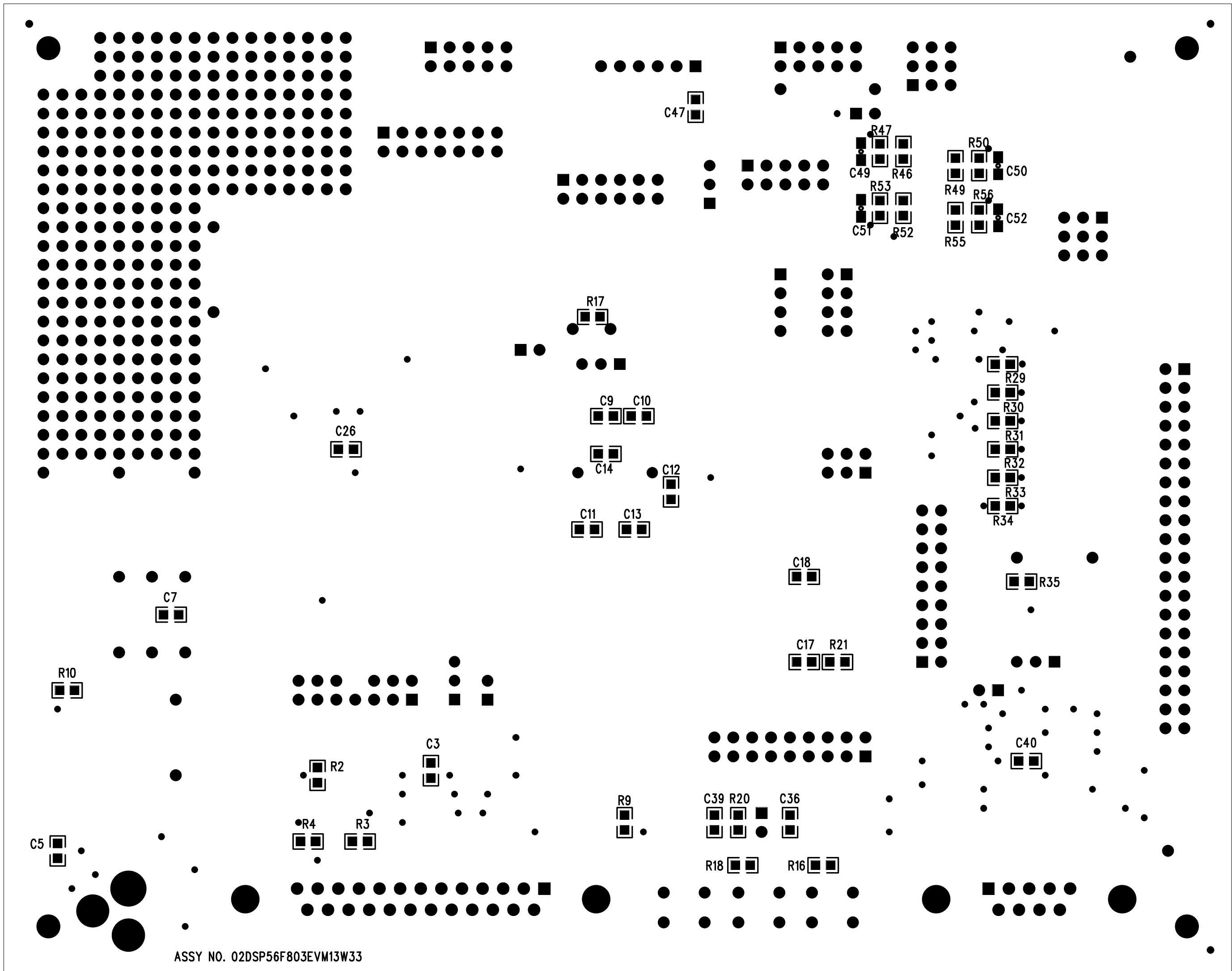
TOP SILKSCREEN	FILE NAMES
TOP SOLDER MASK	FROM ZIP FILE
COMPONENT	TDMOT030-TOPSILK.PHO
GROUND PLANE	TDMOT030-TOPMASK.PHO
POWER PLANE	TDMOT030-L1.PHO
SOLDER	TDMOT030-L2.PHO
BOTTOM SOLDER MASK	TDMOT030-L3.PHO
BOTTOM SILKSCREEN	TDMOT030-L4.PHO
	TDMOT030-BOTMASK.PHO
	TDMOT030-BOTSILK.PHO
LAYER SCHEDULE	
SCALE: NONE 7	

SIZE	QTY	SYM	PLTD
15	428	+	PLTD
40	512	×	PLTD
43	34	□	PLTD
120	6	◇	PLTD
140	1	⊗	PLTD
59	2	⊗	NPLTD
125	4	A	NPLTD

		MATERIAL:	UNLESS OTHERWISE SPECIFIED			THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION AND SHALL NOT BE DUPLICATED OR USED FOR ANY PURPOSE OTHER THAN THAT FOR WHICH PROVIDED OR DISCLOSED IN WHOLE OR IN PART, WITHOUT WRITTEN CONSENT.			ARTWORK DESIGNED BY:		
			DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH DIMENSIONS IN BRACKETS [] ARE IN MILLIMETERS INTERPRET DRAWING PER MIL-D-1000						TRAX Design L.C. 9500 W. PARMER LANE, #721 AUSTIN, TEXAS 78717 (512) 733-5075		
			TOLERANCES						FABRICATION DRAWING, DSP56F803EVM		
			HOLE TOLERANCES PER 78027								
		FINISH:	DECIMALS	ANGLES	SURFACES	ENGINEER					
			.XX +/-	+/-	<div><div></div><div>✓</div></div> <div>MICROINCHES</div>	CHECKER					
			.XXX +/-			DRAFTSMAN	JK	08-20-02			
			PART TO BE FREE OF BURRS								
			BREAK EDGES	BEND RADIUS	BEND RELIEF						
			<div><div></div><div>MAX</div></div>	<div><div></div><div>MAX</div></div>	<div><div></div><div>MAX</div></div>	DO NOT SCALE DRAWING					
NEXT ASSY	USED ON										



ARTWORK DESIGNED BY:		ARTWORK FOR:		
TRAX Design L.C.		Motorola		
9500 W. PARMER LANE, #721		BOARD NAME:	JOB #:	DATE:
AUSTIN, TEXAS 78717 (512) 733-5075		DSP56F803EVM	TDMOT030	08-20-02



BOTTOM ASSEMBLY



ARTWORK DESIGNED BY: Motorola			ARTWORK DESIGNED BY: TRAX Design	
BOARD NAME: DSP56F803EVM			9500 W. PARKER LANE, #751	
JOB #: TDMOT030			AUSTIN, TEXAS 78717 (512) 733-5025	
DATE: 08-20-02				