


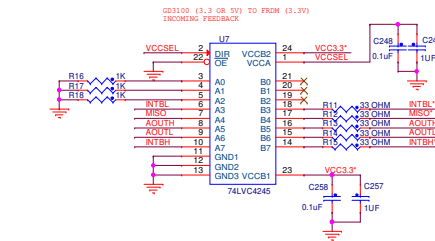
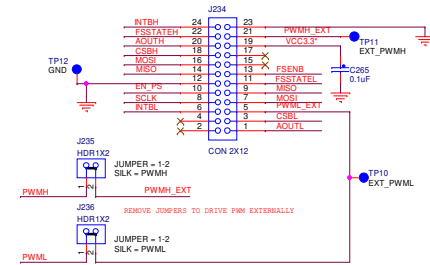
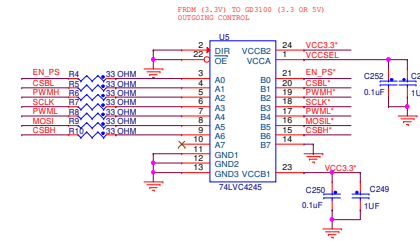
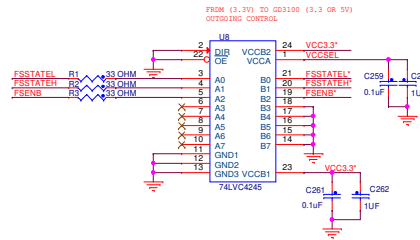
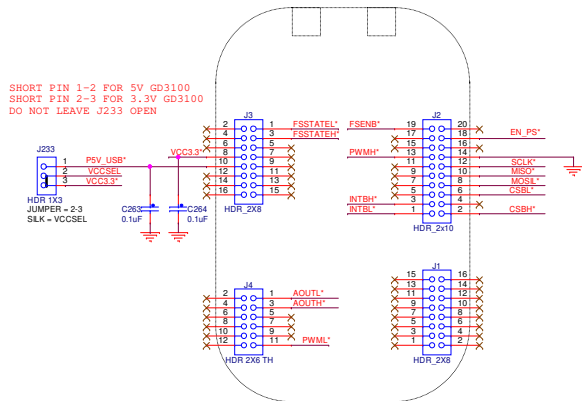
Table of Contents	
1	Title
2	Translator

Revisions		
Rev	Description	Date
B	Release	07-11-17
EX1	Implemented changes	12-06-18
	Changed J235, J236, J233 to Wurth parts	12-07-18
	Added dumping resistors	12-17-18
	Added decoupling caps on power pins of connectors	12-18-18
	Unused driver translator inputs connected to GND	12-18-18
C	Release	12-20-18

KITGD3100TREV B

		Automotive, Industrial & Multi-market Solutions Group	
		6501 Williams Cannon Drive West Austin, TX 78725-6598	
<small>This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.</small>			
Designer: Travis Alexander		Drawing Title: KITGD3100TREV B	
Drawn by: Travis Alexander		Page Title: Title	
Approved: Pierre Colinas	Size: C	Document Number: SCH-29380 PDF: SPF-29380	Rev: C
Date: Thursday, December 20, 2018		Sheet 1 of 2	

REF DES	JUMPER	PAGE NAME
J236, J235	1-2	2. Translator Board
J233	2-3	2. Translator Board



QAP Classification: CP IUC: X PUB:

Drawing Title: **KITGD3100TREV B**

Page Title: **Translator**

Size Document Number SCH-29380 PDF: SPF-29380 Rev C

Date: Thursday, December 20, 2018 Sheet 2 of 2