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	Openocm INTERFACE
5	I/O Headers and Power Supply

Revisions			
Rev	Description	Date	Approved
X1	Initial Draft	08/20/12	L. PUEBLA
A	Prototype Release	08/28/12	L. PUEBLA
B	Production Release	11/21/12	L. PUEBLA
C	Respin Release	01/21/13	L. PUEBLA
C1	D9 pop fix	01/29/13	L. PUEBLA

FRDM-K20D50M

Automotive, Industrial & Multi-Market Solutions Group
6501 William Cannon Drive West Austin, TX 78735-6598

ICAP Classification: FCP: FLK2: PUB: X

Designer: RAFAEL DEL REY

Drawing Title: **FRDM-K20D50M**

Drawn by: RAFAEL DEL REY

Page Title: **TITLE PAGE**

Approved: LUIS PUEBLA

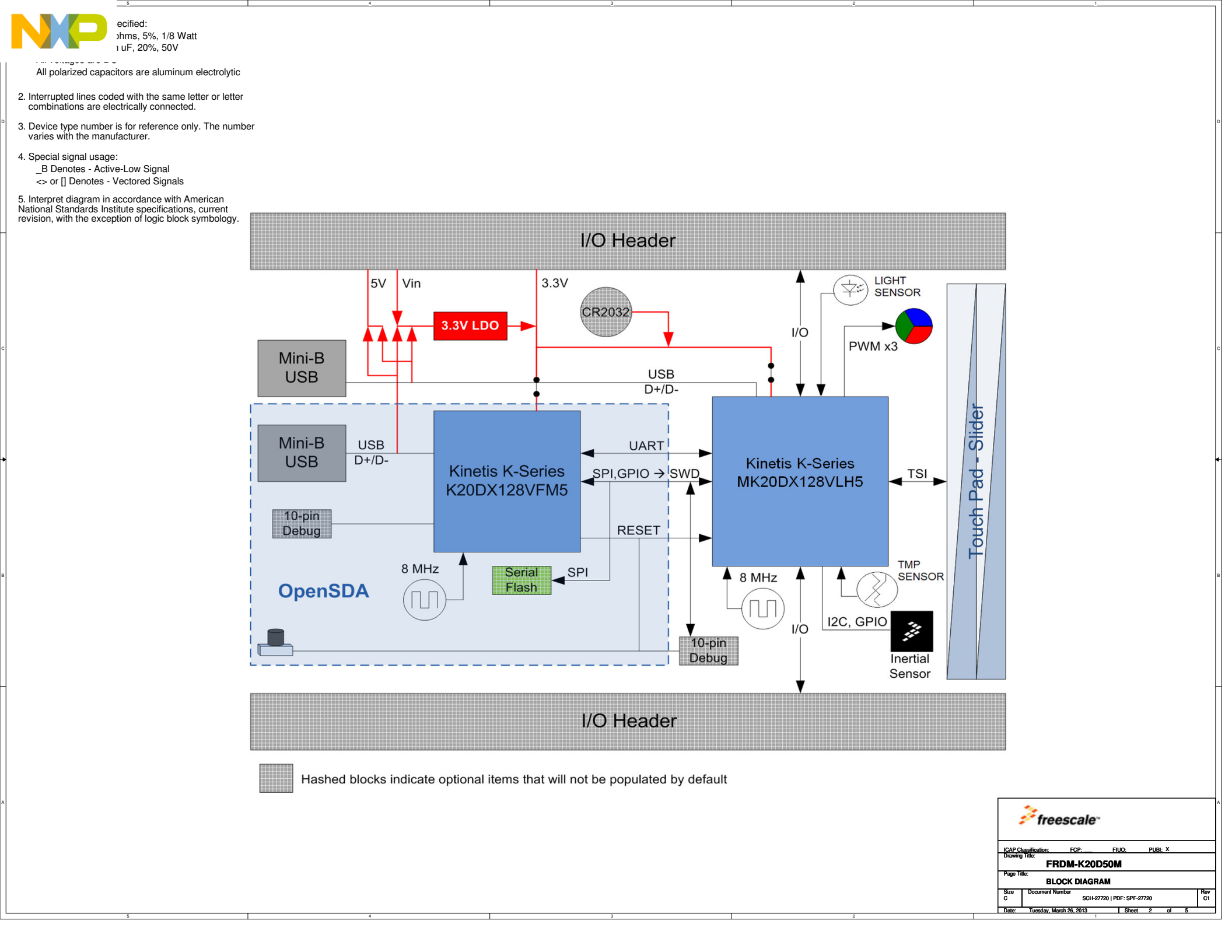
Size C

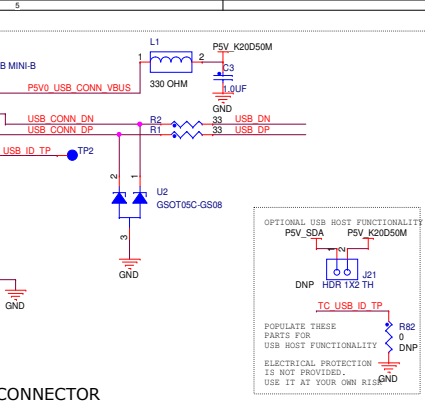
Document Number SCH-27720 | PDF: SPF-27720

Rev C1

Date: Tuesday, March 26, 2013

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OPTIONAL USB HOST FUNCTIONLITY

PSV_SDA P5V K20D50M

DNP HDR TX2 TH

J21

TC USB ID TP

R82 0 DNP

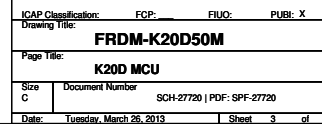
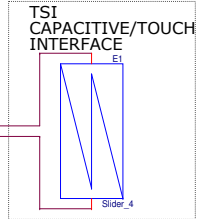
GND

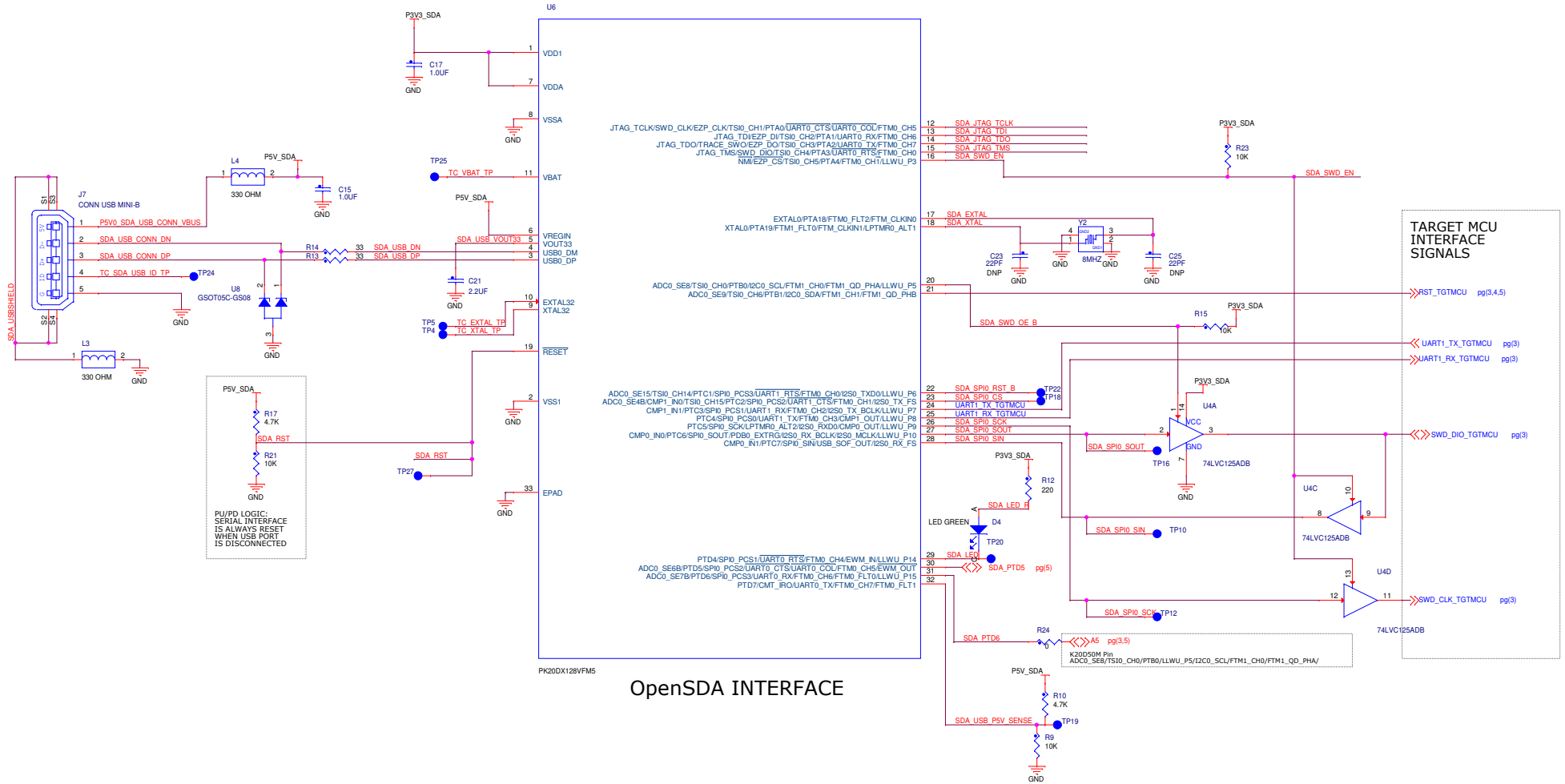
POPULATE THESE
PARTS FOR
USB HOST FUNCTIONLITY

ELECTRICAL PROTECTION
IS NOT PROVIDED.

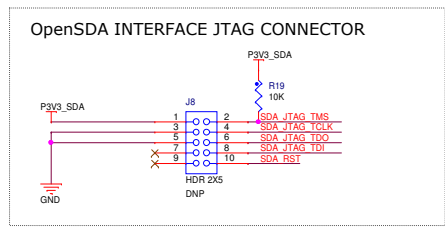
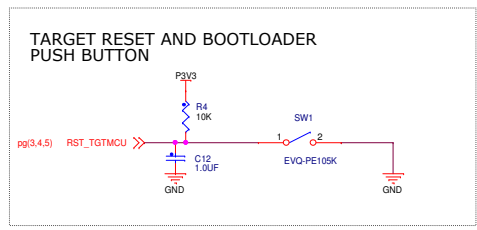
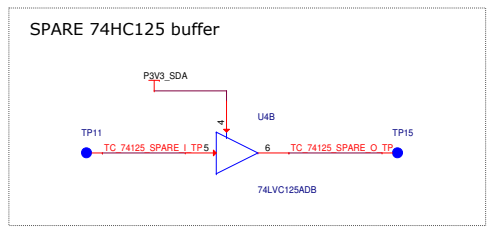
USE IT AT YOUR OWN RISK.

A circuit diagram of a power plane for the P3V3_K20D50M. It shows a horizontal line representing the power plane with six decoupling capacitors connected to ground (GND) at various points. The capacitors are labeled C5, C6, C14, C13, C11, and C10 from left to right. Their values are 1.0UF, 1.0UF, 1.0UF, 0.1UF, 0.1UF, and 0.1UF respectively. The ground connection is shown as a red symbol labeled GND.





OpenSDA INTERFACE



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OpenSDA interface			
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