
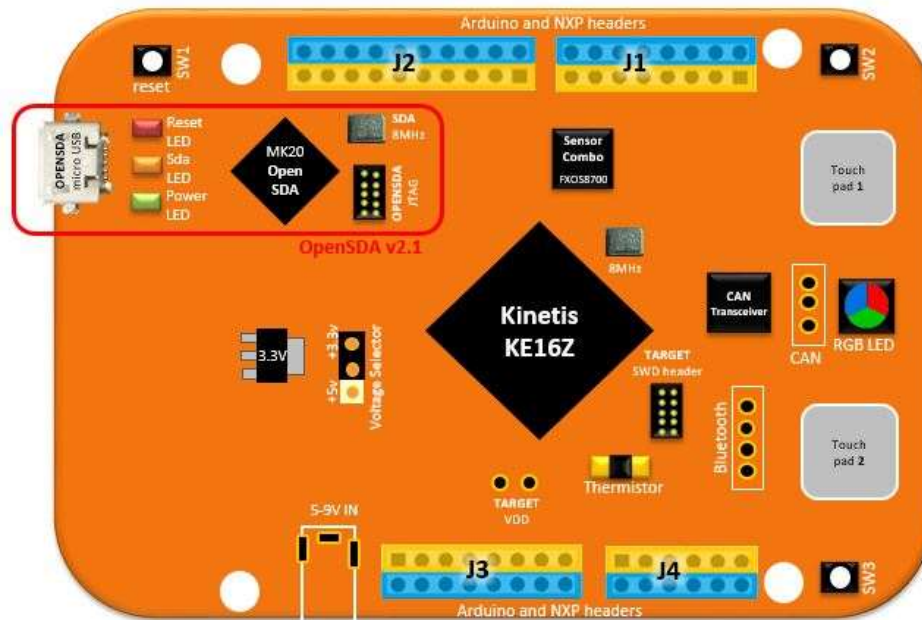


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Revisions & Change Log			
Rev	Description	Date	Approved
X1	Initial Draft	5/25/2015	

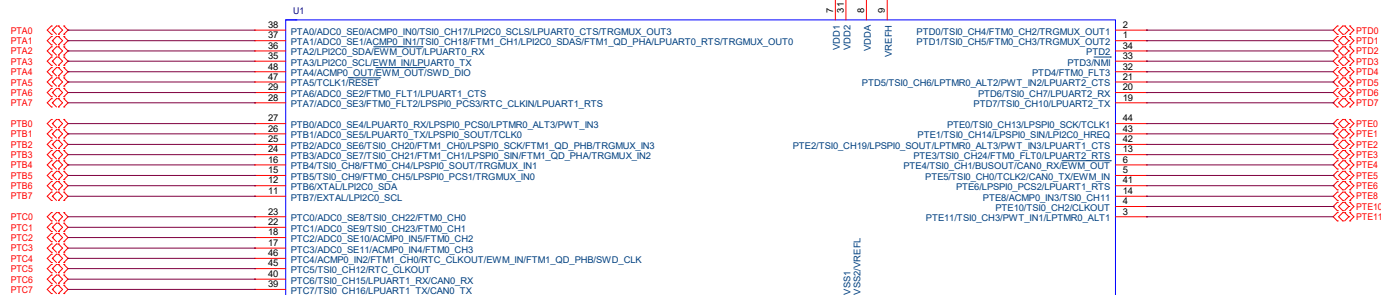
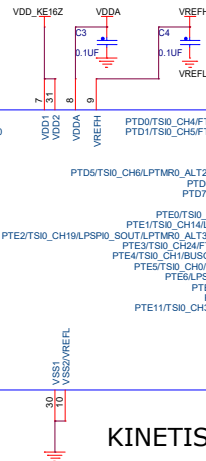
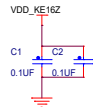
FREEDOM KE16Z

		Microcontroller Product Group 6501 William Cannon Drive West Austin, TX 78755-5500	
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Designer:		Drawing Title: FRDM-KE16Z	
Drawn by:		Page Title: TITLE PAGE	
Approved:		Size C	Document Number SCH-34744 PDF: SPF-34744
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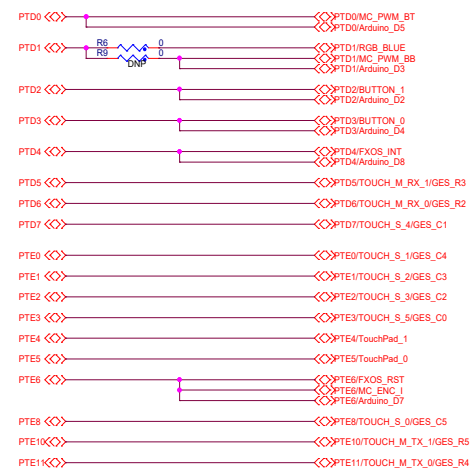
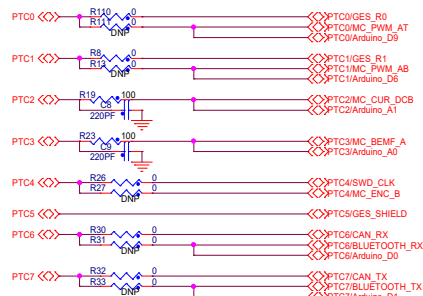
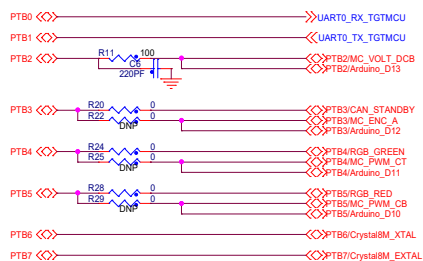
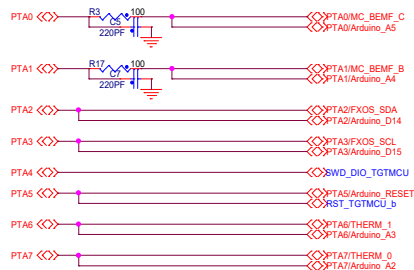


ICAP Classification: CP: IUC: X PUB:		
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Page Title: BLOCK DIAGRAM		
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CAD NOTE:
Please place these capacitors
near their respective CPU pin
(VDD to VSS)

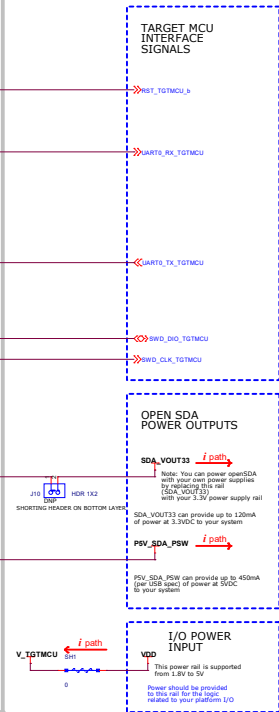
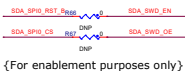
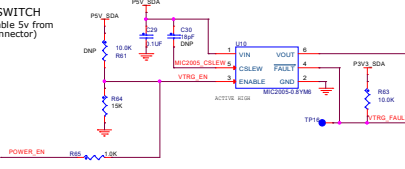
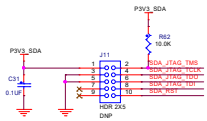
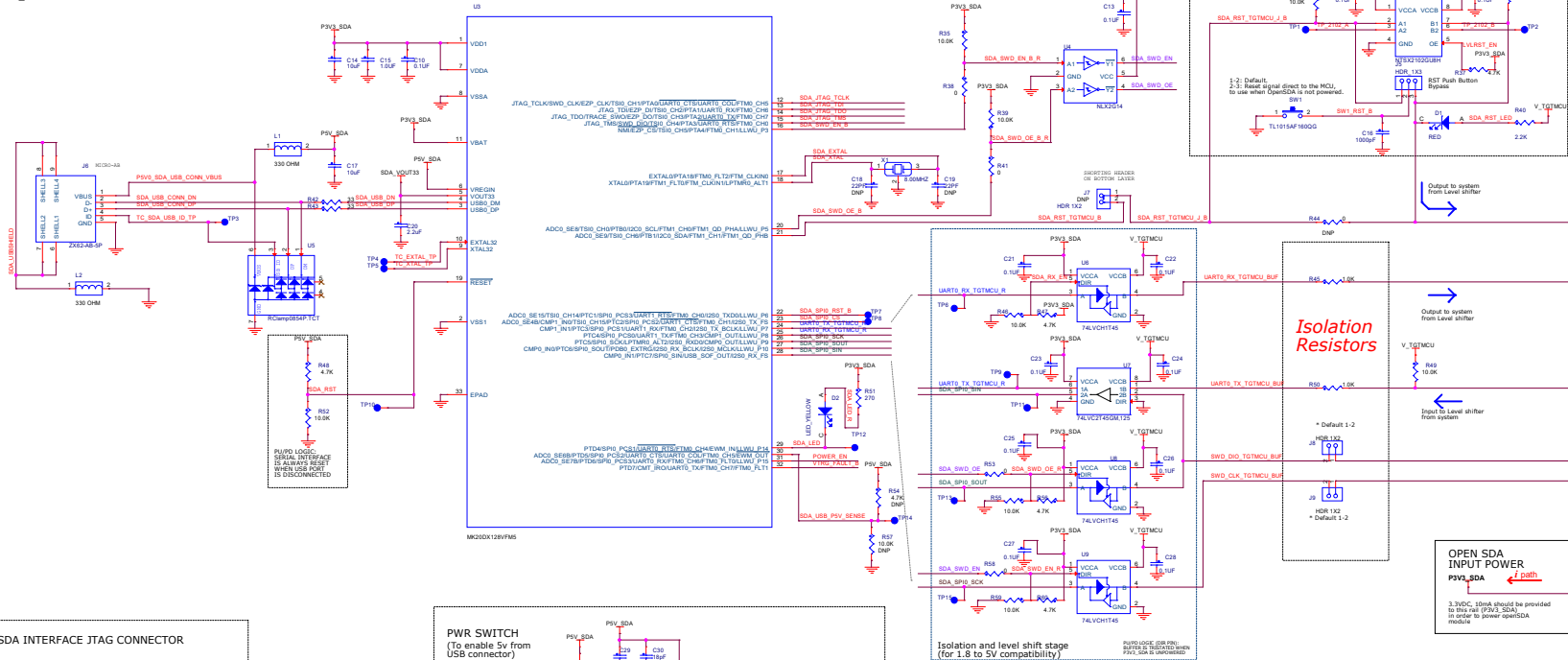


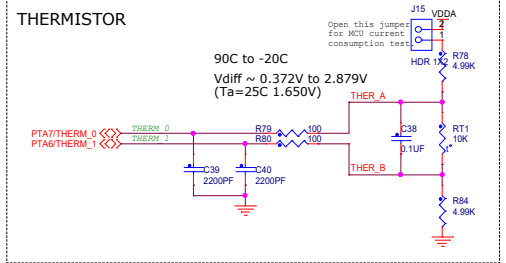
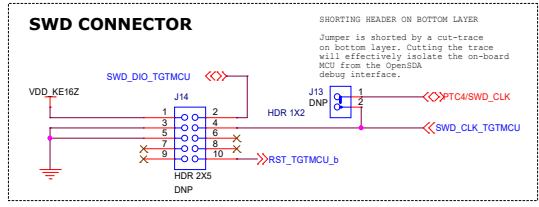
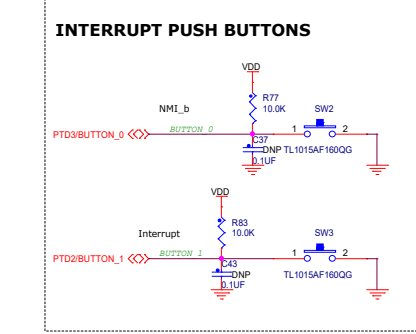
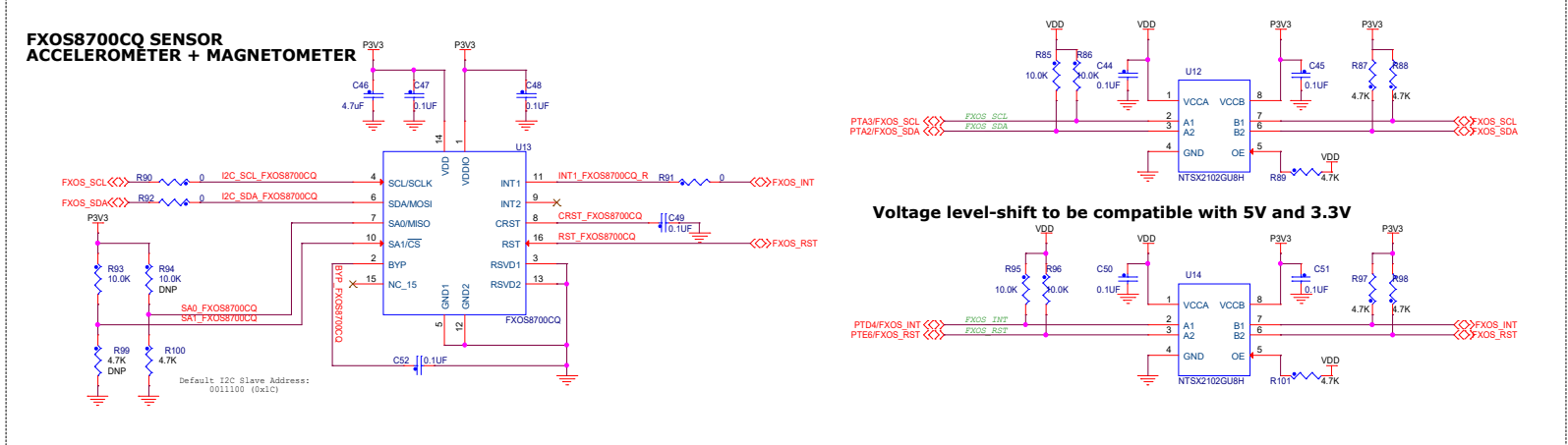
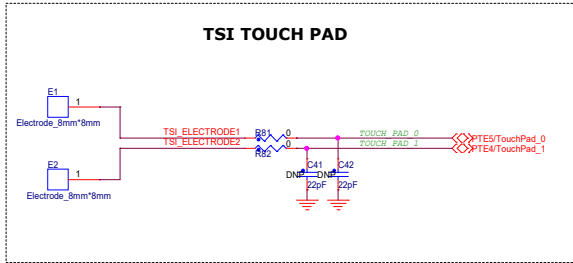
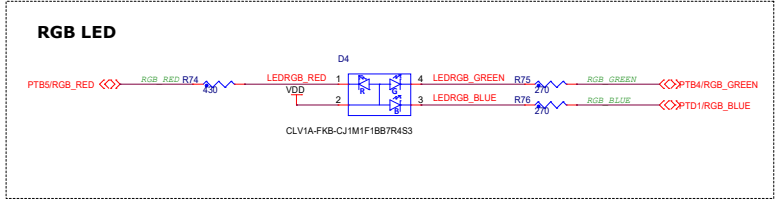
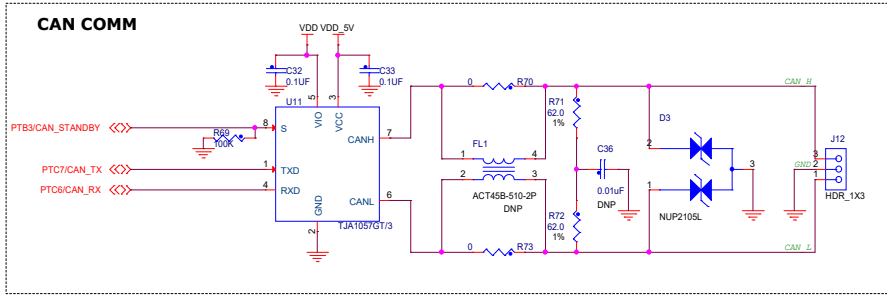
KINETIS KE16Z MCU

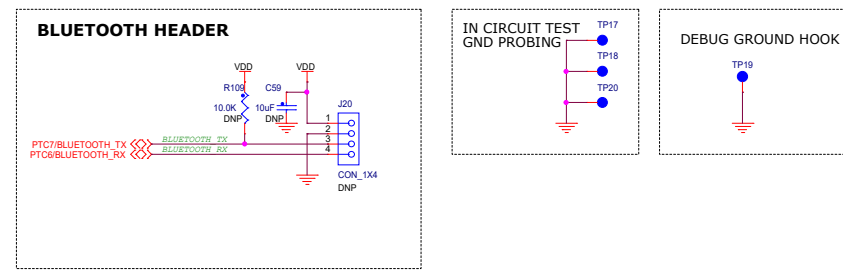
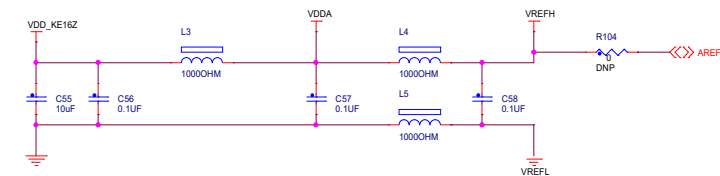



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OpenSDA Interface







			
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