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**Revisions**

Rev	Description	Date
A	Original Release	9-29-16
A1	DNP J1, J2	11-18-16

# FRDMSTBC-A8451



**Analog Sensor Product Group**  
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Austin, TX 78735-8688

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ICAP Classification: CP: ILO: X PUBL:

Designer: Aswin S	Drawing Title: <b>FRDMSTBC-A8451</b>		
Drawn by: Aswin S	Page Title: <b>TITLE PAGE</b>		
Approved: Team	Size C	Document Number SCH-29413 PDF: SPF-29413	Rev A1
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1. Unless Otherwise Specified:

All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.

All capacitors are in uF, some are 10% or 20%

All voltages are DC

All polarized capacitors are tantalum

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Use resistors R2 or R4 for selecting 7 bit I2C address between 1D and 1C respectively.

4. Resistors R5/R6 and R7/R8 can be used if INT1 and/or INT2 is used under open-drain/open-source configuration respectively.

5. Populate J7 and J8 for I2C Communication

6. Device type number is for reference only. The number varies with the manufacturer.

7. Special signal usage:

\_B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

8. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification: GP: I/O: X PUBL:

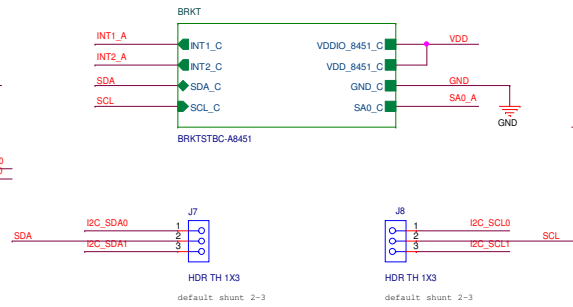
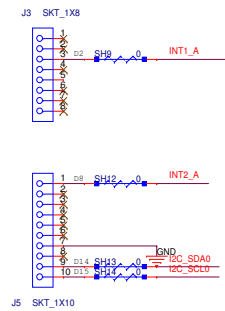
Drawing Title: **FRDMSTBC-A8451**

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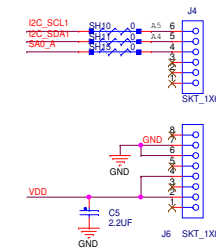
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SHIELD BOARD ARDUINO HEADER

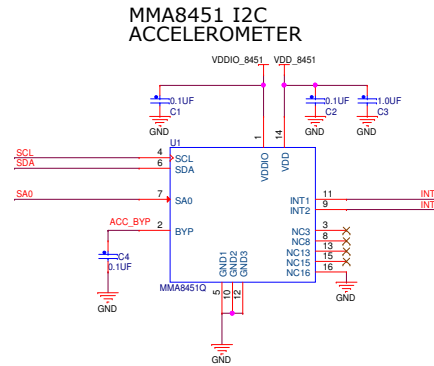
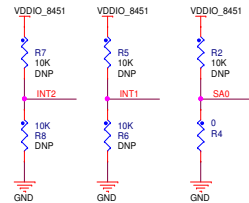
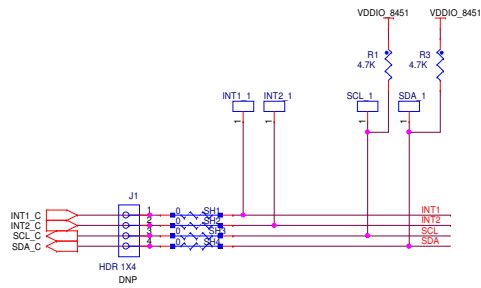


SHIELD BOARD ARDUINO HEADER

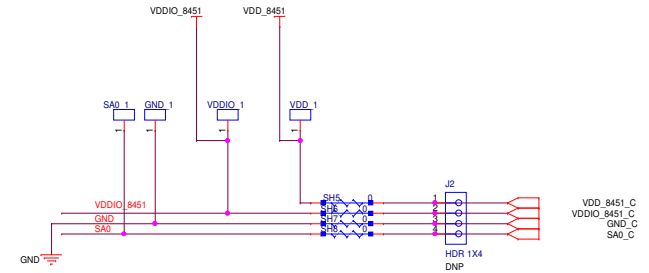



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EDGE OF BRKOUT BOARD



I2C address : 0x1C



			
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