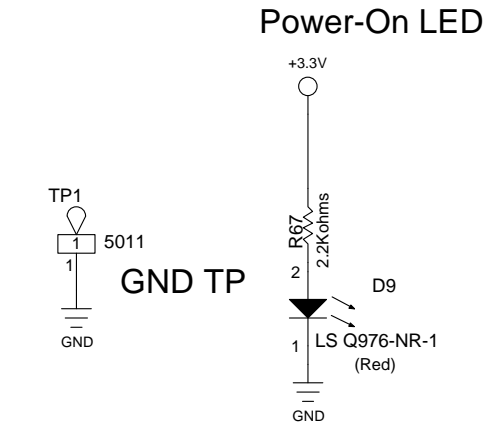
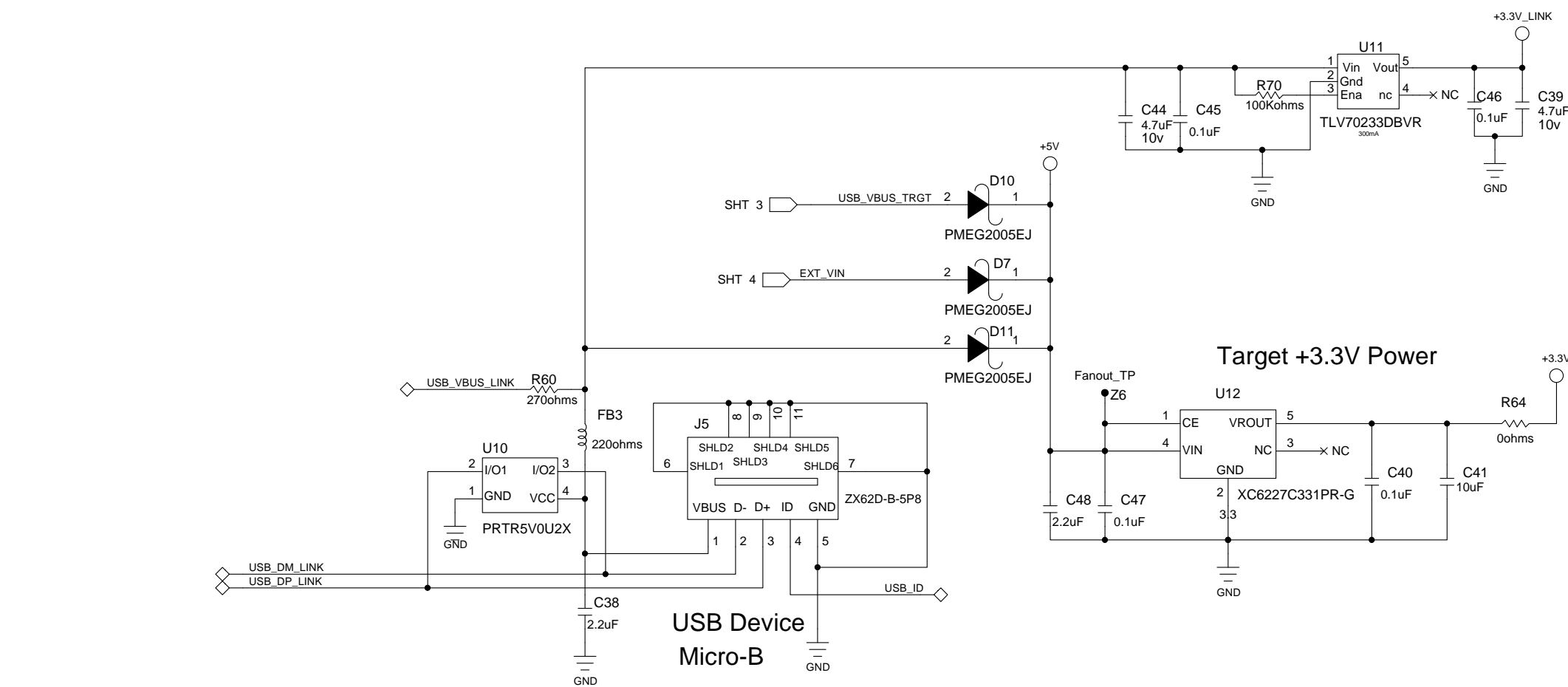
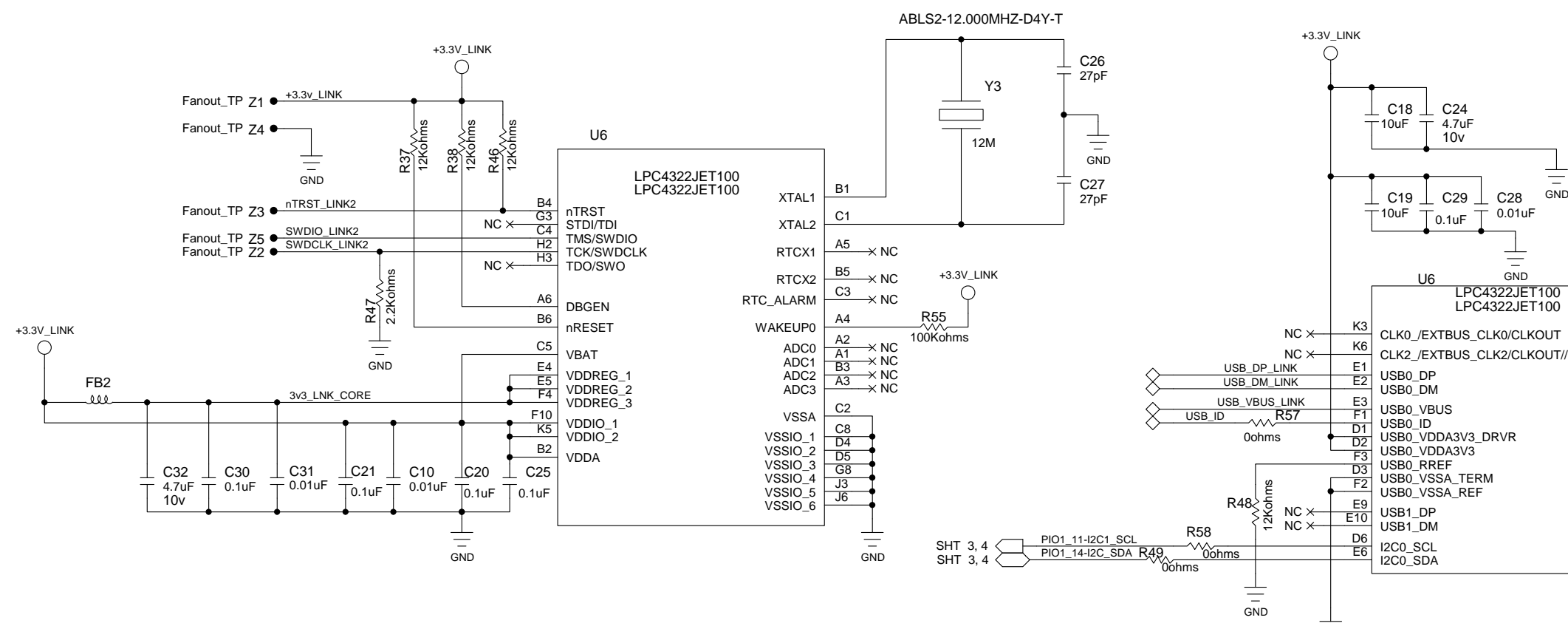
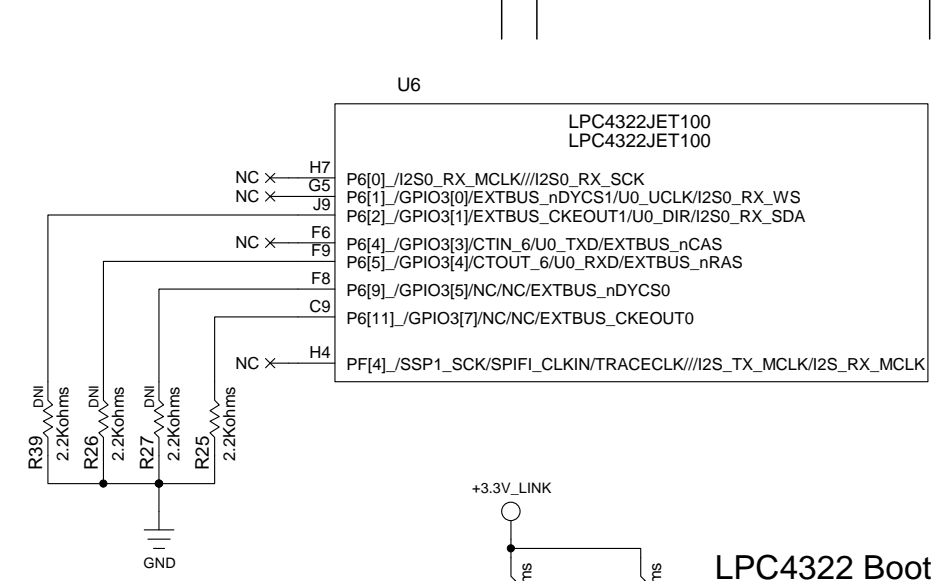
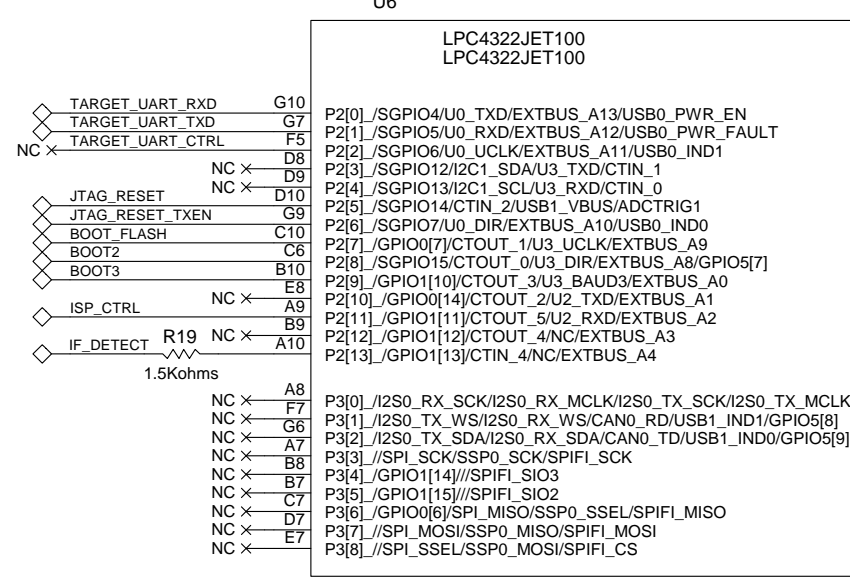
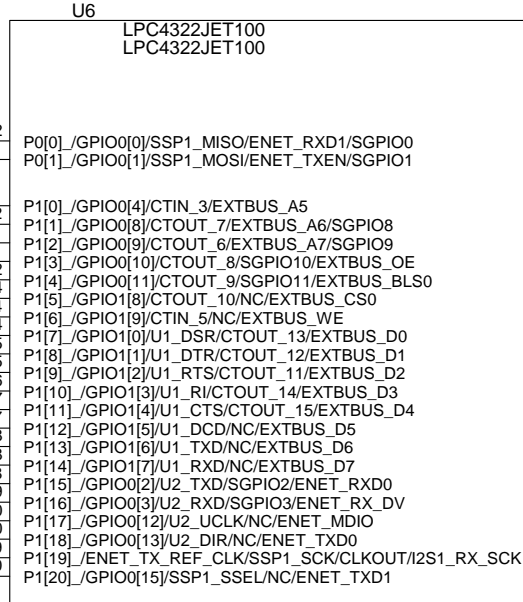


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
B	- Release to production.	10/23/2013	
C	- Change Link2 LDO to TLV70233. - Add PU to Link2 WAKEUP0. - 2.2k PU to Pmod I2C1 - add ESD protection at SWD - change USB conn to ZX62D-B-5PA8 - Xpresso / Arduino use Uart4 - Add I2C & SPI Link2 bridge interface - Add R61 PD to P1_2 for Rev C ID - Arduino A4/A5 now analog/dig pin. - Add R69, P3, P4 for current measurement.	07/30/2014	



CONTRACT NO.		LINK2 LPC4322	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	7/30/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE D	DWG. NO. Xpresso-LPC11U68
ISSUED	07/30/13	SCALE	REV C
		SHEET 1 OF 04	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



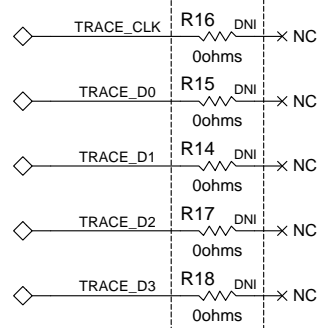
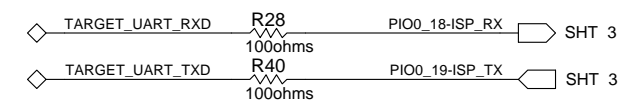
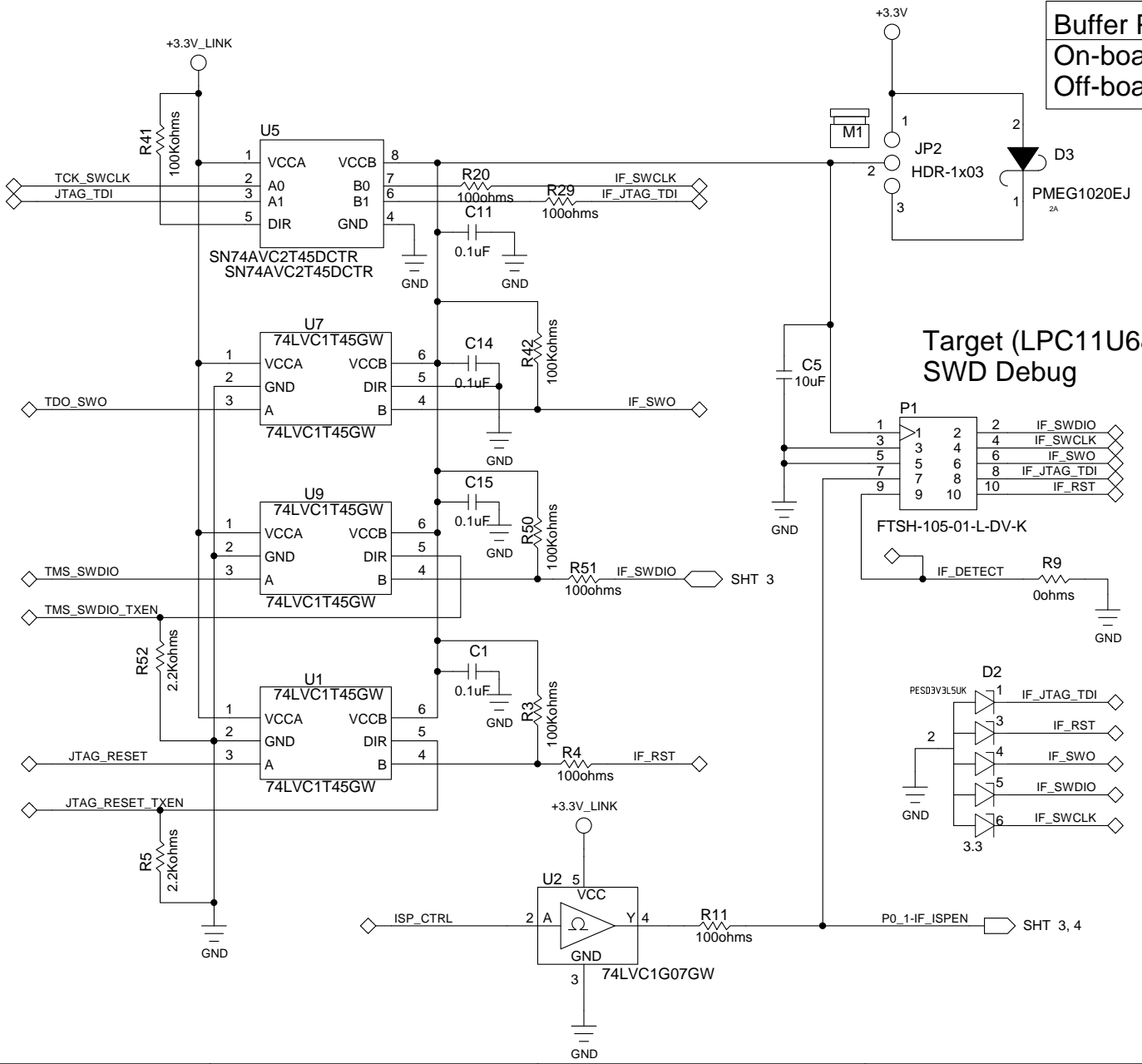
**LPC4322 Boot mode**  
DFU USB0 = B3:0 = 0101

**Buffer Pwr Select (JP2)**  
On-board Target 1 - 2 (default)  
Off-board Target 2 - 3

Short JP3 to force DFU boot  
(when internal flash is programmed)

**Target to debug location (JP1)**  
On-board Target - open (default)  
Off-board Target - short

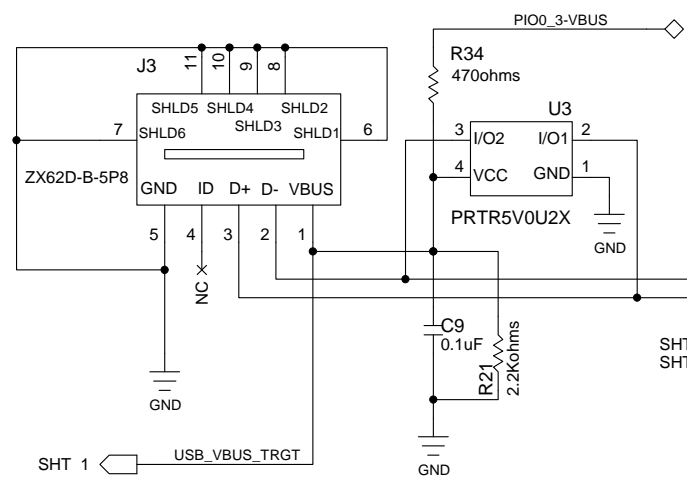
LPC11U68 Target does not have trace.



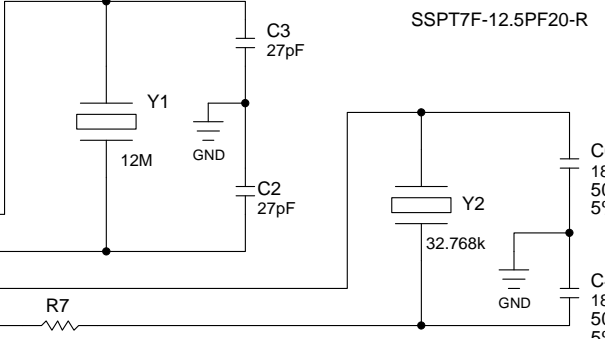
CONTRACT NO.		LINK LPC4322	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	7/30/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE	DWG. NO.
ISSUED	07/30/13	D	Xpresso-LPC11U68
		SCALE	SHEET 2 OF 04

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

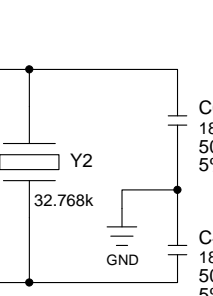
### USB Device Micro-B



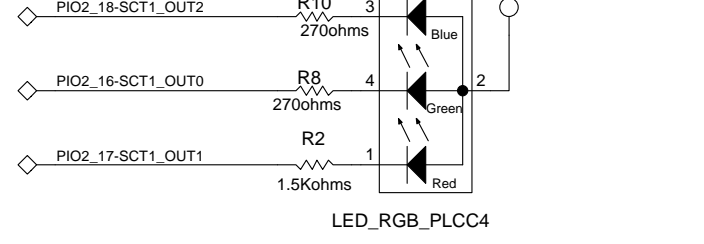
### ABLS2-12.000MHZ-D4Y-T



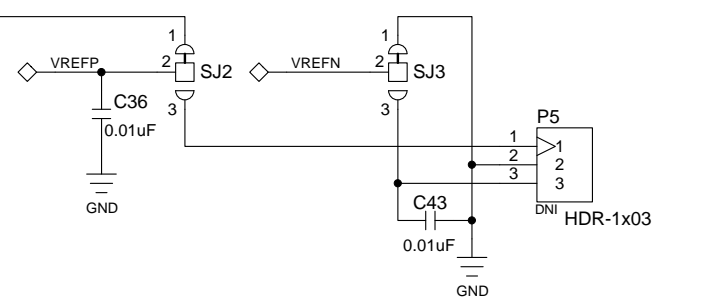
### SSPT7F-12.5PF20-R



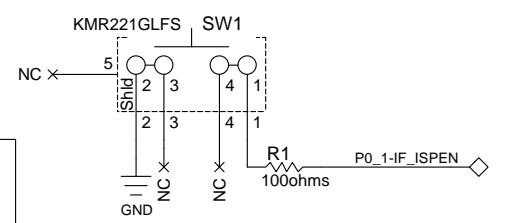
### RGB LED



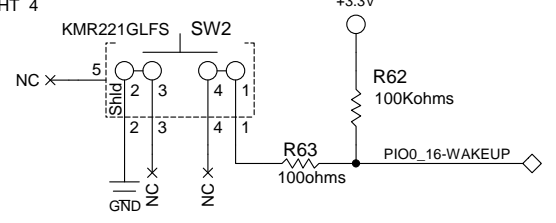
### ADC Reference Voltage



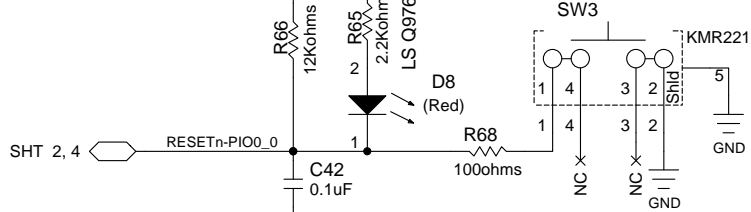
### Bootload Enable



### WAKEUP



### RESET



SHT	Pin	Function
SHT 2, 4	P0_1-IF_ISPEN	9
SHT 4	PIO0_2-SSP0_SSEL	19
SHT 4	PIO0_3-VBUS	30
SHT 4	PIO0_4-I2C_SCL	31
SHT 4	PIO0_5-I2C_SDA	32
SHT 4	PIO0_6	44
SHT 4	PIO0_7	45
SHT 4	PIO0_8-SSP0_MISO	58
SHT 4	PIO0_9-SSP0_MOSI	59
SHT 4	PIO0_11-ADC_9	64
SHT 4	PIO0_12-ADC_8	66
SHT 4	PIO0_13-ADC_7	68
SHT 4	PIO0_14-ADC_6	69
SHT 4	PIO0_16-WAKEUP	82
SHT 4	PIO0_17	90
SHT 4	PIO0_18-ISP_RX	94
SHT 2	PIO0_19-ISP_TX	95
SHT 4	PIO0_20	17
SHT 4	PIO0_20/CT16B1_CAP0/U2_RXD	33
SHT 4	PIO0_21	62
SHT 4	PIO0_22	62
SHT 4	PIO0_23-ADC_1	83

Pin	Function
PIO0_1/CLKOUT/CT32B0_MAT2/USB_FTGOGLLE	19
PIO0_2/SSP0_SSEL/CT16B0_CAP0/R_0	30
PIO0_3/VBUS/R_1	30
PIO0_4/I2C_SCL/R_2	31
PIO0_5/I2C_SDA/R_3	32
PIO0_6/USB_CONNECTn/SSP0_SCK/R_4	44
PIO0_7/U0_CTSn/R_5/I2C1_SCL	45
PIO0_8/SSP0_MISO/CT16B0_MAT0/R_6	58
PIO0_9/SSP0_MOSI/CT16B0_MAT1/R_7	59
TDI/PIO0_11/ADC_9/CT32B0_MAT3/U1_RTSn/U1_SCLK	64
TMS/PIO0_12/ADC_8/CT32B1_CAP0/U1_CTSn	66
TDO/PIO0_13/ADC_7/CT32B1_MAT0/U1_RXD	68
TRSTn/PIO0_14/ADC_6/CT32B1_MAT1/U1_TXD	69
PIO0_16/ADC_2/CT32B1_MAT3/R_8/WAKEUP	82
PIO0_17/U0_RTSn/CT32B0_CAP0/U0_SCLK	90
PIO0_18/U0_RXD/CT32B0_MAT0	94
PIO0_19/U0_TXD/CT32B0_MAT1	95
PIO0_20/CT16B1_CAP0/U2_RXD	33
PIO0_21/CT16B1_MAT0/SSP1_MOSI	62
PIO0_22/ADC_11/CT16B1_CAP1/SSP1_MISO	62
PIO0_23/ADC_1/R_9/U0_nRi/SSP1_SSEL	83

Pin	Function
PIO1_0	97
PIO1_1	28
PIO1_2	55
PIO1_3	72
PIO1_4	23
PIO1_5	47
PIO1_6	98
PIO1_7	10
PIO1_8	61
PIO1_9-ADC_0	86
PIO1_10	18
PIO1_11-I2C1_SCL	65
PIO1_12	89
PIO1_13	78
PIO1_14-I2C_SDA	79
PIO1_15	87
PIO1_16/PMOD_INT	96
PIO1_17	34
PIO1_18	43
PIO1_19	4
PIO1_20-SSP1_SCK	29
PIO1_21-SSP1_MISO	56
PIO1_22-SSP1_MOSI	80
PIO1_23-SSP1_SSEL	35
PIO1_24-CT32B0_MAT0	42
PIO1_25	100
PIO1_26-CT32B0_MAT2	20
PIO1_27-CT32B0_MAT3	22
PIO1_28	46
PIO1_29-SSP0_SCK	63
PIO1_30	67
PIO1_31	48

Pin	Function
PIO2_0/XTALIN	12
PIO2_1/XTALOUT	13
RTCXIN	5
RTCXOUT	6
VDDIO_1	14
VDDIO_2	71
VDDIO_3	54
VDDIO_4	70
VDDMAIN_EXT	93
VDDMAIN_EXT	92
VREFP	73
VDDA	84
VBAT	99
PIO2_2-SCT0_OUT1	21
PIO2_3-U3_RXD/CT32B0_MAT1	36
PIO2_4-U3_TXD/CT32B0_MAT2	41
PIO2_5-U3_CTSn/SCT0_IN1	15
PIO2_6/U1_RTSn/U1_SCLK/SCT0_IN2	37
PIO2_7-SSP0_SCK/SCT0_OUT2	40
PIO2_8/SCT1_IN0	2
PIO2_9/SCT1_IN1	3
PIO2_10/U4_RTSn/U4_SCLK	16
PIO2_11/U4_RXD	24
PIO2_12/U4_TXD	25
PIO2_13	26
PIO2_14	27
PIO2_15	49
PIO2_16-SCT1_OUT0	50
PIO2_17-SCT1_OUT1	51
PIO2_18-SCT1_OUT2	52
PIO2_19-SCT1_OUT3	57
PIO2_20	75
PIO2_21	76
PIO2_22	77
PIO2_23	1

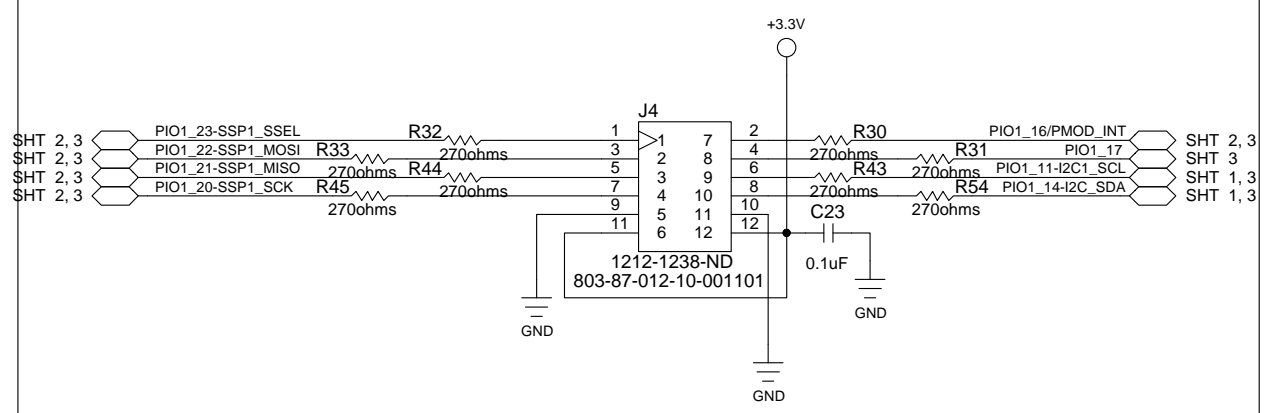
ISP Boot Modes		
Mode	PIO0_1	PIO0_3
No ISP (Flash)	high	X
USB	low	high
UART0	low	low

Note: Please refer to the data sheet (RTC oscillator component selection section) to select C<sub>x1</sub> and C<sub>x2</sub> values.

CONTRACT NO.		Target LPC11U68	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	d.consiglio	411 E. Plumeria Dr	
CHECKED	7/30/2014	San Jose, CA 95134	
ISSUED	07/30/13	www.standardics.nxp.com/microcontrollers/	
SIZE	FSCM NO.	DWG. NO.	REV
D		Xpresso-LPC11U68	C
SCALE		SHEET	3 OF 04

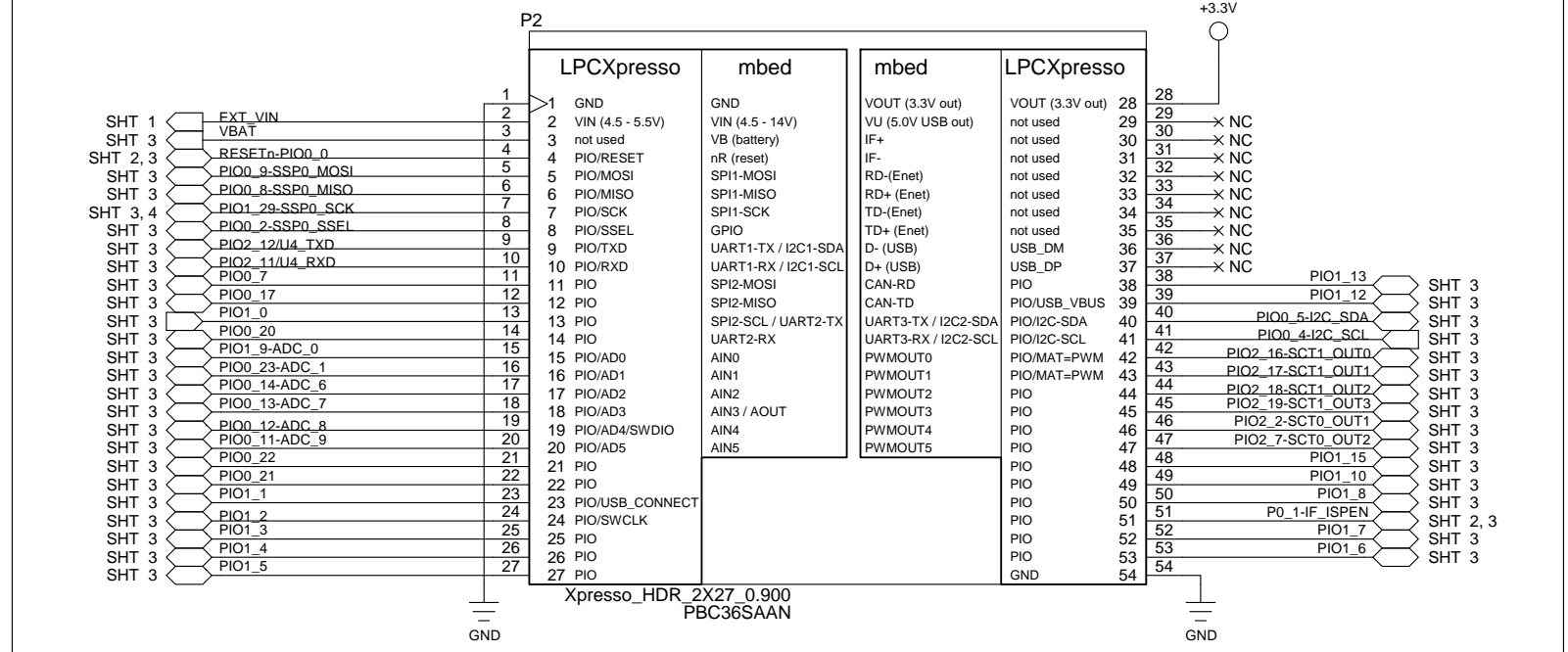
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

### PMOD compatible header

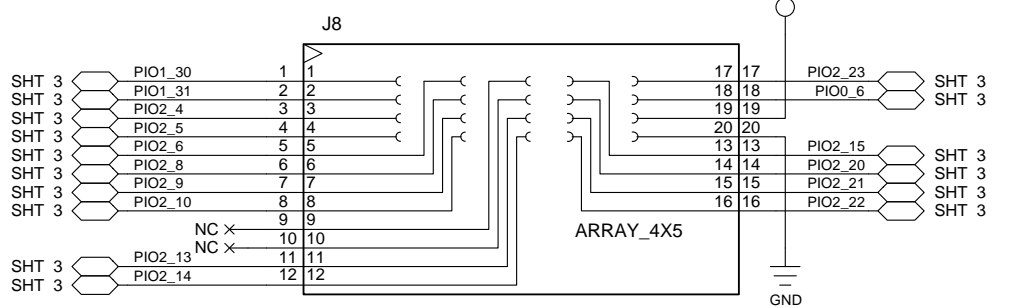
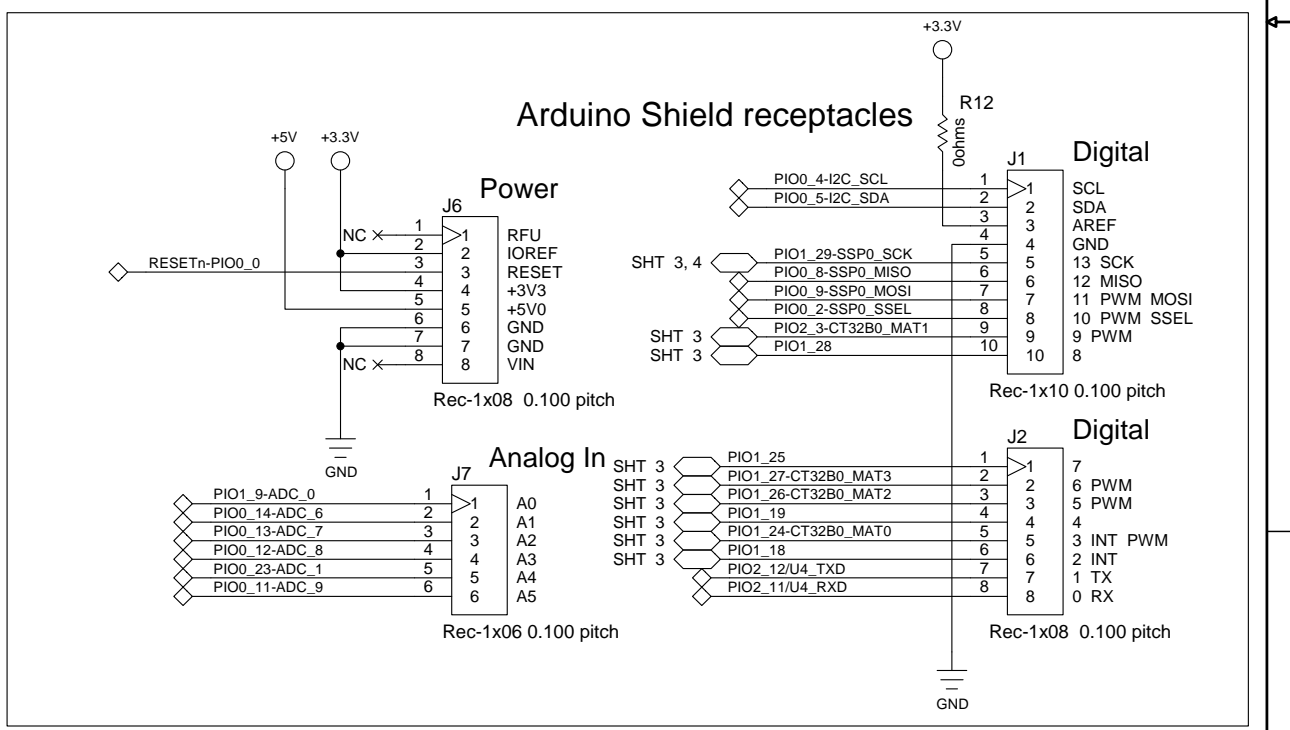


PMOD pin function	LPC11U68 supported function
Pin 1: GPIO/SPI-SSEL(out)/UART-CTS(in)	GPIO/SPI-SSEL(out)
Pin 2: GPIO/SPI-MOSI(out)/UART-TXD(out)	GPIO/SPI-MOSI(out)
Pin 3: GPIO/SPI-MISO(in)/UART-RXD(in)	GPIO/SPI-MISO(in)
Pin 4: GPIO/SPI-SCK(out)/UART-RTS(out)	GPIO/SPI-SCK(out)
Pin 5: GND	GND
Pin 6: VCC(3.3V)	VCC(3.3V)
Pin 7: GPIO/INT(in)	GPIO/INT(in)
Pin 8: GPIO/RESET(out)	GPIO/RESET(out)
Pin 9: GPIO	GPIO
Pin 10: GPIO	GPIO
Pin 11: GND	GND
Pin 12: VCC(3.3V)	VCC(3.3V)

### LPCXpresso brd header (Bottom side of PCB)



### Arduino Shield receptacles



CONTRACT NO.				Xpresso/Arduino/PMOD headers			
APPROVALS	DATE	<b>NXP Semiconductors</b>		411 E. Plumeria Dr		San Jose, CA 95134	
DRAWN	d.consiglio	www.standardics.nxp.com/microcontrollers/		Xpresso-LPC11U68		REV C	
CHECKED	07/30/13	SCALE	SHEET 4 OF 04				