Target Hi-Speed USB Device / Host
Micro-AB

Install shunt only when Target is USB Host!

Target Vddc measurement

Target VddIO measurement

Target LPC433x / Power / USB

NXP Semiconductors
411 E. Plumeria Dr
San Jose, CA 95134
www.standardics.nxp.com/microcontrollers

+3.3V

USB Device / Host

Hi-Speed USB

Micro-AB

RGB LED

RESET

Target LPC433x / Power / USB

USB Device / Host

Hi-Speed USB

Micro-AB
PMOD pin function
Pin 1: GPIO/SPI-SEL(out)/UART-CTS(in)
Pin 2: GPIO/SPI-MOSI(out)/UART-TXD(out)
Pin 3: GPIO/SPI-MISO(in)/UART-RXD(in)
Pin 4: GPIO/SPI-SCK(out)/UART-RTS(out)
Pin 5: GND
Pin 6: VCC(3.3V)
Pin 7: GPIO/INT(in)
Pin 8: GPIO/RESET(out)
Pin 9: GPIO/SCL
Pin 10: GPIO/SDA
Pin 11: GND
Pin 12: VCC(3.3V)

LPC43xx supported function
GPIO/SPI-SSEL(in/out)
GPIO/SPI-MOSI(in/out)
GPIO/SPI-MISO(out/in)
GPIO/SPI-SCK(in/out)
GND
VCC(3.3V)
GPIO/INT(out/in)
GPIO/RESET(out)
SCL
SDA
GND
VCC(3.3V)

NXP A70xx / A71 secure MCU

JS20 position:
1-2: A710x installed at U4
2-3: A7001/2 installed at U4

NXP A7010x security / Pmod interfaces