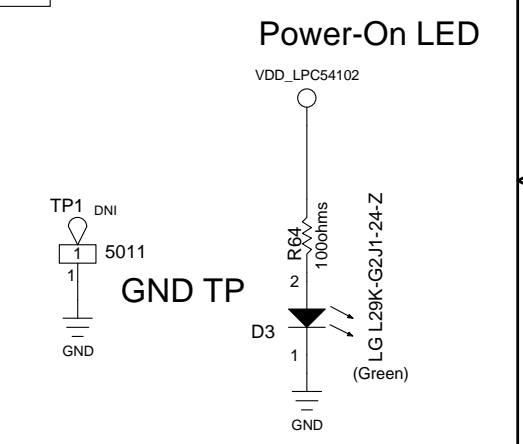
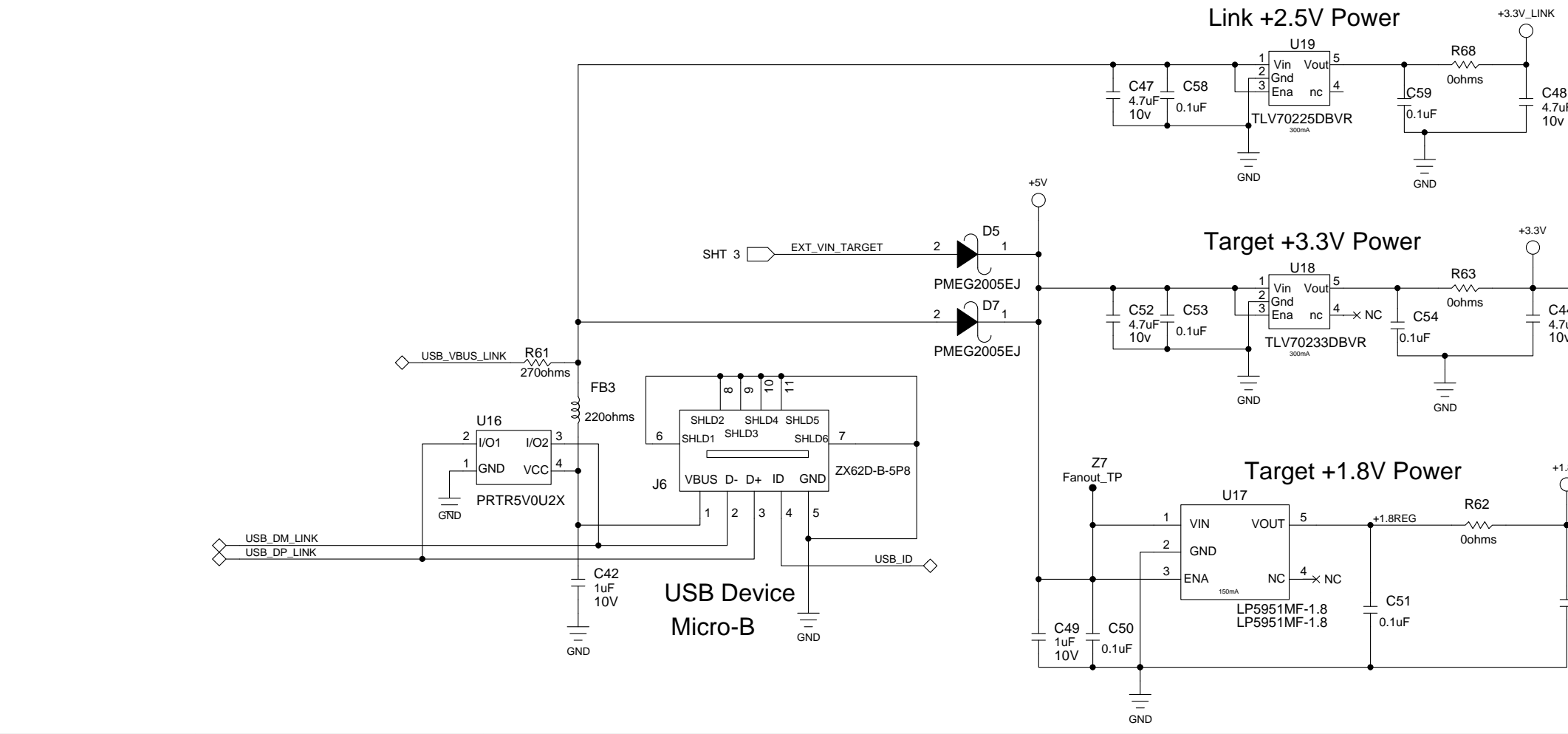
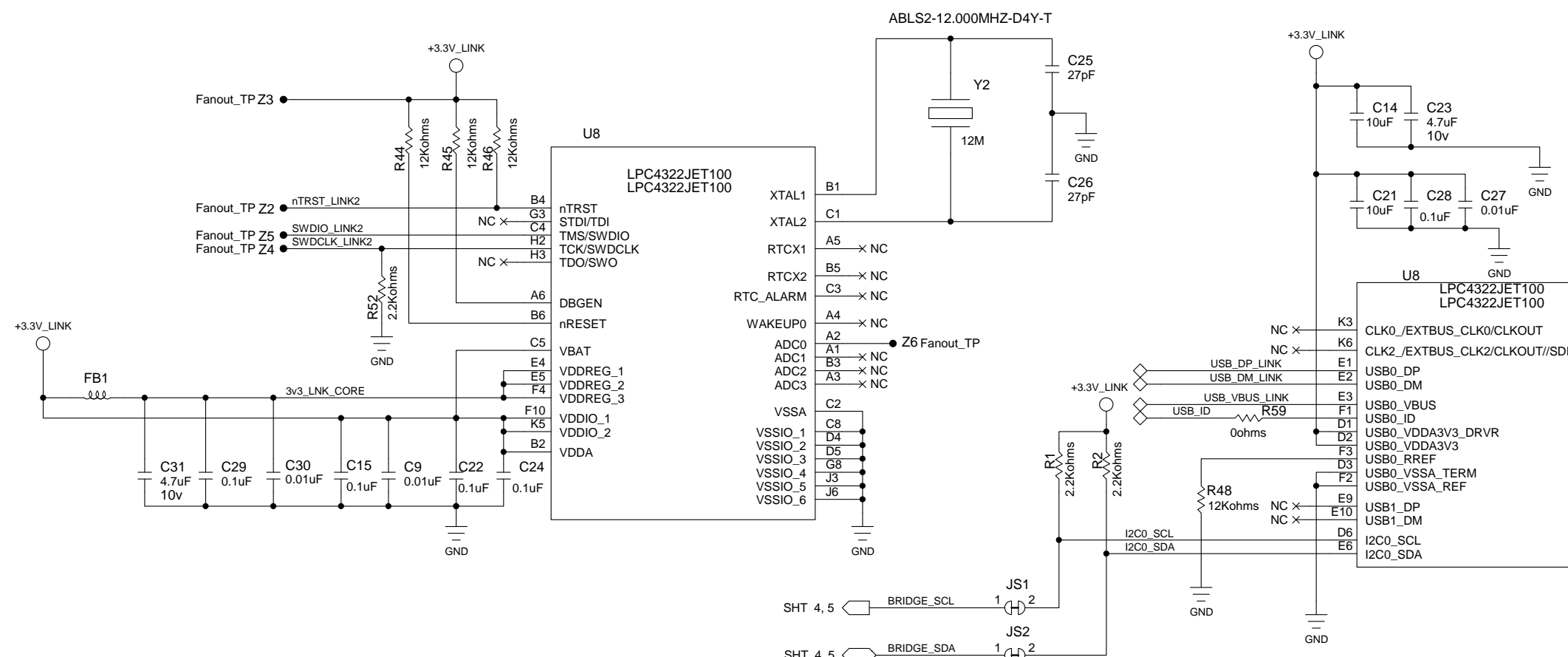


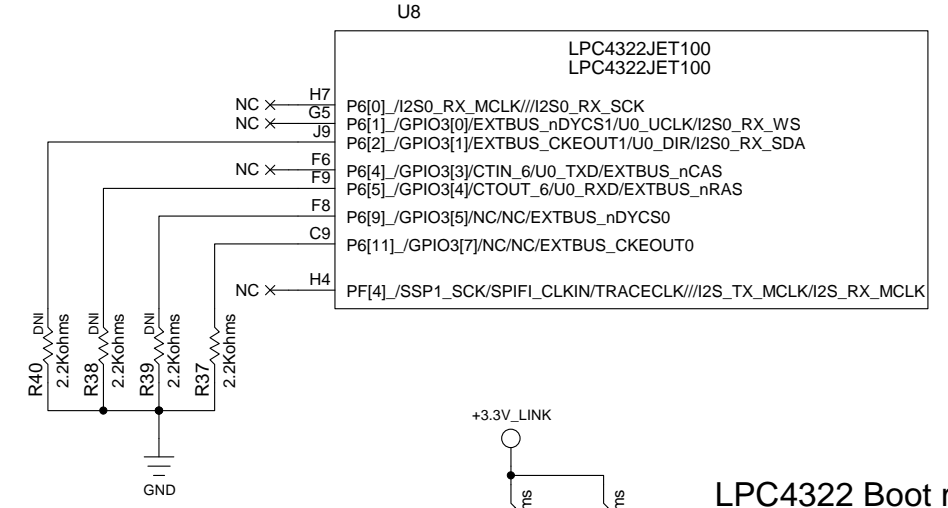
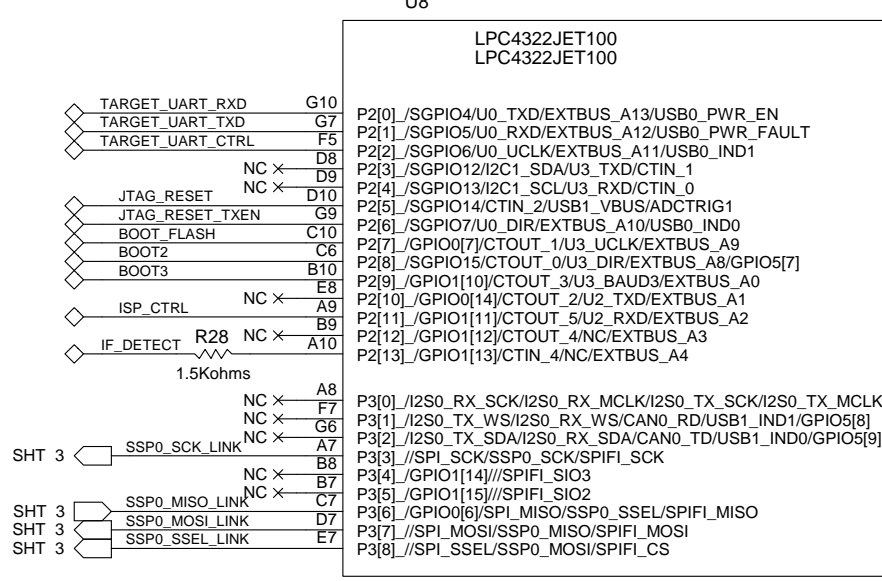
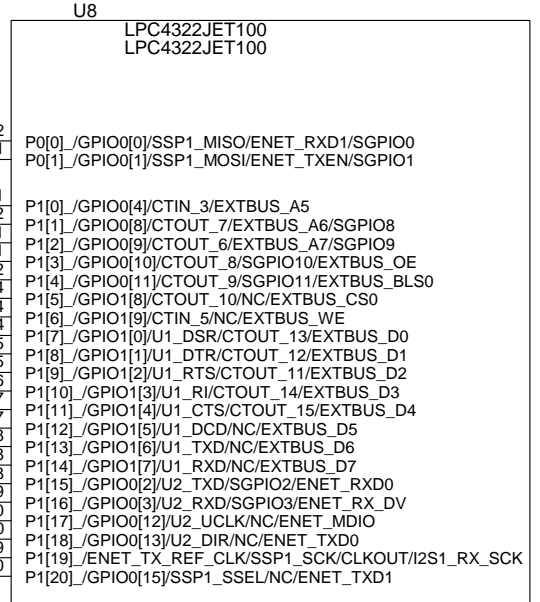
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	- Prototype LQFP64 board Release	9/04/2014	
A	- Production LQFP64 board release - 74LVC1G53 (U7) VDD changed to +3.3V; sht 3 - Add C60 47pF; sht 3 - Target P0_29 & P0_30 access now at J1; sht 6 - Update LPC54102 current measurement chart; sht 3 - Changed J3 to receptacle 2x06RA; sht 5	9/13/2014	
A1	- LPC54102 (U12) symbol update; sht 4	3/20/2015	



LPC54102 VDD voltage select JP7
 VDD = 3.3V; 2 - 3 (default)
 VDD = 1.8V; 1 - 2

CONTRACT NO.		LINK2 LPC43xx	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	d.consiglio	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	3/20/15	www.standardics.nxp.com/microcontrollers/	
SIZE	FSCM NO.	DWG. NO.	REV
D		LPCX54102 LQFP64	A1
SCALE		SHEET	1 OF 06

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



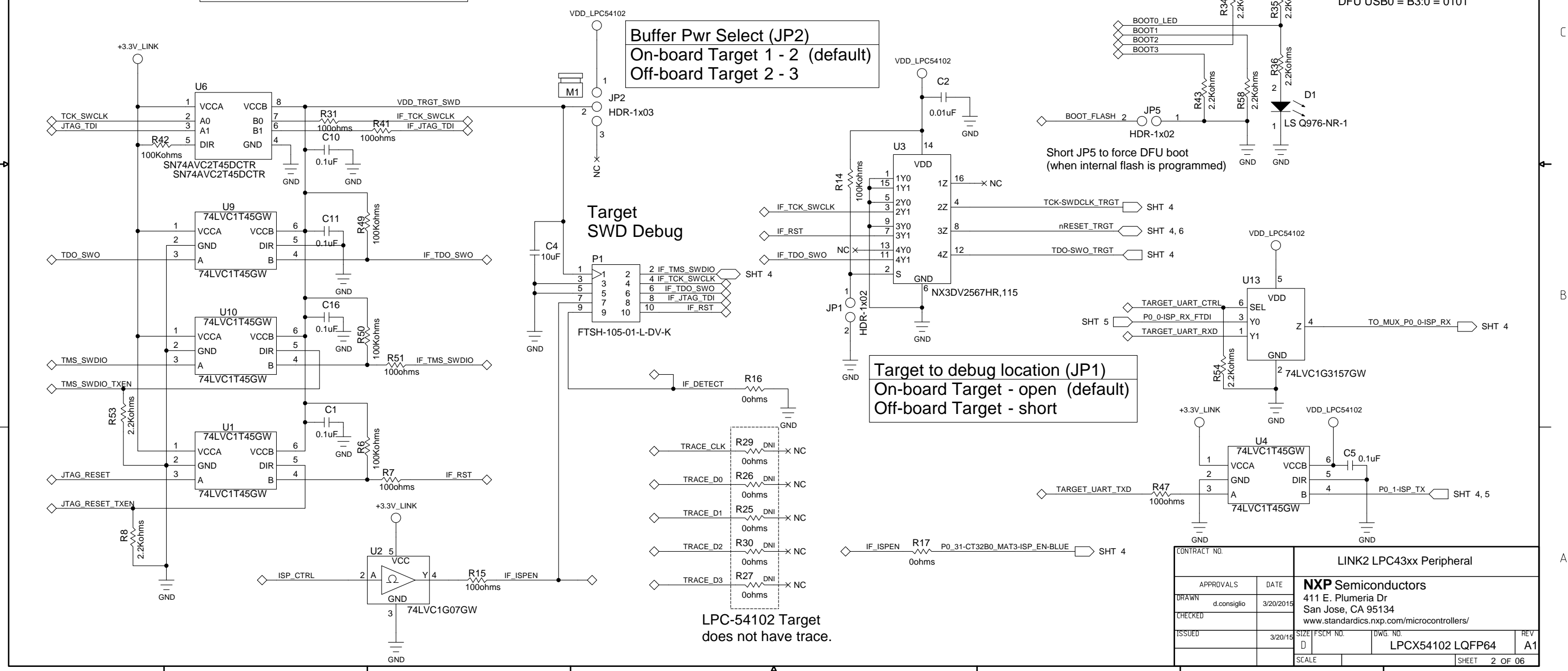
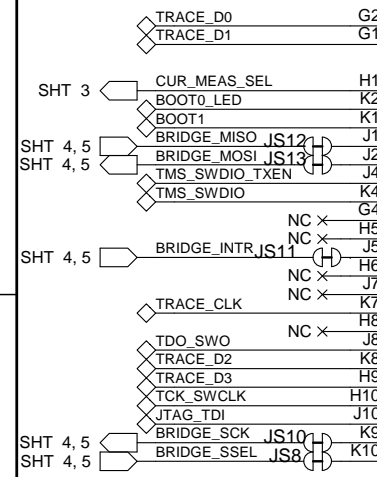
LPC4322 Boot mode
DFU USB0 = B3:0 = 0101

Buffer Pwr Select (JP2)
On-board Target 1 - 2 (default)
Off-board Target 2 - 3

Short JP5 to force DFU boot
(when internal flash is programmed)

Target to debug location (JP1)
On-board Target - open (default)
Off-board Target - short

LPC-54102 Target
does not have trace.

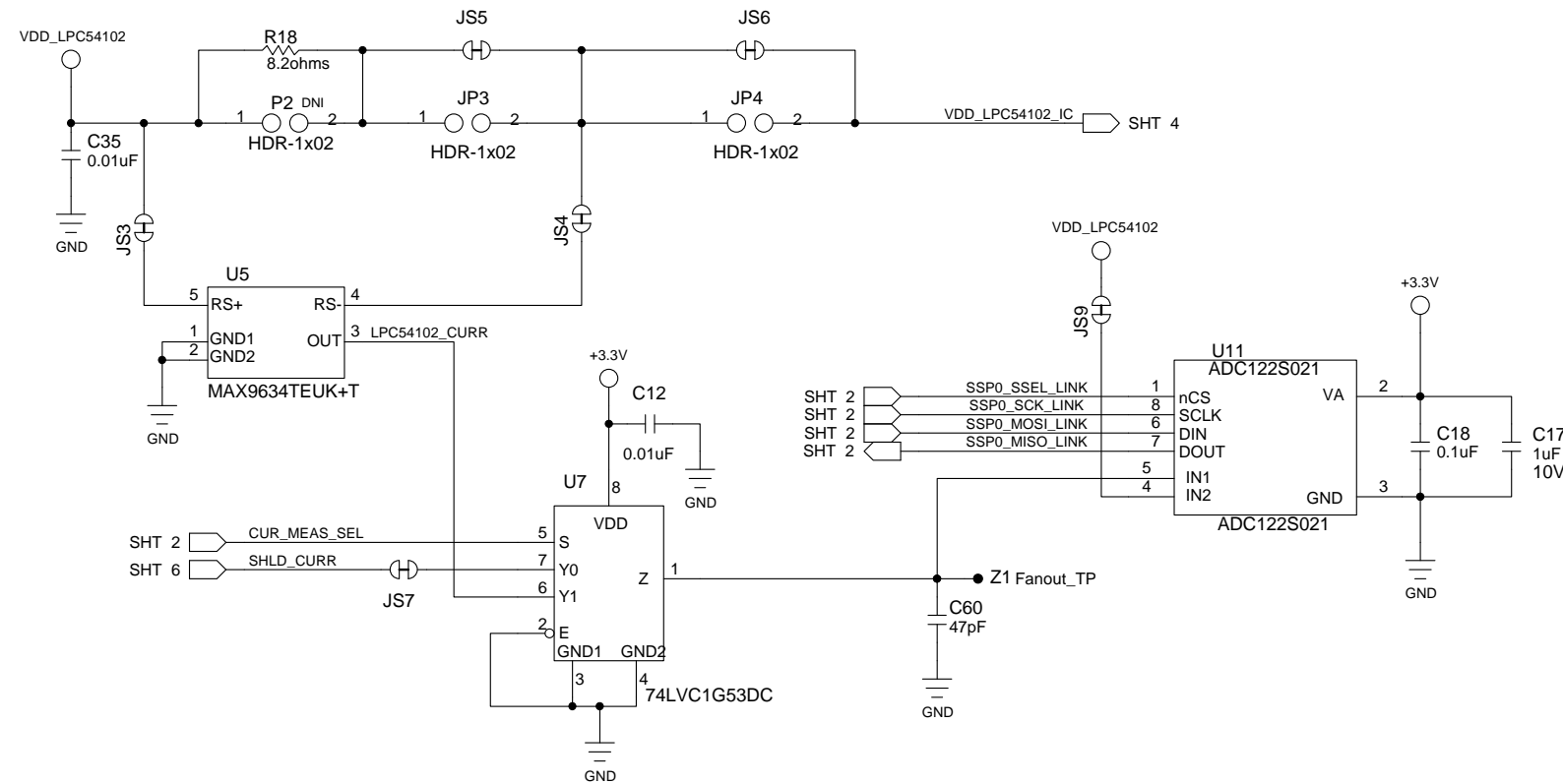
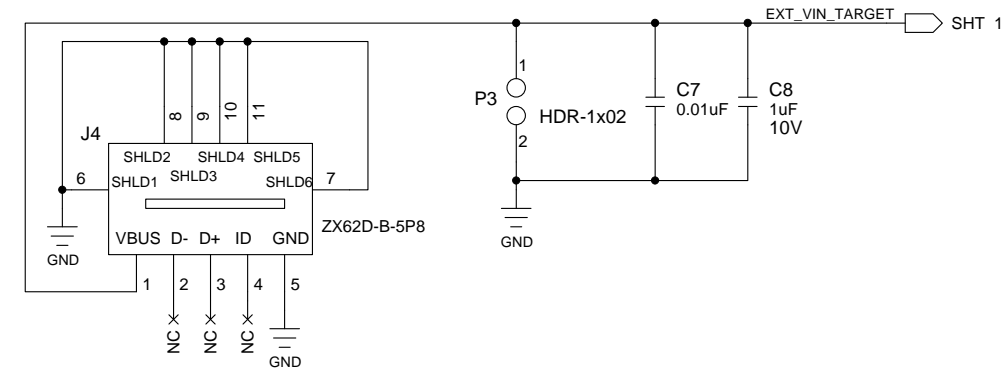


CONTRACT NO.		LINK2 LPC43xx Peripheral	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	3/20/2015	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE	D
ISSUED	3/20/15	FWSCM NO.	LPCX54102 LQFP64
		DWG. NO.	A1
		SHEET	2 OF 06

LPC54102 Current measurement

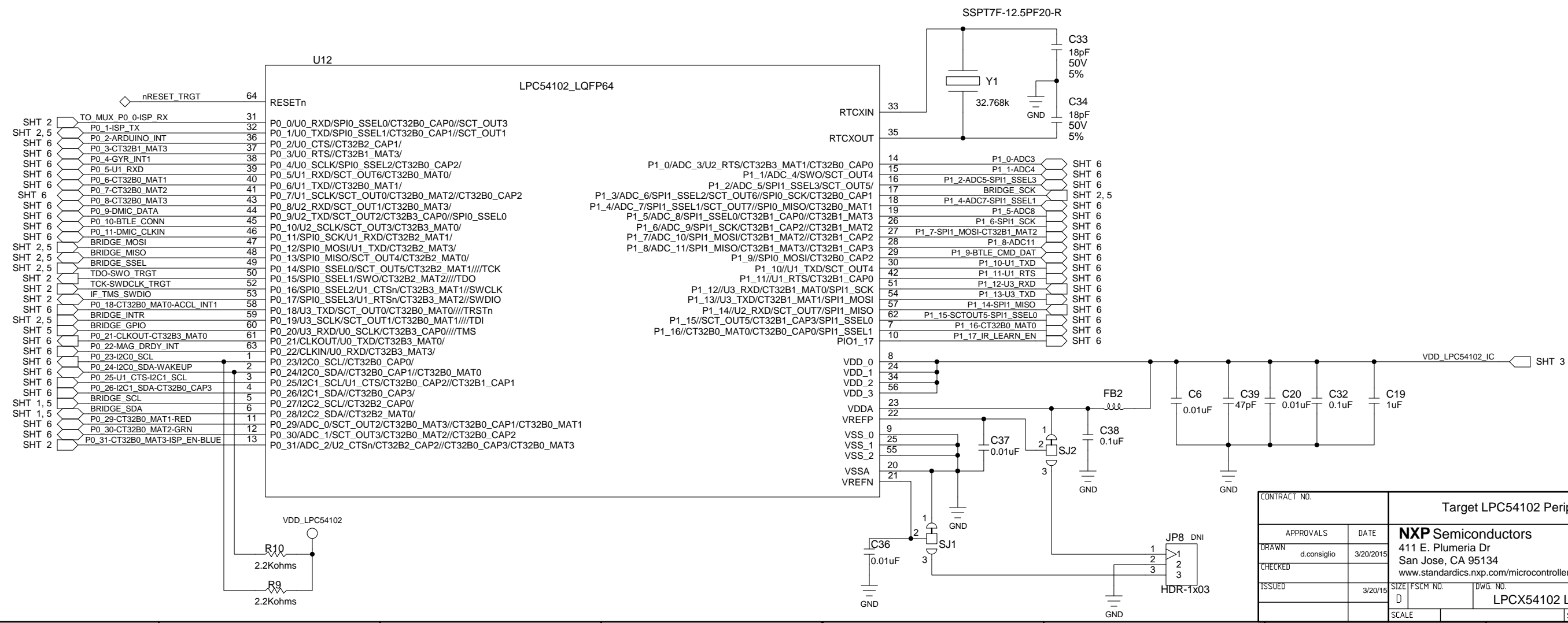
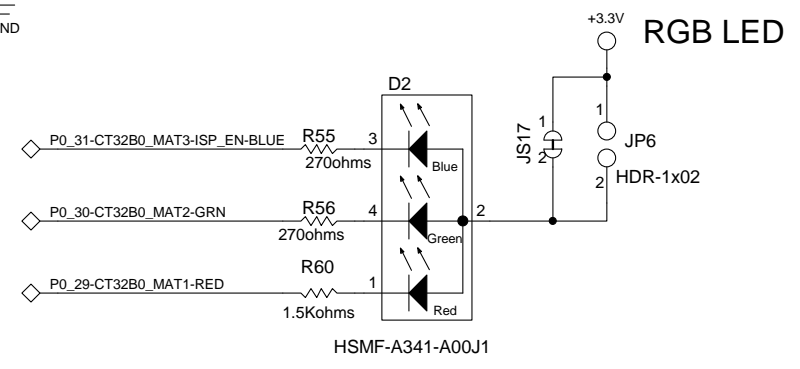
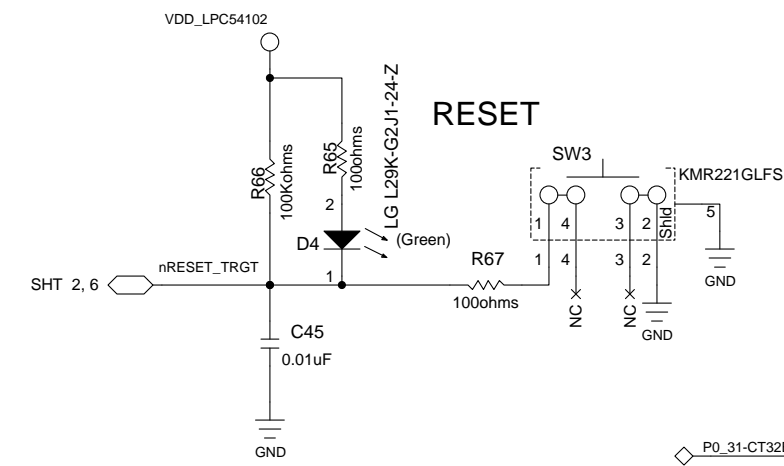
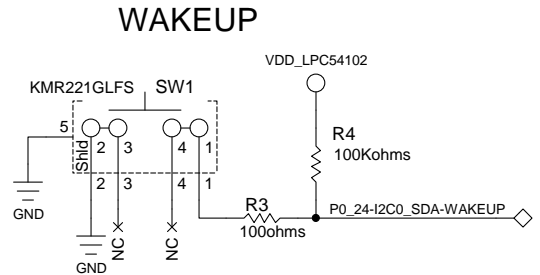
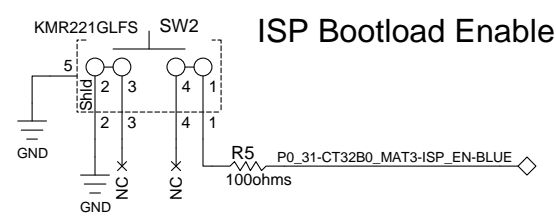
LPC54102			ADC111S021 12bit ADC
Vsense R18 voltage 1-lsb	LPC54102 Current 1-lsb	maximum current	ADC input 1- lsb
32uV	3.9uA	16mA	800uV

Target External Vin 5V



CONTRACT NO.		Target LPC54102 power & current measurement		
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/		
DRAWN d.consiglio	3/20/2015			
CHECKED				
ISSUED	3/20/15			
SIZE D	FSCM NO.	DWG. NO. LPCX54102 LQFP64	REV A1	
SCALE			SHEET 3 OF 06	

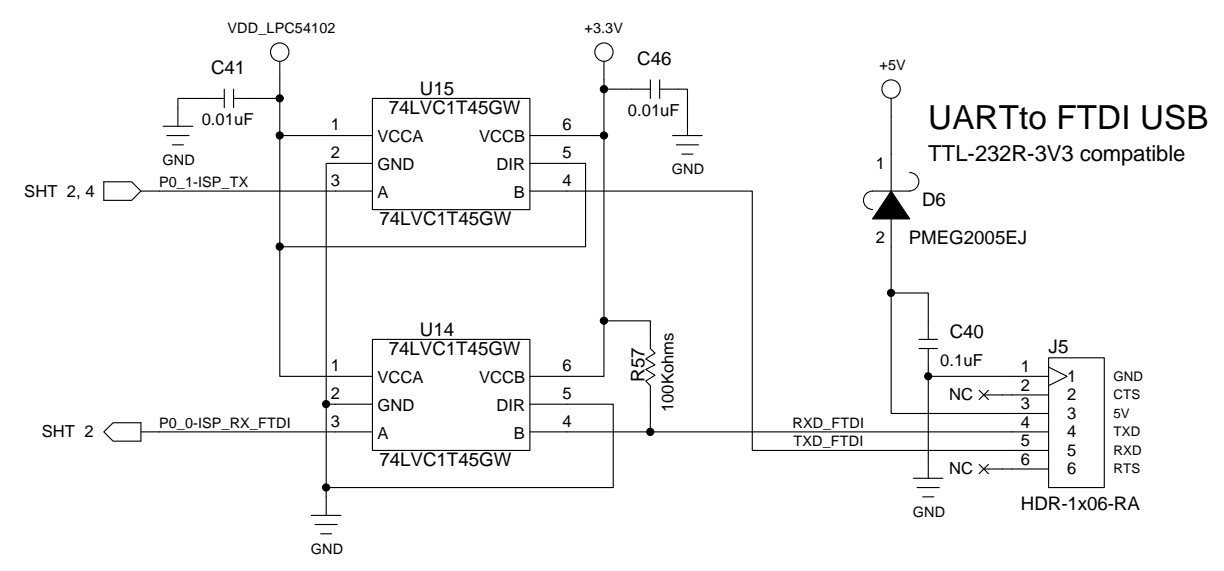
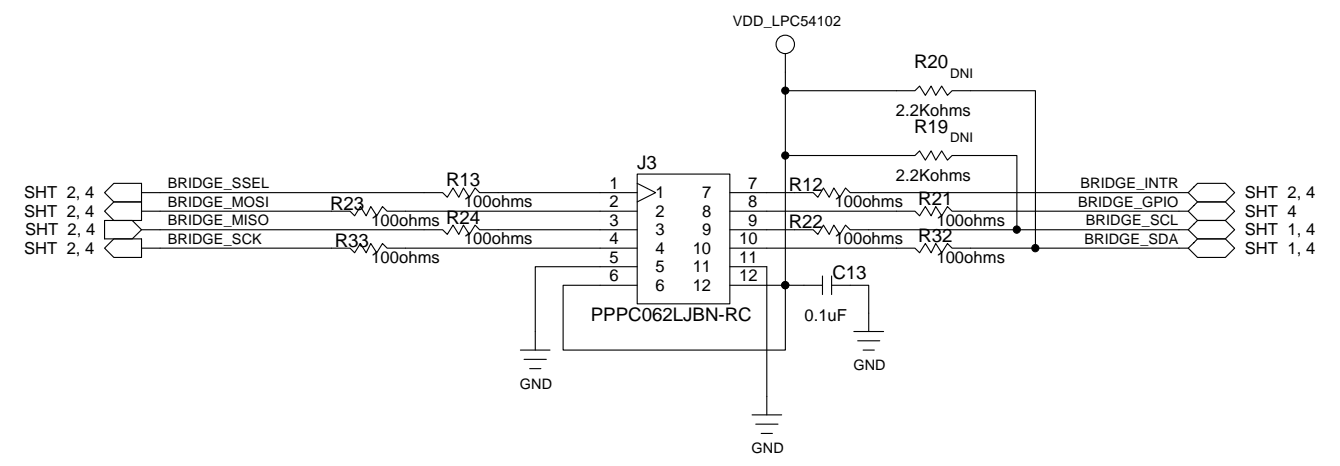
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



CONTRACT NO.		Target LPC54102 Peripheral	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	3/20/2015	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	3/20/15	www.standardics.nxp.com/microcontrollers/	
SIZE/FSCM NO.	DWG. NO.	LPCX54102 LQFP64	REV. A1
SCALE	SHEET	4 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

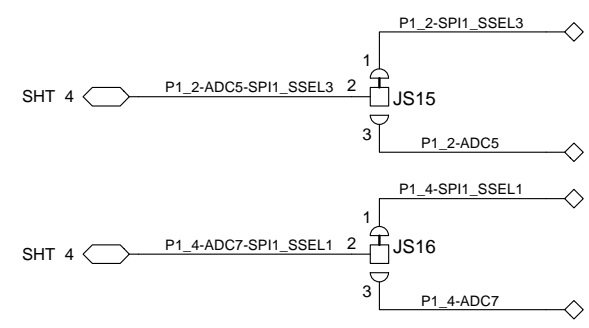
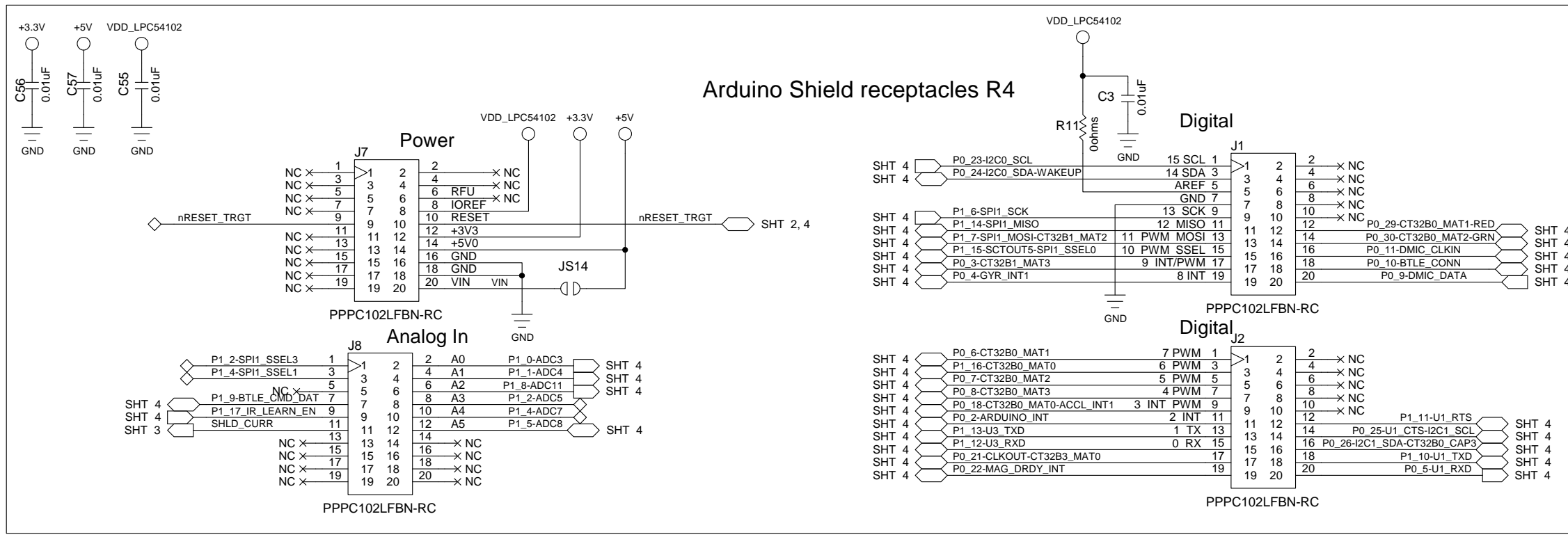
SPI / I2C brdge header (Pmod compatible)



CONTRACT NO.		I2C_SPI bridge hdr / FTDI			
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
DRAWN	d.consiglio				
CHECKED					
ISSUED	3/20/15	SIZE	FSCM NO.	DWG. NO.	REV.
		D		LPCX54102 LQFP64	A1
SCALE				SHEET 5 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

Arduino Shield receptacles R4



CONTRACT NO.		Arduino Interface		
APPROVALS	DATE	NXP Semiconductors		
DRAWN	d.consiglio	411 E. Plumeria Dr		
CHECKED		San Jose, CA 95134		
ISSUED	3/20/15	www.standardics.nxp.com/microcontrollers/		
		SIZE	FSCM NO.	DWG. NO.
		D		LPCX54102 LQFP64
		SCALE		REV A1
				SHEET 6 OF 06