2.2µF recommended to ensure USB 1µF minimum requirement is met for all conditions.

(1) Vsense voltage is between U16 RS+ to RS- Total Rsense = R19 + (JS10 || JP3).

JP3 Setting

<table>
<thead>
<tr>
<th>Vsense (volts)</th>
<th>JP3 (ohms)</th>
<th>Total Rsense</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.3V</td>
<td>open</td>
<td>8.24</td>
</tr>
<tr>
<td>1.8V</td>
<td>shunted</td>
<td>4.12</td>
</tr>
</tbody>
</table>

ADC111S021 12bit ADC

LPC5411x current measurement, Link2 SPI isolation transceiver

NXP Semiconductors
411 E. Plumas Dr
San Jose, CA 95134
www.nxp.com/lsx5411x
**LPC5411x AP control port**
(AP must drive open-drain)

**SPI / I2C bridge header (Pmod compatible)**

Note: 2.2k on BRIDGE_T_INTR-ISP1 (P0_4) is required for SPI / I2C boot over host interface.

**NXP A700x / A71x secure MCU**

- Operating range: 1.62 - 1.98V; 2.4 - 3.6V
- **A710x**: 1 - 2
- **A700x**: 2 - 3

**UART to FTDI USB**

TTL-232R-3V3 compatible

use SCI2C protocol

I2C address = 0b1001000 (JCOP default)

**Pmod / FTDI / SecElem / SPI Flash**

**LPC5411x QFP64**

4/21/2015

Semiconductors

411 E. Plumeria Dr
San Jose, CA 95134

www.standardics.nxp.com/microcontrollers/