MAC57D5-208DC DAUGHTERCARD

INDEX

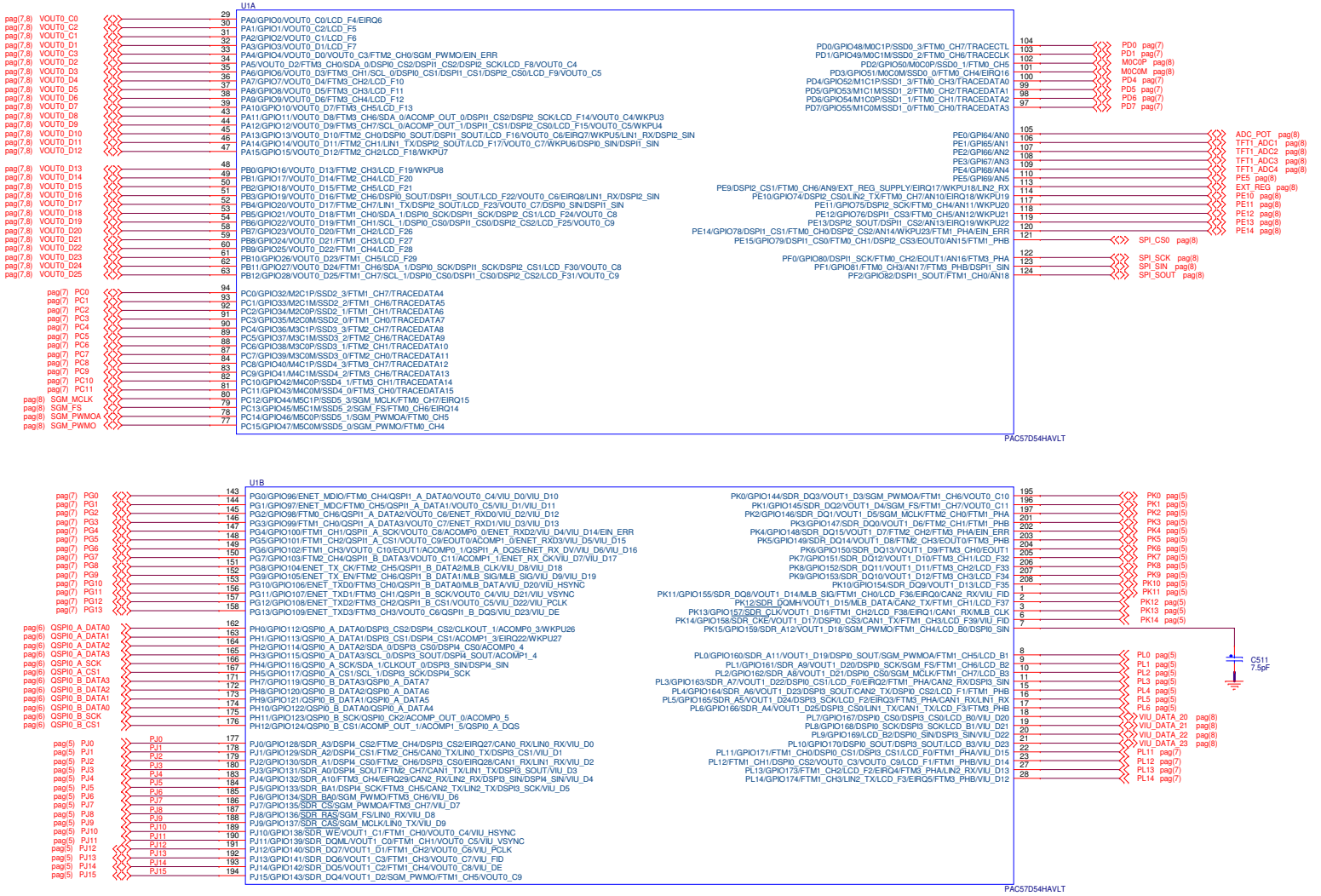
freescale semiconductor

Microcontroller Solutions Group

6501 William Cannon Drive West
Austin, TX 78735-8598

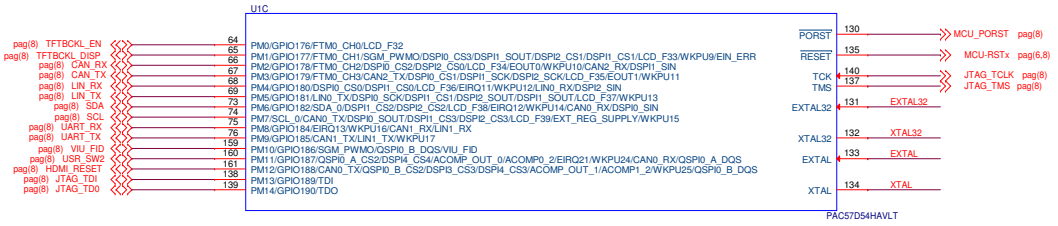
ICAP Classification: FCP: ___ FIUO: X PUBI: ___

Designer: Ioseph Martinez	Drawing Title: **MAC57D5-208DC**		
Drawn by: Ioseph Martinez	Page Title: **TITLE and NOTES**		
Approved: Jesus Sanchez	Size B	Document Number SCH-28805 PDF: SPF-28805	Rev A
	Date: Monday, May 11, 2015	Sheet 1 of 8	

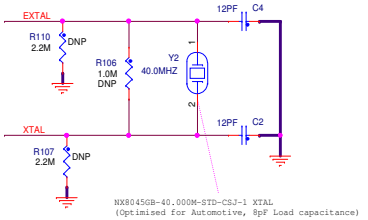


ICAP Classification:	FCP: ___ FIUO: X PUBI: ___
Drawing Title:	MAC57D5-208DC
Page Title:	MCU IO
Size: C	Document Number: SCH-28805 PDF: SPF-28805 Rev: A
Date: Monday, May 11, 2015	Sheet 2 of 8

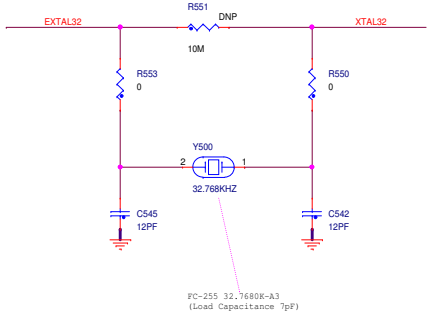
MCU IO



XTAL INTERFACE



XTAL32 INTERFACE



Clock Requirements

1. FXOSC : 8- 40 MHz Crystal - Pierce Configuration
2. SXOSC : 32KHz Crystal

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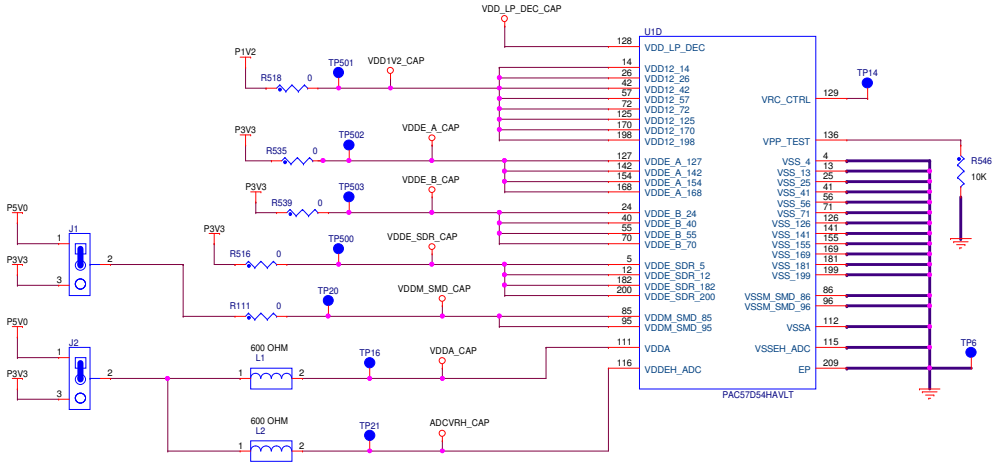
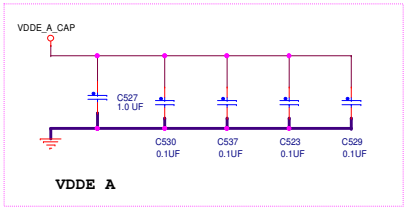
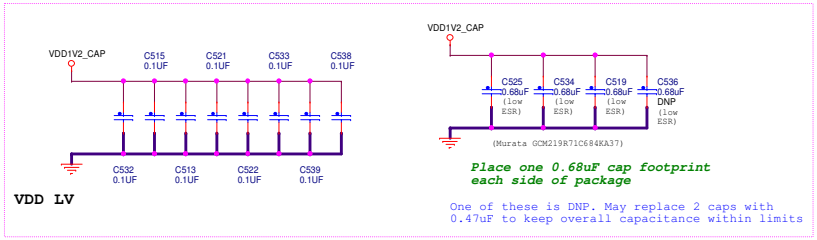
Drawn by: Ioseph Martinez	Page Title: MCU CLK XTAL	
Approved: Jesus Sanchez	Size C	Document Number SCH-28805 PDF: SPF-28805 Rev A
	Date: Monday, May 11, 2015	Sheet 3 of 8

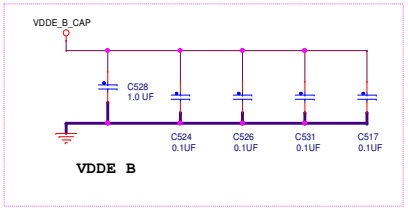
POWER SUPPLY SECTION

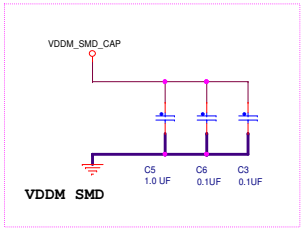


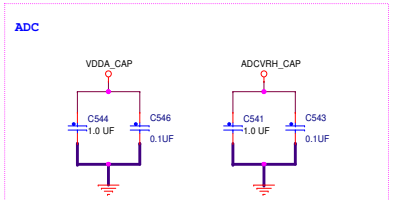
DECAP (HALO)

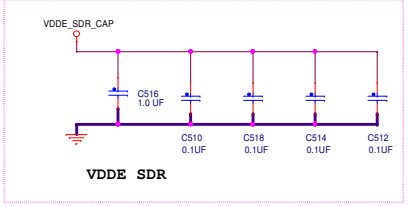


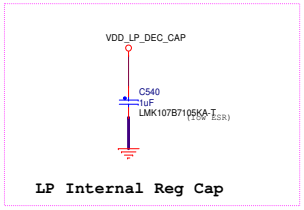


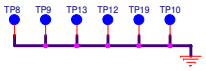












Function	Voltage Range	Description
VDD12	1.2V – 1.32V	Core Logic Low Voltage Supply
VSS	GND	Core Logic & HV Ground Supply
VDDE_A	3.0V – 5.5V	I/O Segment A Voltage Supply
VDDE_B	3.0V – 5.5V	I/O Segment B Voltage Supply
VDDE_SDR	3.0V – 5.5V	I/O Segment C Voltage Supply
VDDE_DDR	1.7V – 1.9V	DDR2 DRAM I/O Segment Voltage Supply
VDD0P9_DDR	0.85V – 0.95V	0.9V DDR2 Supply Voltage
VDDM_SMD	3.0V – 5.5V	Stepper Motor Drive Voltage Supply
VPP_TEST	0V	Flash Test Supply / Test Mode Input
VDDA_REF	3.0V – 5.5V	SAR ADC High Voltage Reference
VSSA_REF	0V	SAR ADC Low Voltage Reference
VDDEH_ADC	3.0V – 5.5V	SAR ADC I/O Segment Voltage Supply
VSSEH_ADC	GND	SAR ADCI/O Segment Ground
VDDA	3.0V – 5.5V	SAR ADC Voltage Supply
VSSA	GNDA	SAR ADC Ground Supply
VDD_LP_DEC	GND	VDD_LV Decoupling Capacitor
VRC_CTRL	–	PMC Voltage Regulator Control Output

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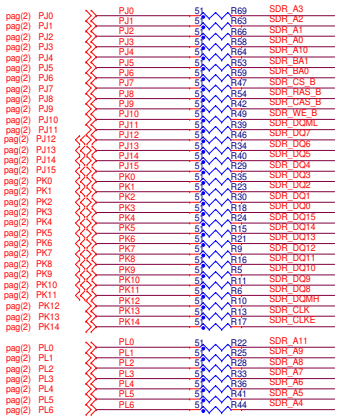
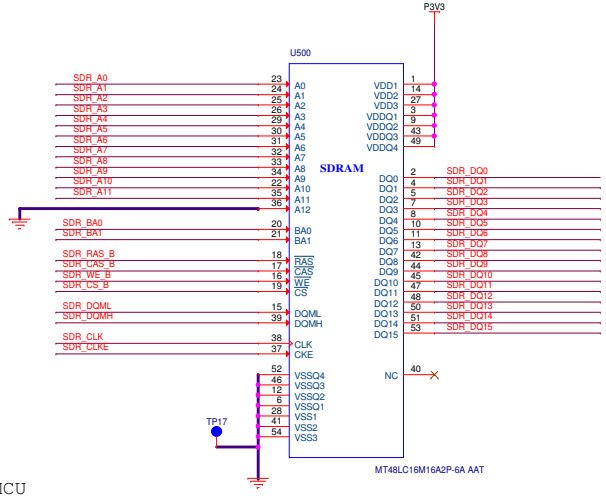
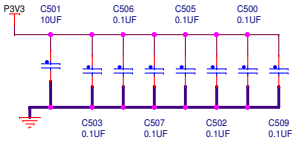
Designer: Ioseph Martinez	Drawing Title: MAC57D5-208DC
Drawn by: Ioseph Martinez	Page Title: MCU PWR
Approved: Jesus Sanchez	Size C; Document Number SCH-28805 PDF: SPF-28805; Rev A
	Date: Monday, May 11, 2015; Sheet 4 of 8

SDR INTERFACE







Note: All 51 Ohm Resistors will be placed near the MCU

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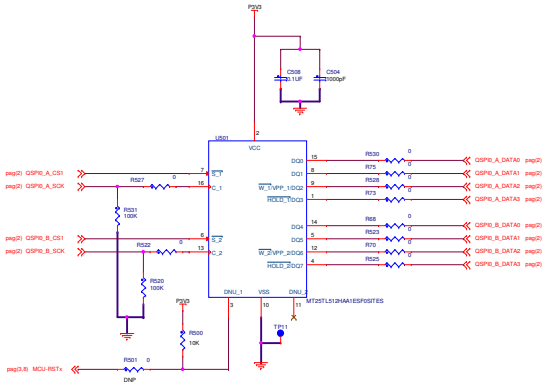
Drawn by: Ioseph Martinez	Page Title: **SDR**	
Approved: Jesus Sanchez	Size C	Document Number SCH-28805 PDF: SPF-28805 Rev A
	Date: Monday, May 11, 2015	Sheet 5 of 8

QSPI MEMORY



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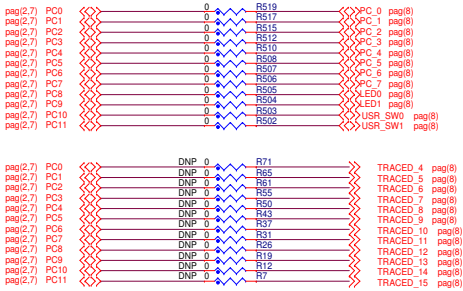
Designer: Joseph Martinez	Drawing Title: MAC57D5-208DC		
Drawn by: Joseph Martinez	Page Title: QSPI		
Approved: Jesus Sanchez	Size: D	Document Number: SCH-28805 PDF: SPF-28805	Rev: A
	Date: Monday, May 11, 2015	Sheet 8 of 8	

PORT C: SMC/TRACE

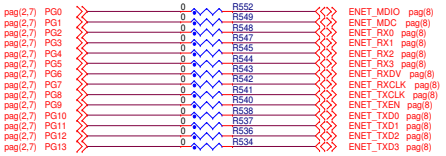


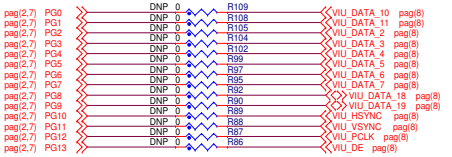
Place resistors as close as possible to the MCU pins

PORT D: SMC/TRACE

Place resistors as close as possible to the MCU pins

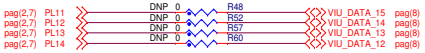
PORT G: ENET/VIU





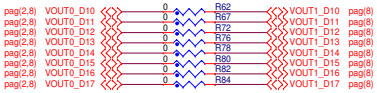
Place resistors as close as possible to the MCU pins

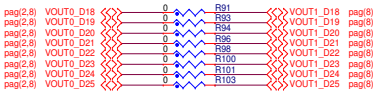
PORT L: ENET/VIU



Place resistors as close as possible to the MCU pins

VOUT0 = VOUT1





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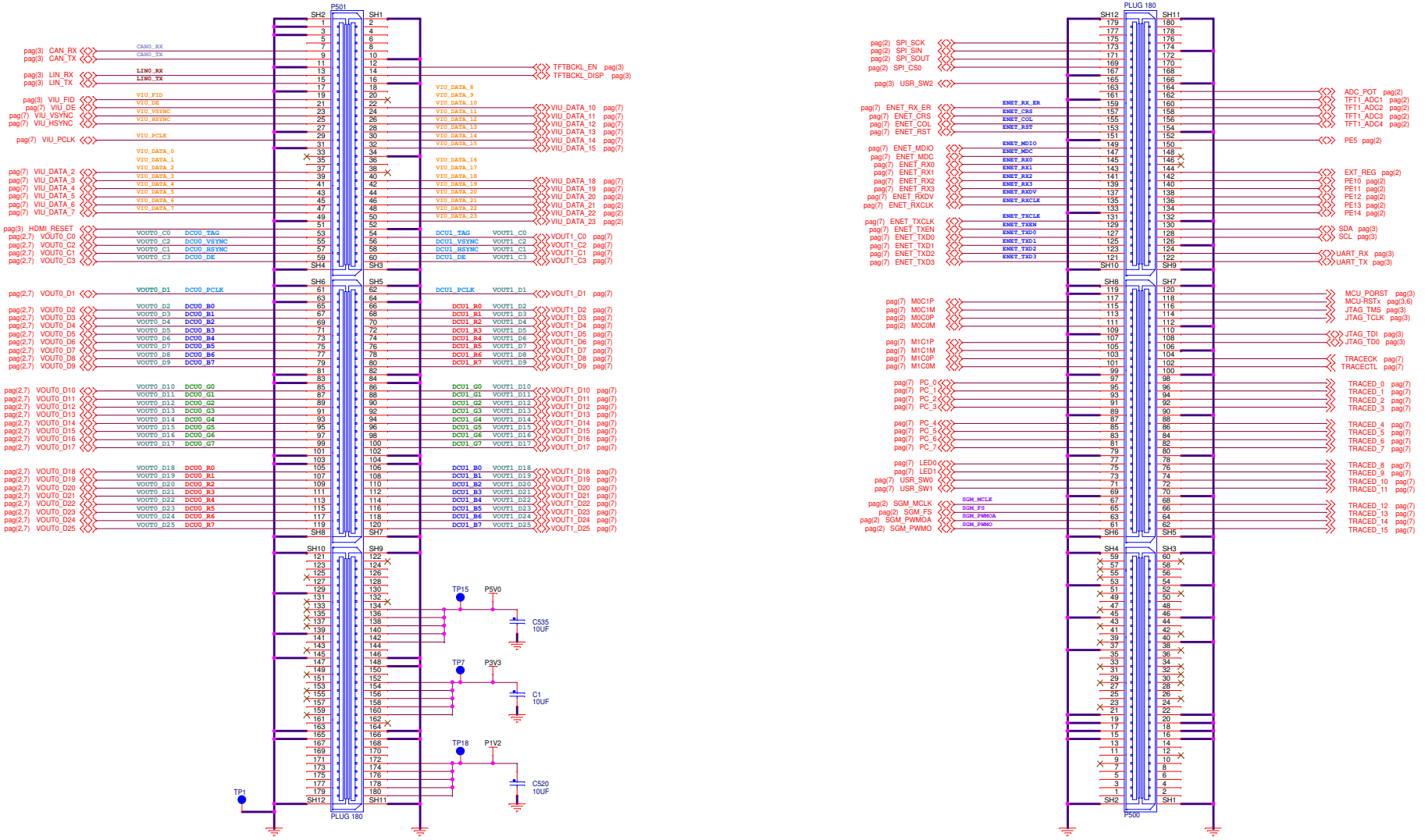
Drawing Title: **MAC57D5-208DC**

Page Title: **MUX IO**

Size	Document Number	Rev
C	SCH-28805 PDF: SPF-28805	A

Date: Monday, May 11, 2015 Sheet 7 of 8



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Drawing Title:

MAC57D5-208DC

Page Title:

MB CONN

Size	Document Number	Rev
C	SCH-28805 PDF: SPF-28805	A

Date: Monday, May 11, 2015 Sheet 8 of 8