

1. MINIMUM CLEARANCE ON THE BOARD SHALL BE .008 INCH.
2. INNER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1 OZ.
OUTER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1.5 OZ
AFTER PLATING.
3. MINIMUM THICKNESS OF COPPER ON THE WALL OF A PLATED HOLE
SHALL BE .001 INCH.
4. HOLE SIZE TOLERANCES SHALL BE:
PLATED THRU HOLES =.015 SHALL BE +.005/- .015 INCH.
PLATED THRU HOLES =.020 SHALL BE +/- .005 INCH.
PLATED THRU HOLES >.020 SHALL BE +/- .003 INCH.
UNPLATED THRU HOLES SHALL BE +/- .002 INCH.
5. SOLDER MASK ON BOTH SIDES OF THE BOARD BE S.M.O.B.C,
LPI, COLOR GREEN.
6. ALL MATERIAL SHALL BE NEMA GRADE FR-4 OR EQUIVALENT.
7. TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES.
8. DRC'S MUST BE RAN ON THE GERBERS BEFORE BUILDING BOARD UNLESS
PRIOR APPROVAL IS GIVEN IN WRITING.
9. SILKSCREEN TOP SIDE WITH NON-CONDUCTIVE EPOXY-BASED
WHITE INK.
10. MANUFACTURE TO IPC-A-600D SPECIFICATIONS.
11. MINIMUM ANNULAR RING SHALL BE .001".
12. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE .003".
13. BOW AND TWIST SHALL NOT EXCEED .010" PER INCH.
14. DRILL TABLE AND GERBER FILES OUTPUT IN MILS. 1 MIL = .001".

SIZE	QTY	SYM	PLTD
15	85	⊕	PLTD
20	3	⊗	PLTD
40	27	⊞	PLTD
47	9	⊠	PLTD
120	4	⊗	PLTD
140	1	⊗	PLTD
48	2	A	NPLTD
125	2	B	NPLTD

TOP SILKSCREEN	<
TOP SOLDER MASK	<
COMPONENT	<
SOLDER	<
BOTTOM SOLDER MASK	<

LAYER SCHEDULE
SCALE: NONE 7

FILE NAMES
FROM ZIP FILE

TDMOT102-TOPSILK.PHO
TDMOT102-TOPMASK.PHO
TDMOT102-L1.PHO
TDMOT102-L2.PHO
TDMOT102-BOTMASK.PHO

UNLESS OTHERWISE SPECIFIED:

DIMENSIONS ARE IN INCHES.
REMOVE ALL BURRS AND SHARP EDGES.
ALL DIMENSIONS INCLUDE CHEMICALLY
APPLIED FINISHES.

ALL MACH SURFACES APPROX.
CONCENTRICITY ON MACH DIA .004 TIR.

125 ✓

TOLERANCES:			
FRACTIONS	DECIMALS	ANGLES	
$\pm 1/64$	$\pm .01$	$\pm 5^\circ$	
	$\pm .005$		

	MATERIAL:
	NOTE #6

FINISH:
NOTE #5

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ENGINEER		
CHECKER		
DRAFTSMAN	JK	07-04-0

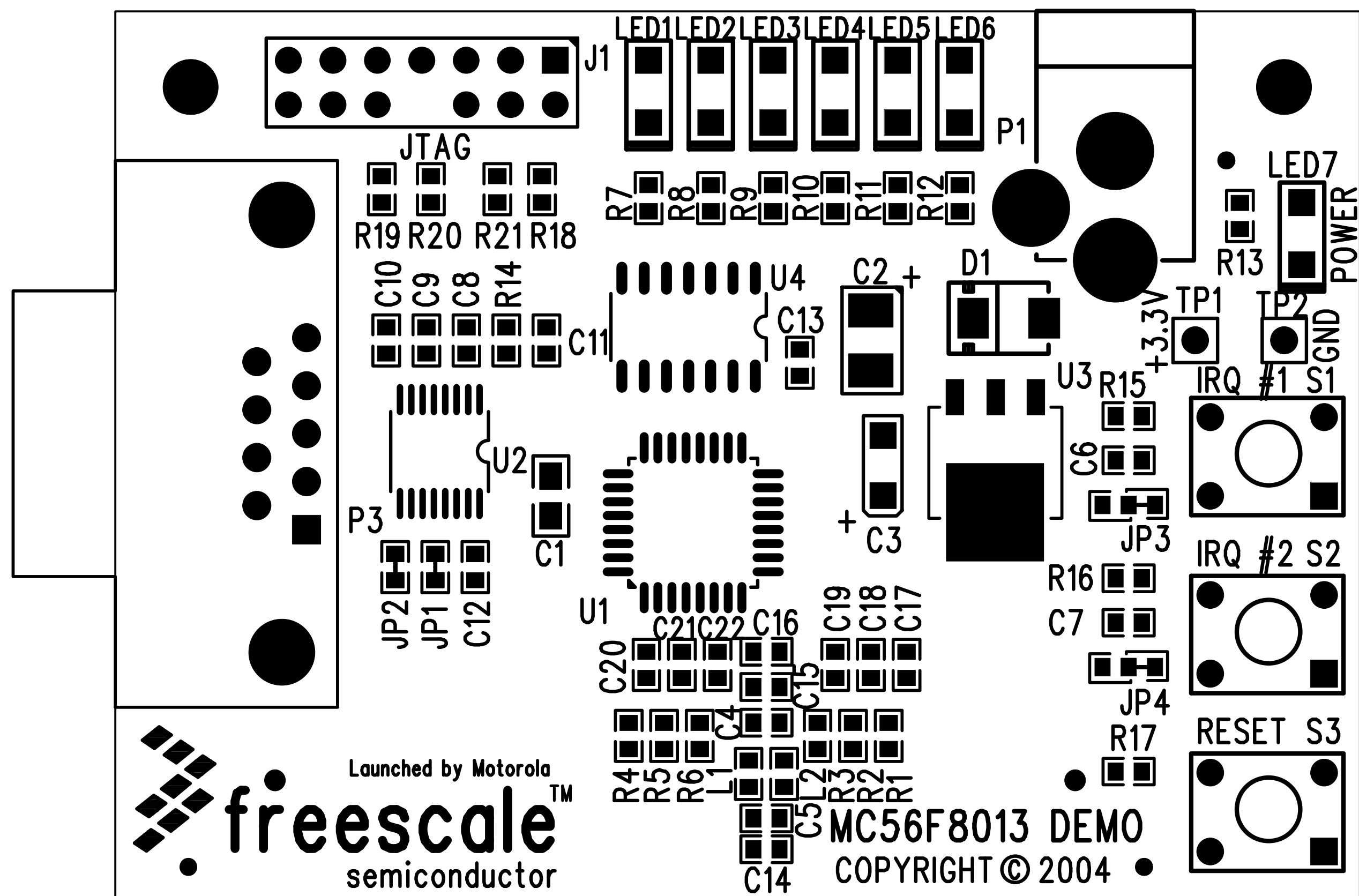
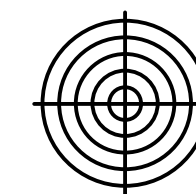
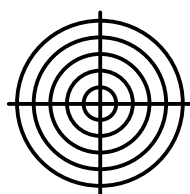
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ARTWORK DESIGNED BY:

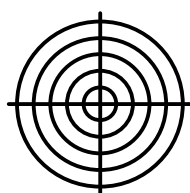
TRAX Design L.C. P.O. Box 204431
AUSTIN, TEXAS 78720 (512) 733-5075

FABRICATION DRAWING,
MC56F8013DEMO

SIZE A	ARTWORK FOR: freescale semiconductor		REV
SCALE 1:1	TD# TDMOT102	DWG#	SHEET 1 OF 1



TOP ASSEMBLY



ARTWORK DESIGNED BY:

TRAX Design L.C.

P.O. Box 204431
AUSTIN, TEXAS 78720
(512) 733-5075

ARTWORK FOR:

freescale semiconductor

BOARD NAME:

MC56F8013DEMO

JOB #:

TDMOT102

DATE:

07-04-04