



ents	
	Sheet 2
.MLH)	Sheet 3
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ETHERNET PHY1 (BCM89810)	Sheet 5
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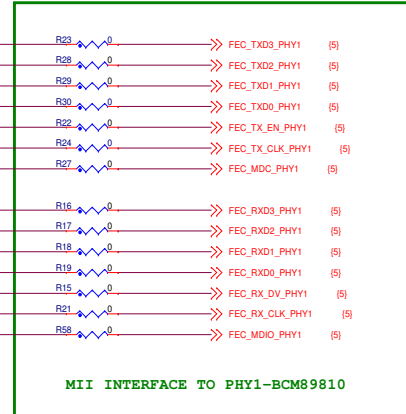
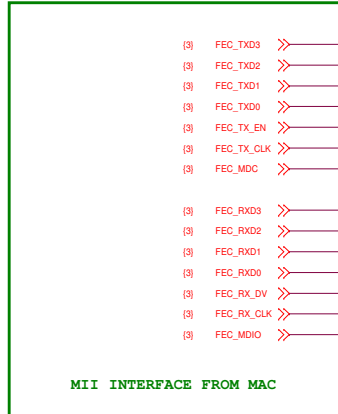
Revisions

Rev	Description	Date	Designer
X1	Initial Draft	24-SEP-2013	K.S Chelvi
	RN1, RN3, RN4 pin swapping done for easy PCB layout routing	03-OCT-2013	K.S Chelvi
	Re-sequencing completed	09-OCT-2013	K.S Chelvi
A	Vendor Feedbacks implemented	18-OCT-2013	K.S Chelvi
	DS1,DS2 changed to GRN LEDs		
	Proto Release (A085)	28-OCT-2013	K.S Chelvi
A1	DNP Resistors made populated	6-FEB-2014	K.S Chelvi

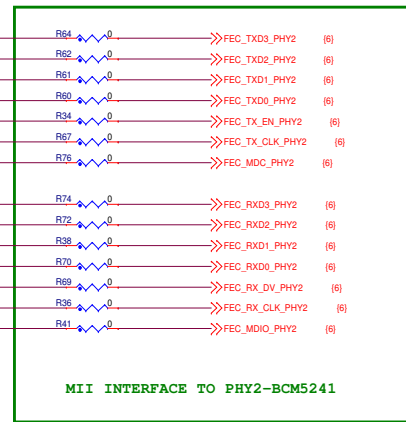
MPC5604E-MINI

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Date: Thursday, February 06, 2014		Sheet 1 of 6	

**SPC5604EEF1MLH
MAC**



BCM89810



BCM5241 (DEFAULT)

