

5	4	3	2	1
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Add to USB compatibility!

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Title				Interconnections4			
Size	Document Number						Rev
A3	MPC8560ADS						A
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MPC8560ADS Rev.A (084-00169-3)

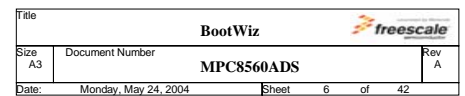
SHEET	CONTENTS
01	INTERCONNECTIONS 1
02	INTERCONNECTIONS 2
03	INTERCONNECTIONS 3
04	INTERCONNECTIONS 4
05	THIS PAGE
06	BOOT WIZ
07	CLOCKING
08	CPM PART
09	CPM CONTROL
10	CPM_ATM1_155
11	CPM_ATM2_155
12	CPM_ATM_POWER
13	CPM_CLOCKS
14	CPM_FETHERNET
15	CPM_RS-232
16	CPM_SWITCHES_1
17	CPM_SWITCHES_2
18	CPM_SWITCHES_3
19	CPU CONFIG & RESET1
20	CPU CONFIG & RESET2

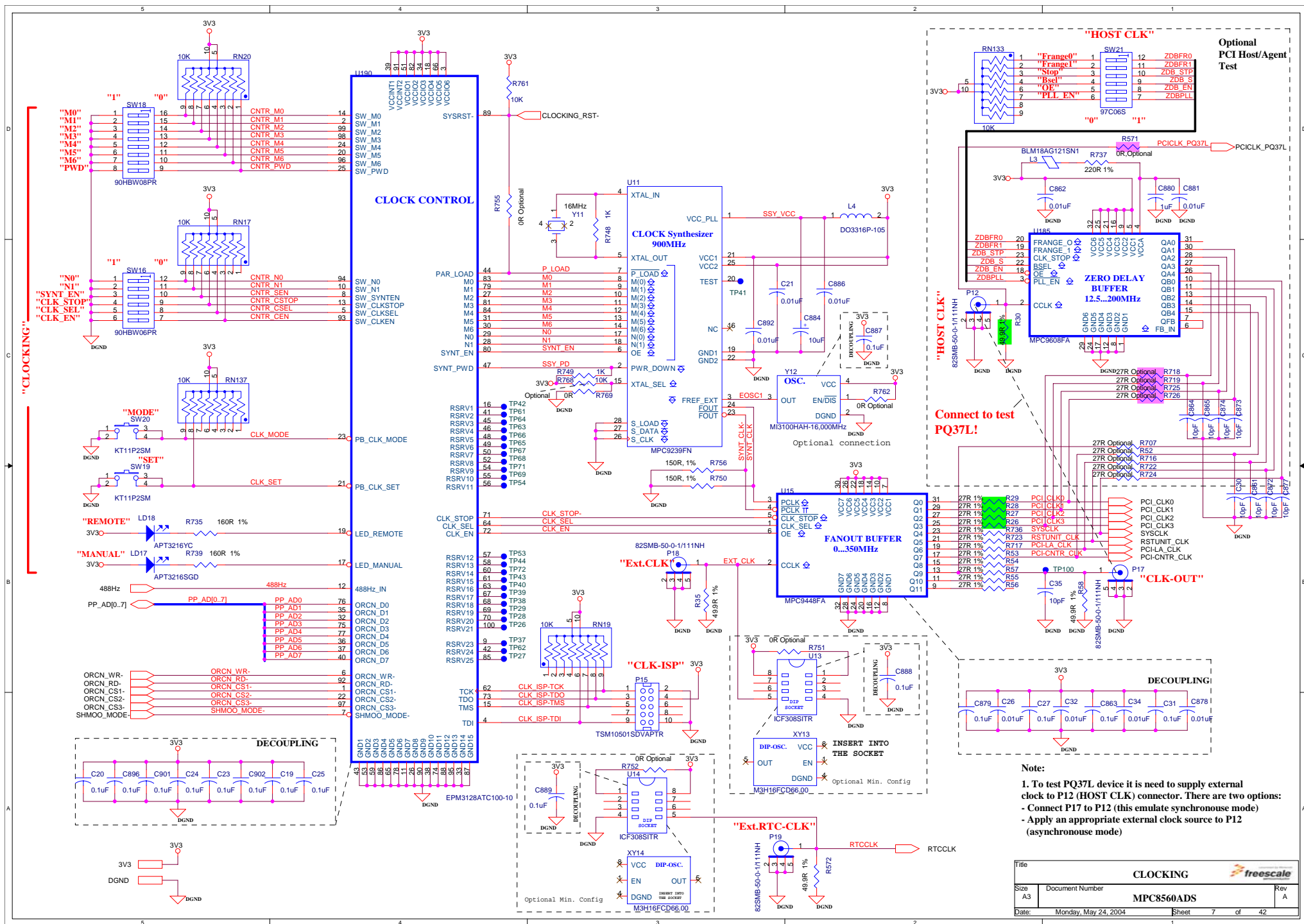
SHEET	CONTENTS
21	CPU POWER
22	DDR SDRAM PART
23	DDR SDRAM SLOTS
24	DDR TERMINATION
25	JTAG, ORCN, BOOT I2C
26	LB_FLASH & EXPANSION
27	LB_TERMINATION
28	LB_TRANSCEIVER & RAM
29	MPC8560 PACKAGE
30	PCI SLOTS
31	PCIX SLOT & BUS SWITCHES
32	PCIX/PCI CONTROL
33	PCIX/PCI PART & LA CONN.
34	PS1_CORE & DDR
35	PS2_TSEC
36	PS3_PERIPHERY & CONTROL
37	RAPID IO
38	TSEC 1ST CHANNEL
39	TSEC 2ND CHANNEL
40	TSEC COMMON

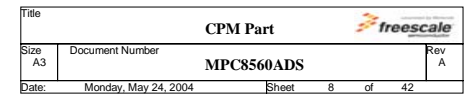
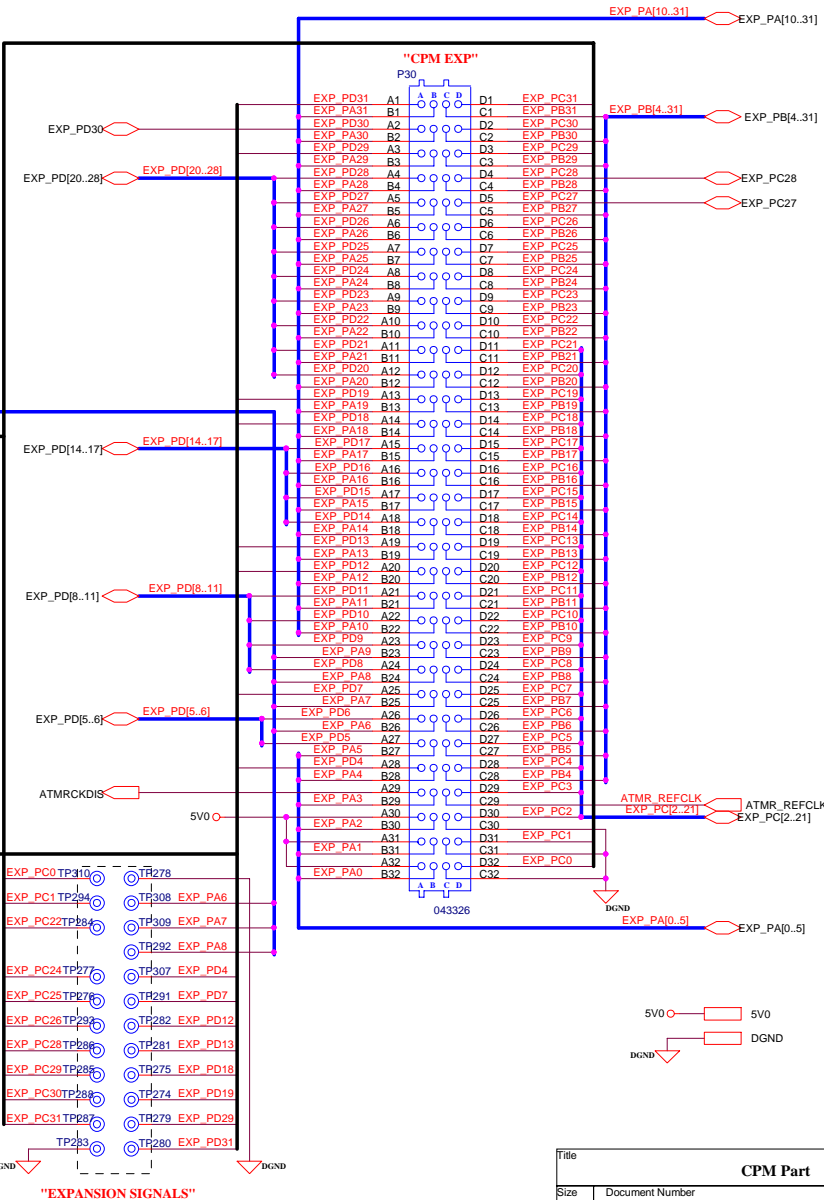
SHEET	CONTENTS
41	TSEC CONNECTORS
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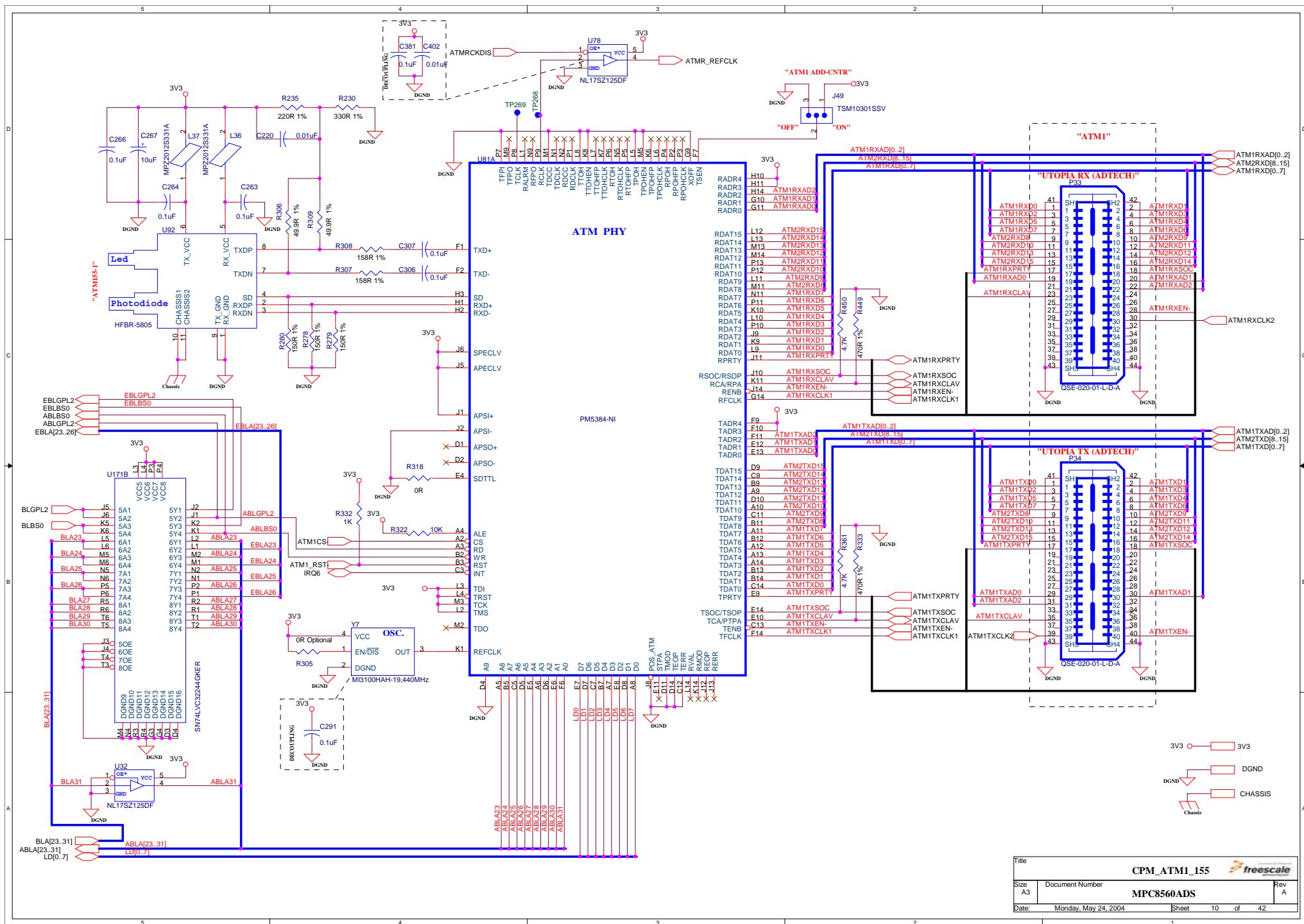
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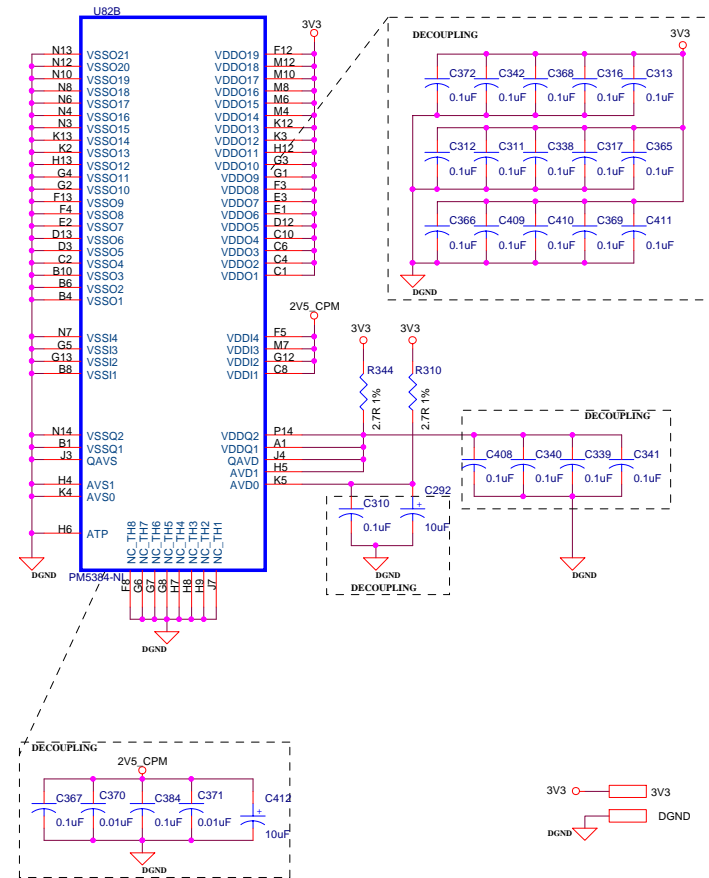
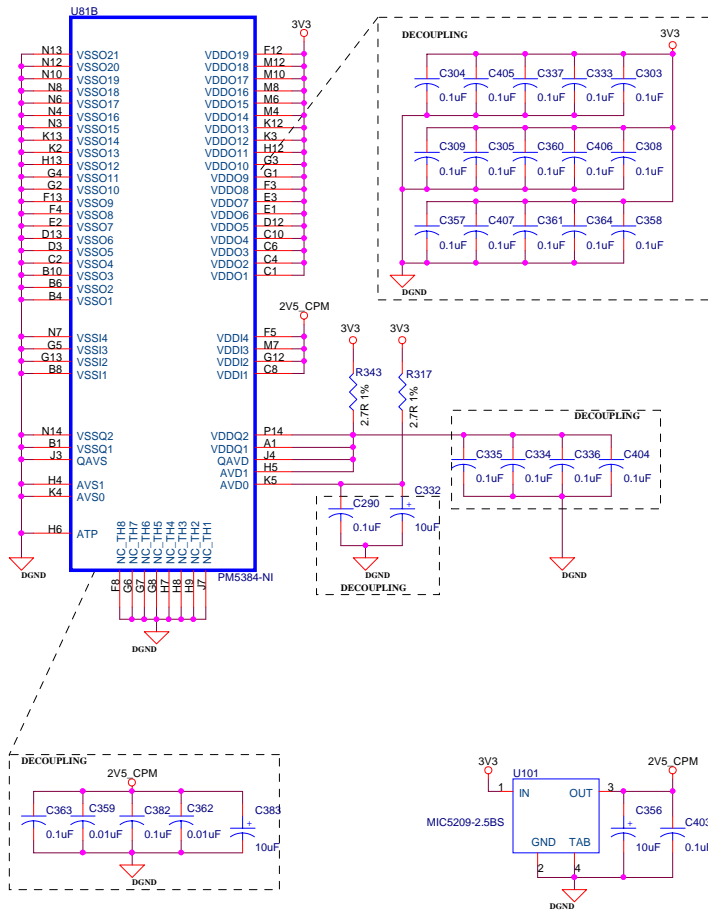
- Notes marked as **"Add to test PCI2 PQ37L!"** means updates should be done to provide PCI2 interface testing of the PQ37L device together with PQ37L_PCI2 adapter only
- Notes marked as **"Add to ATX-PS compatibility"** means updates should be done to provide reliable operation with various types of the ATX-PS
- Notes marked as **"Add to test PCIX mode"** means updates should be done to provide PCIX bus functionality
- Notes marked as **"Add to USB compatibility"** means updates should be done to provide PQ37L USB bus functionality
- Components marked with  should be assembled for PQ3 version of the ADS only
- Components marked with  should be assembled for PQ3-Lite version of the ADS only

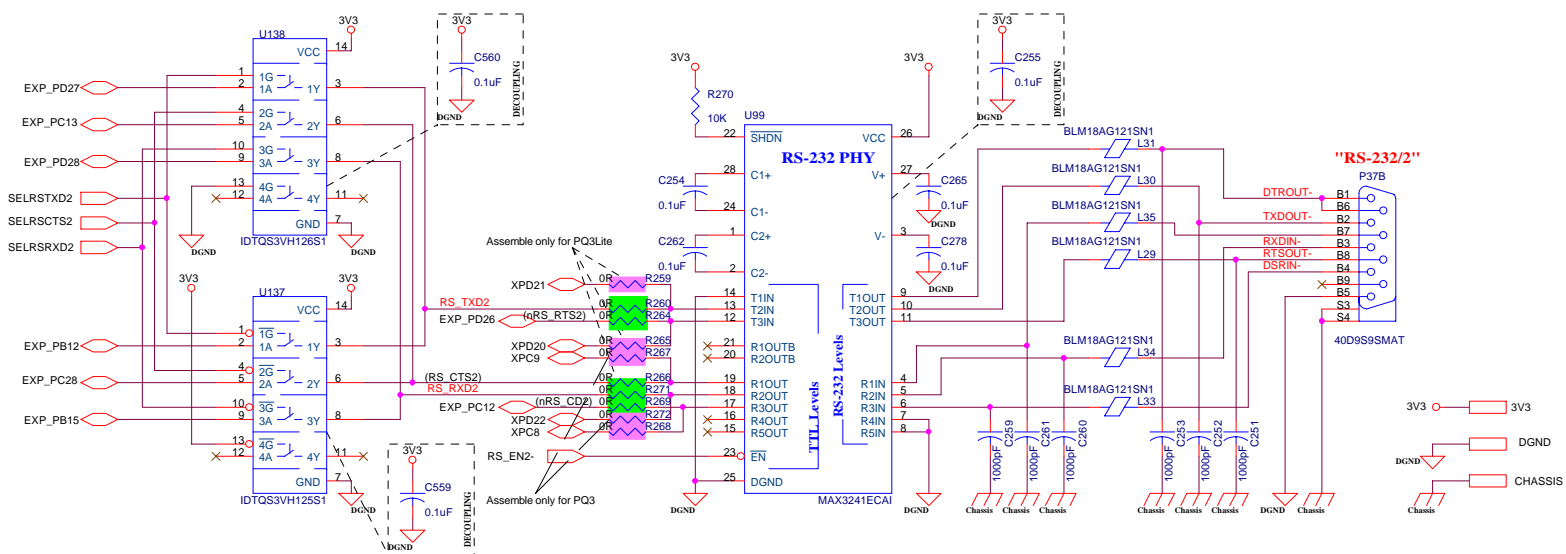


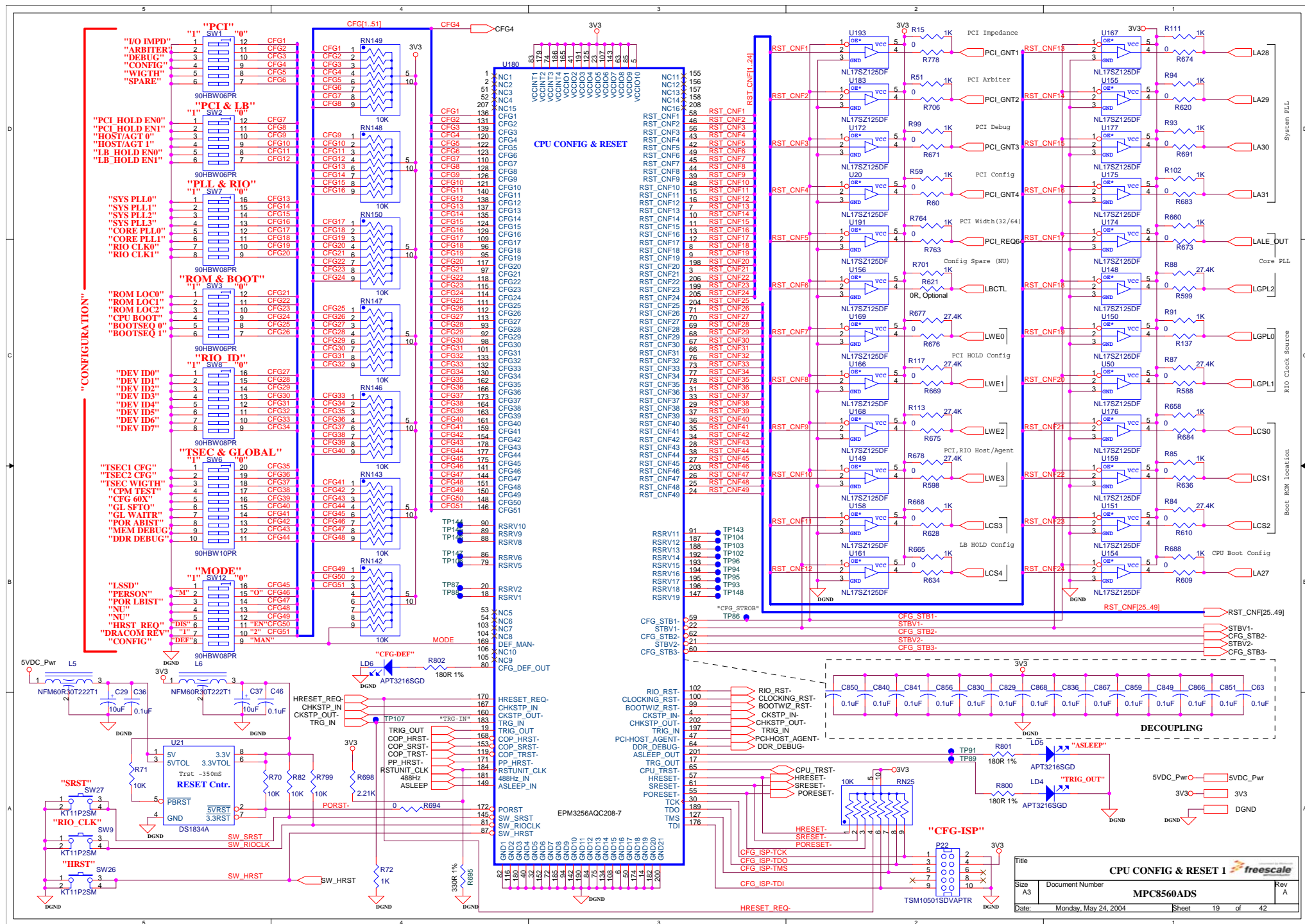


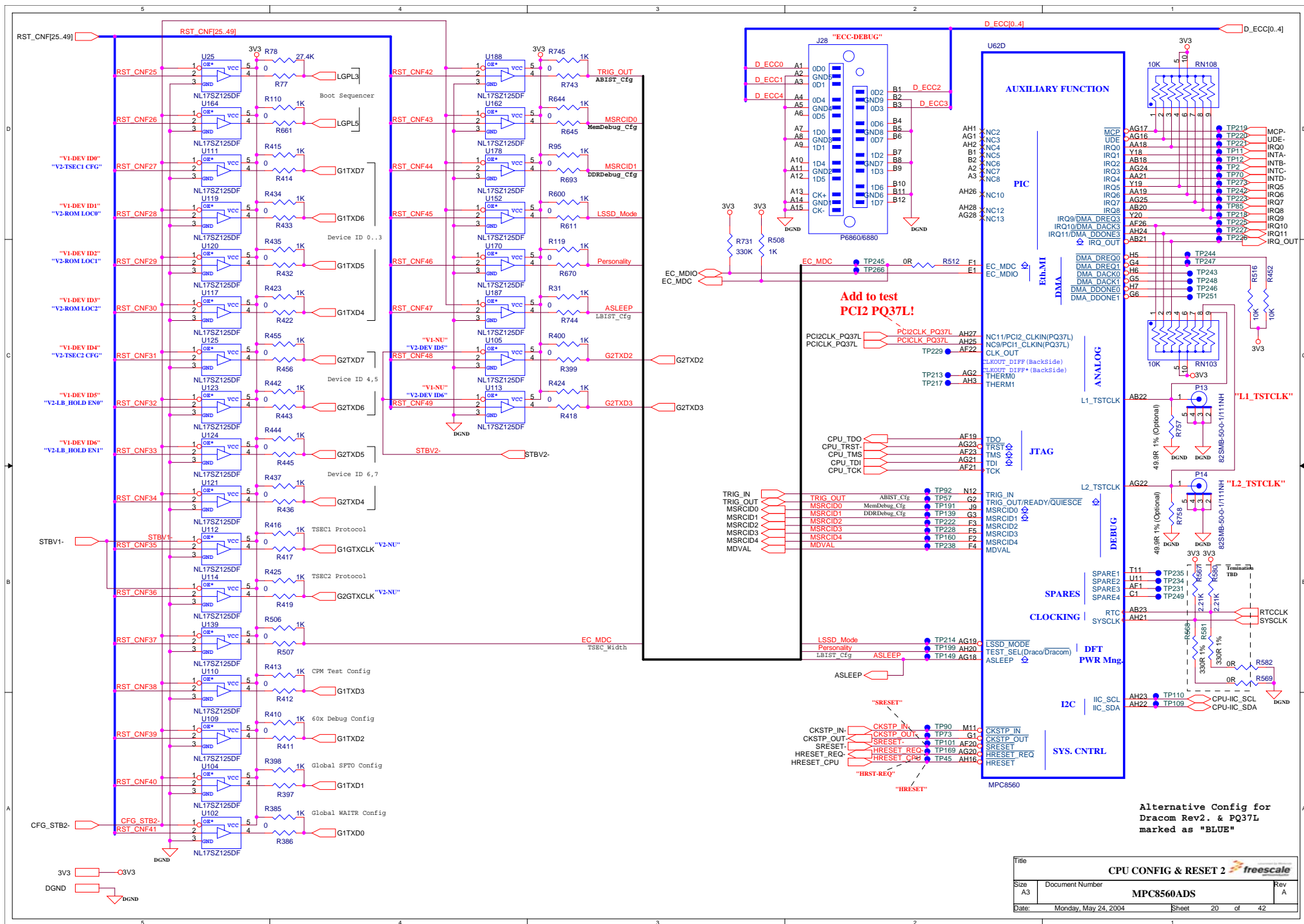


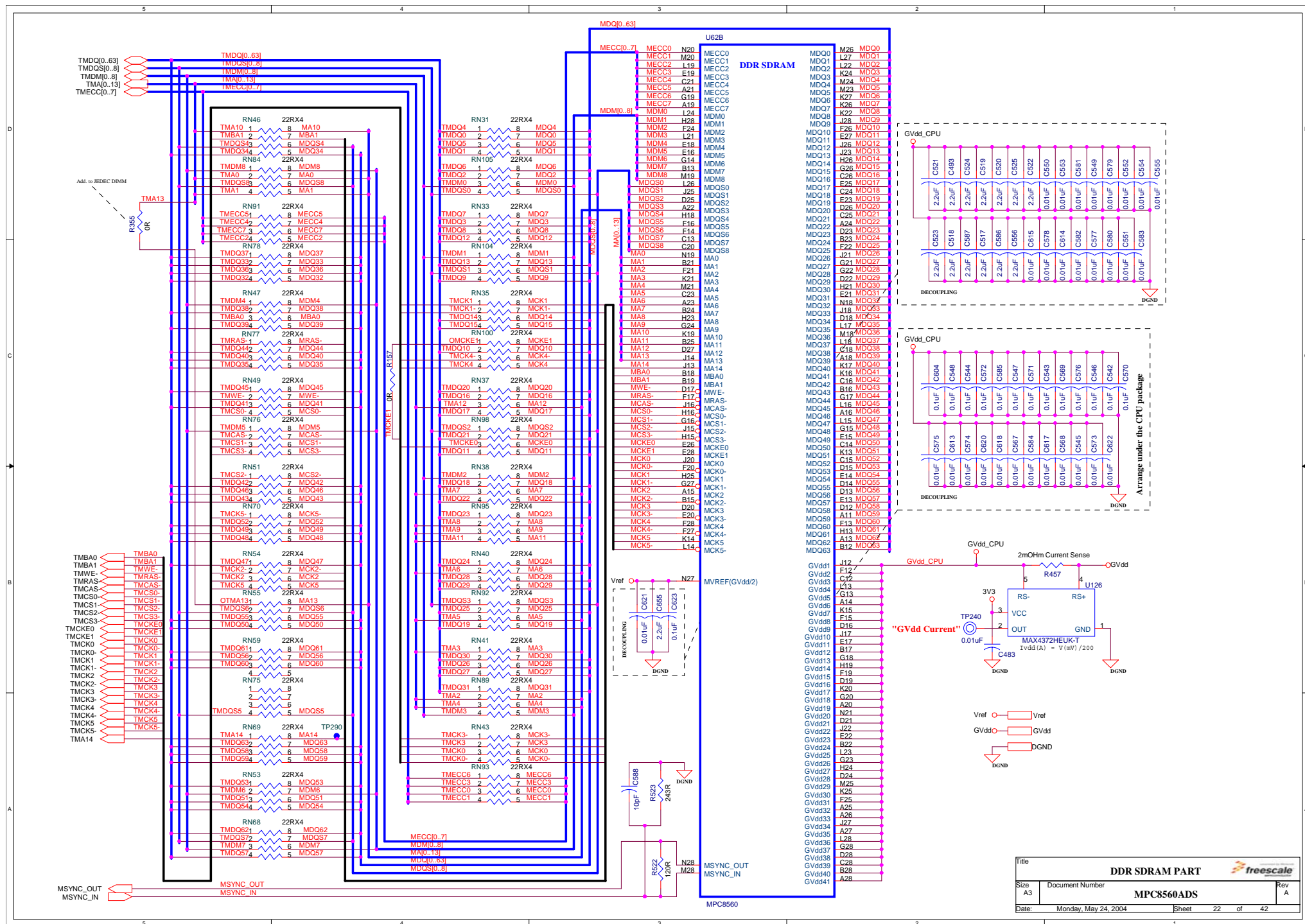


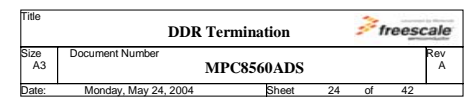


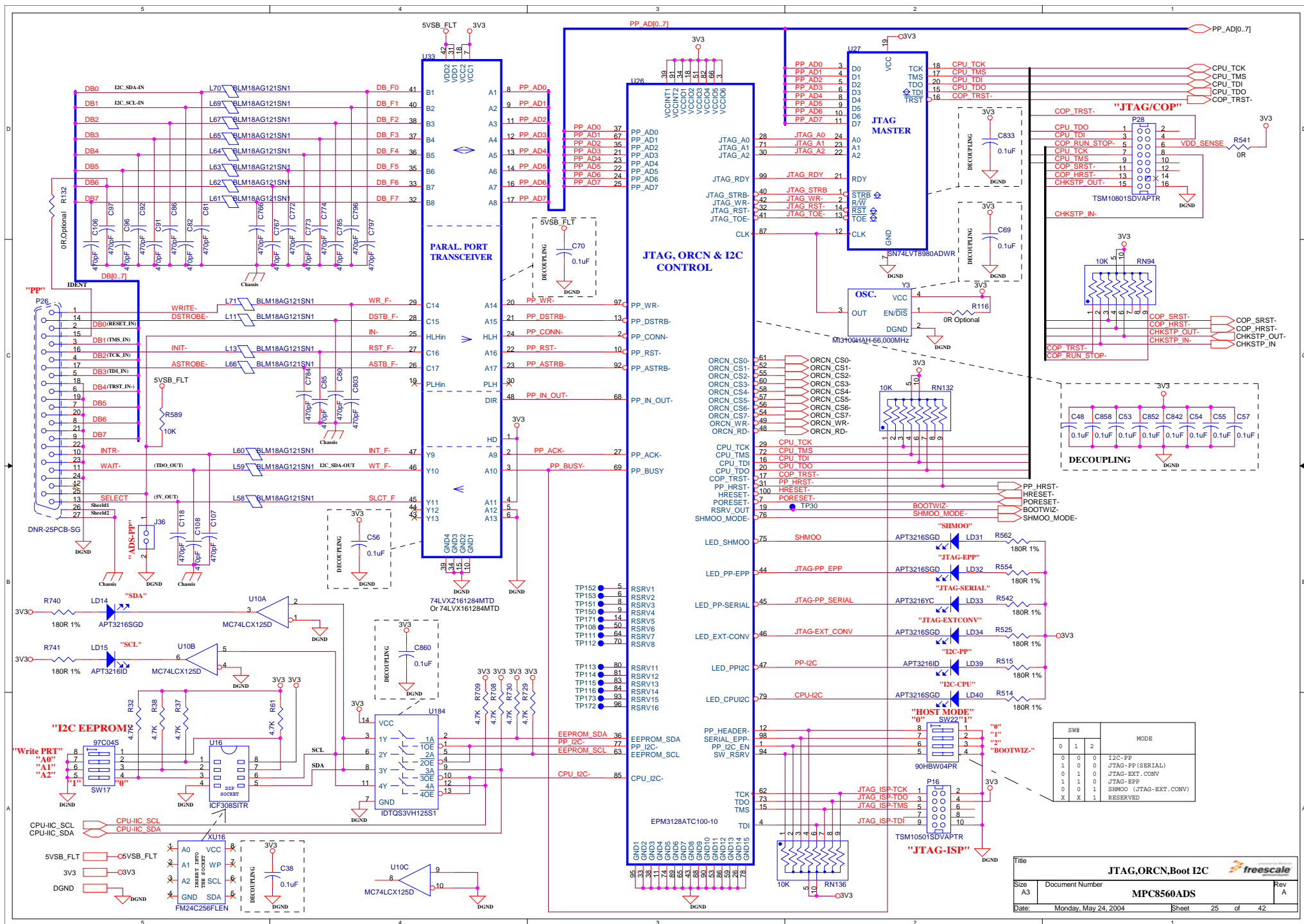


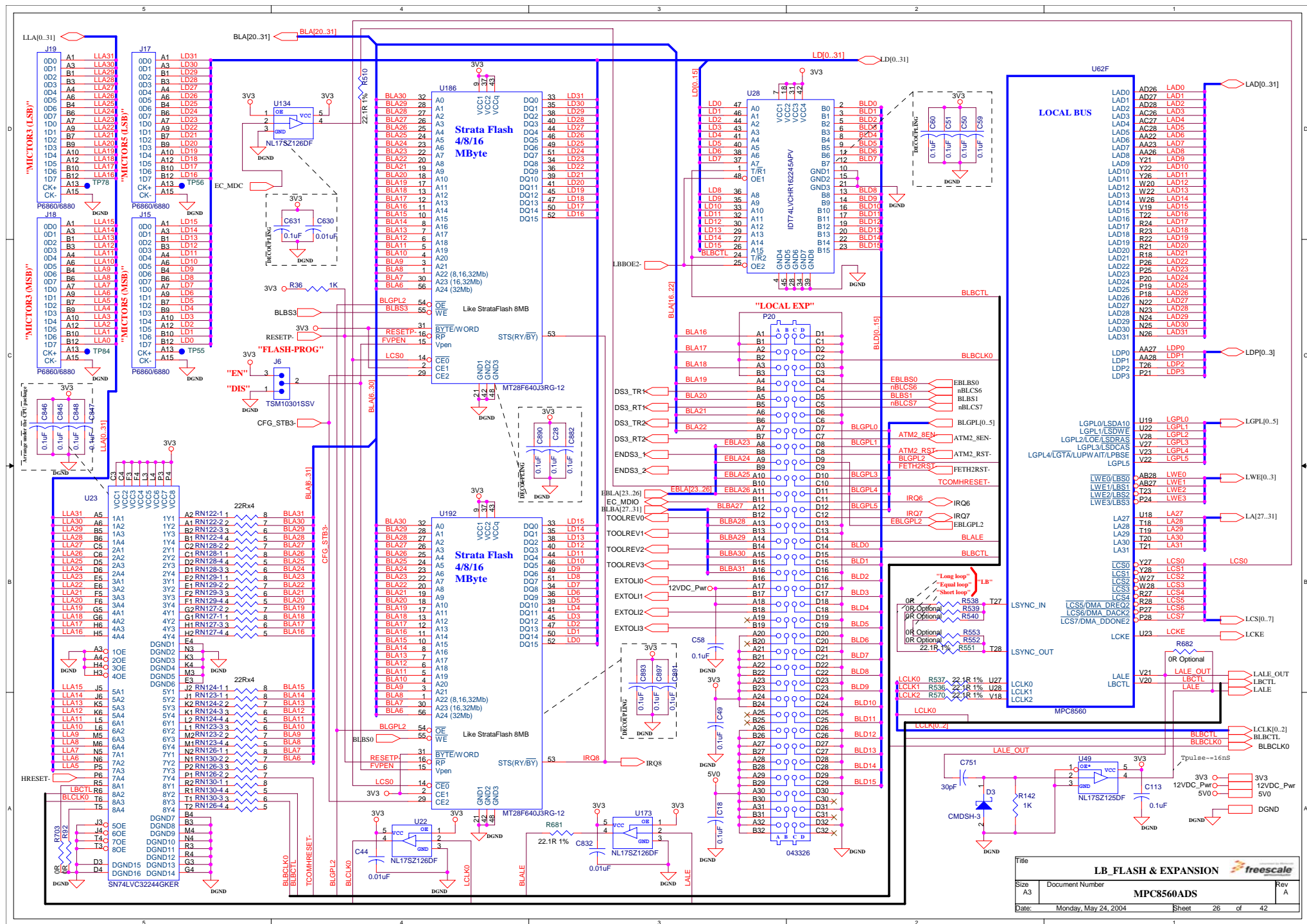


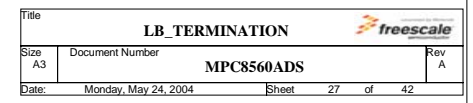


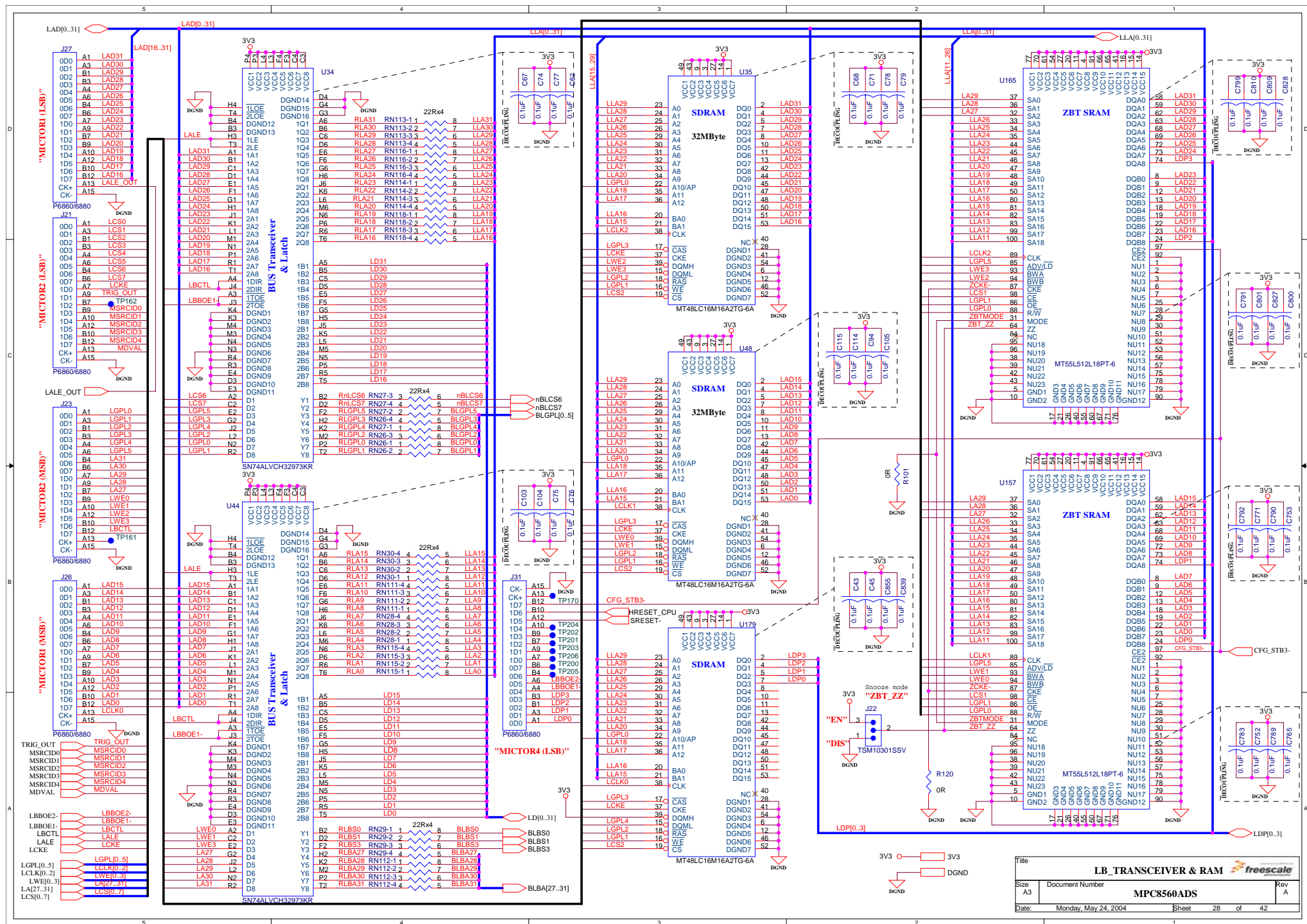












CPM INTERFACE

DEBUG & DMA

U62J

	AH	AG	AF	AE	AD	AC	AB	AA	Y	W	V	U	T	R	P	N	M	L	K	J	H	G	F	E	D	C	B	A
1	NC2	NC3	SPARE10 SPARE10	OVdd	P025 P025 P025	P005	P016	P015	P024 P024 P024	PC24	PC23	PC9	PC8	PB25	PB24	PB1 NC for P037L	PB1 NC for P037L	PA16	PA15	PA2 NC for P037L	PA1 NC for P037L	OKSTP- OUT-	EC- MDIO	EC- MDIO	OVdd	SPARE4 SPARE11	NC5	NC1
2	NC4	THERM1 THERM1	GND	P031	P004	P017	P014	P014	PC25	PC22	PC10	OVdd	PB26	PB23	GND	PA31	PA17	PA14	PA1 NC for P037L	PA1 NC for P037L	TBR- OUT-	MSRC- R02	EC- CLK25	EC- CLK25	GND	NC6	NC7	
3	THERM1	GND	P030	GND	P003	P018	P013 NC for P037L	P013 NC for P037L	PC26	PC21	PC11	GND	PB27	PB22	OVdd	PA30	PA18	PA13	PA1 NC for P037L	PA1 NC for P037L	MSRC- R02	MSRC- R02	GND	TSEC1- R005	TSEC1- R05	GND	NC8	
4	OVdd	P030	GND	P030	OVdd	P022	GND	P012 NC for P037L	P07	OVdd	PC28	PC12	PC7	PB28	PB21	PA1 NC for P037L	GND	PA19	OVdd	GND	DMA- DREQ0	DMA- DREQ0	OVdd	TSEC1- R007	TSEC1- R007	GND	TSEC1- R006	LVdd
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TSEC1&2 INTERFACE

PCI-X/PCI INTERFACE

CLOCK, RESET & JTAG

EPIC

DDR SDRAM INTERFACE

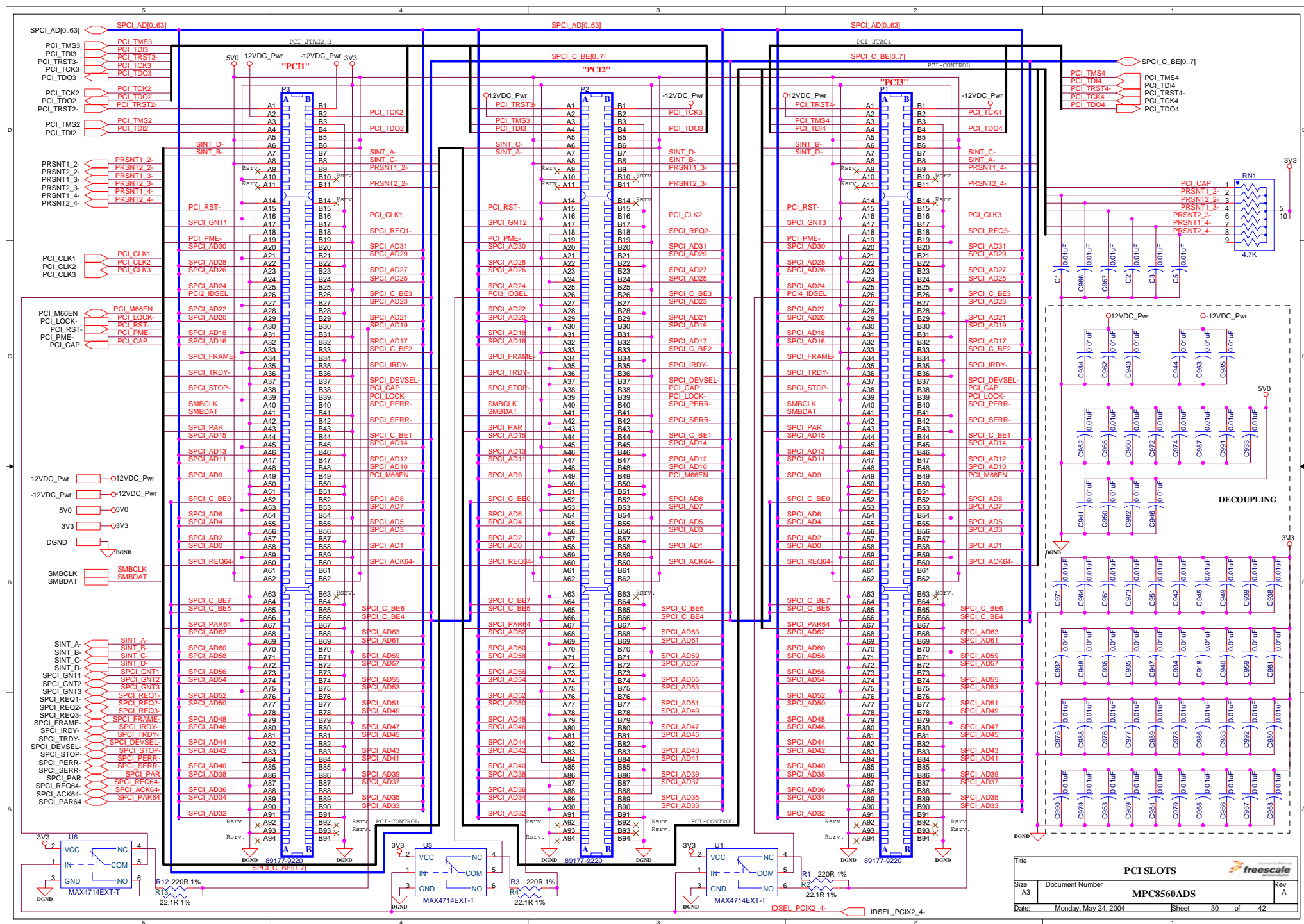
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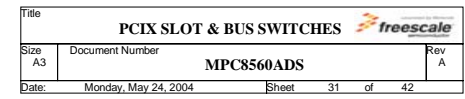
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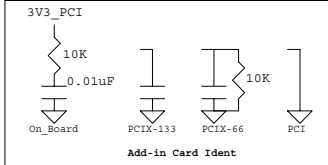
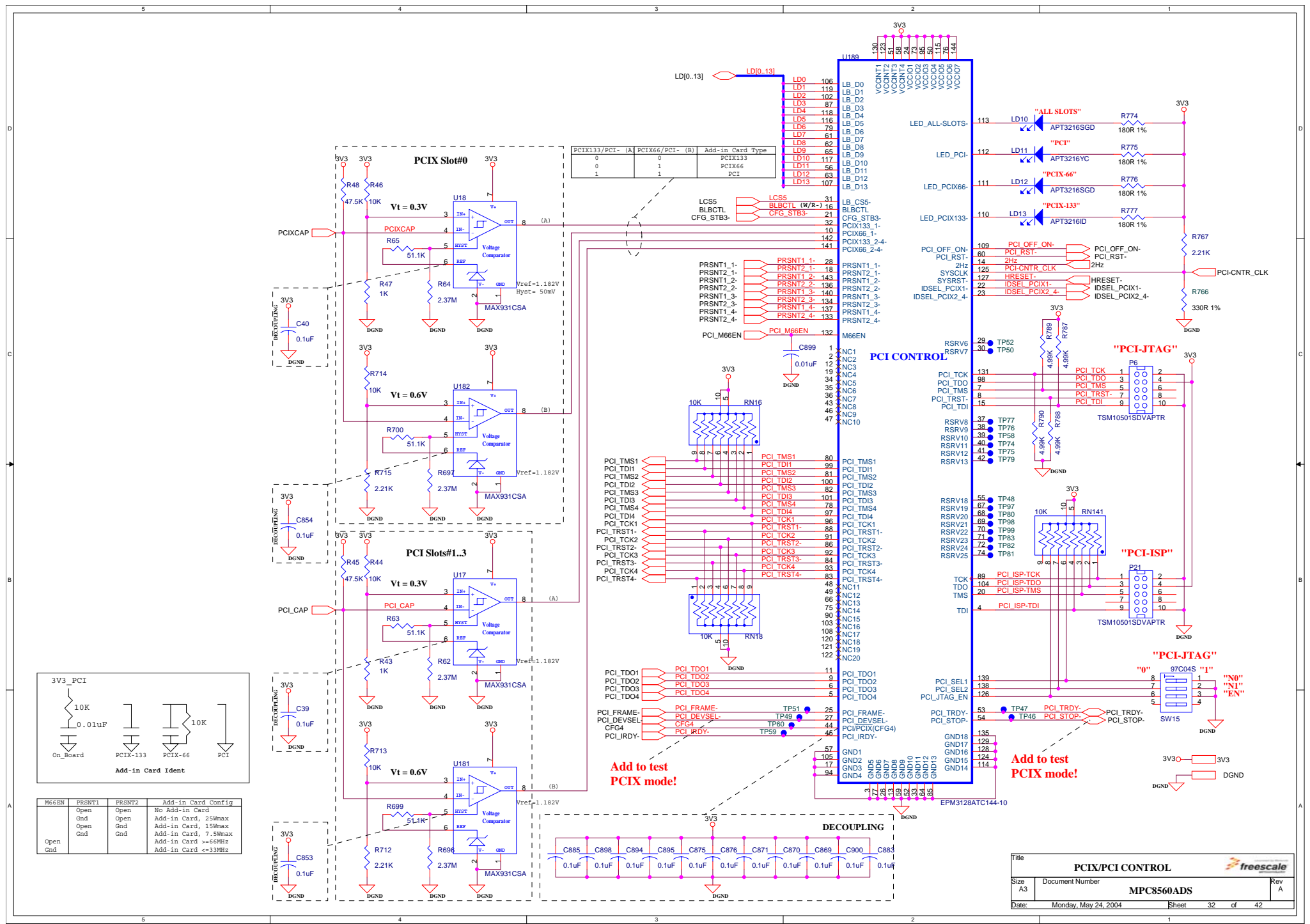
Legend:

- The Pin have alternative function for PQ37L device
- The Pin have PCI1 bus function for PQ37L device
- The Pin have PCI2 bus function for PQ37L device

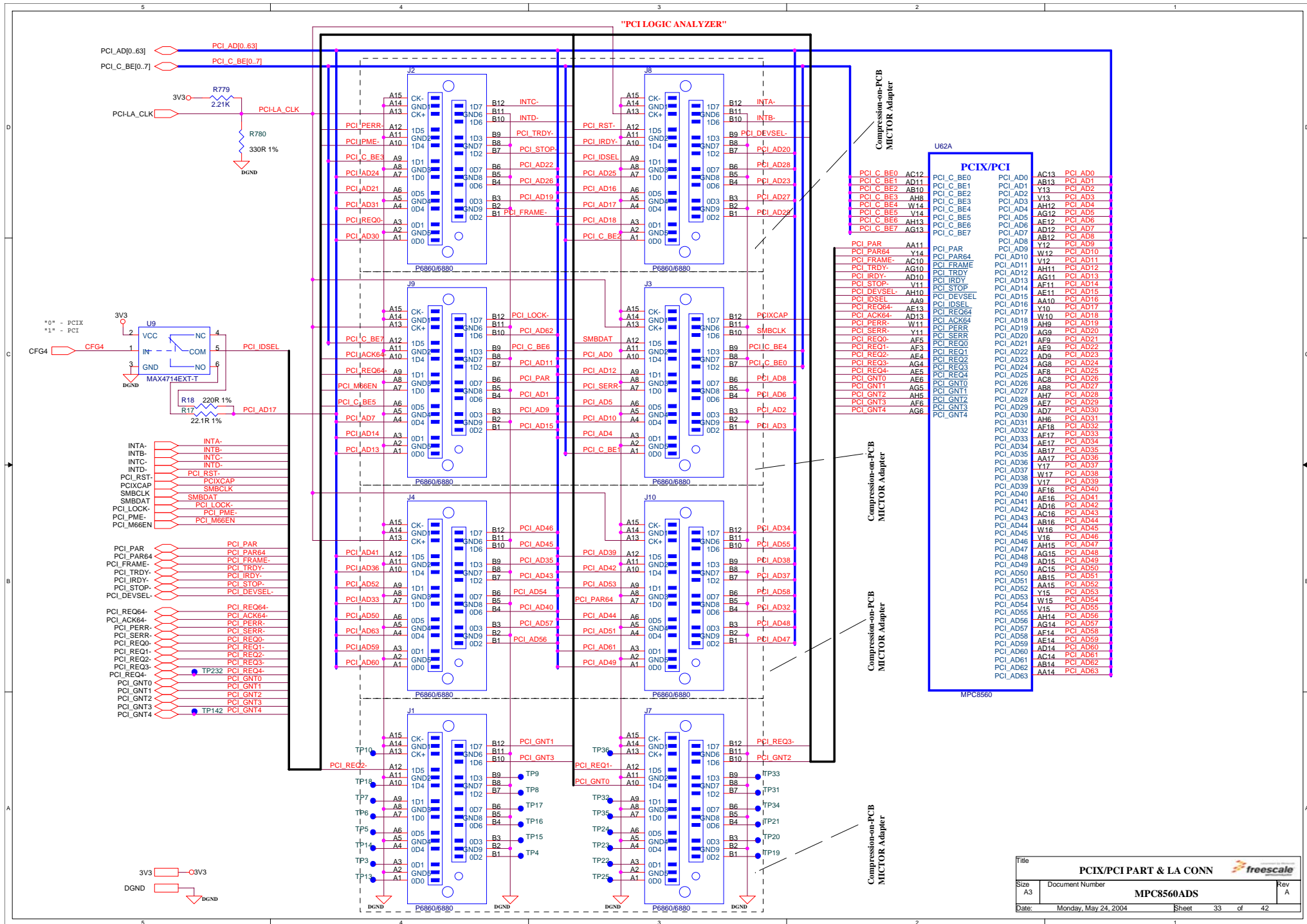
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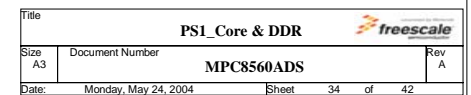


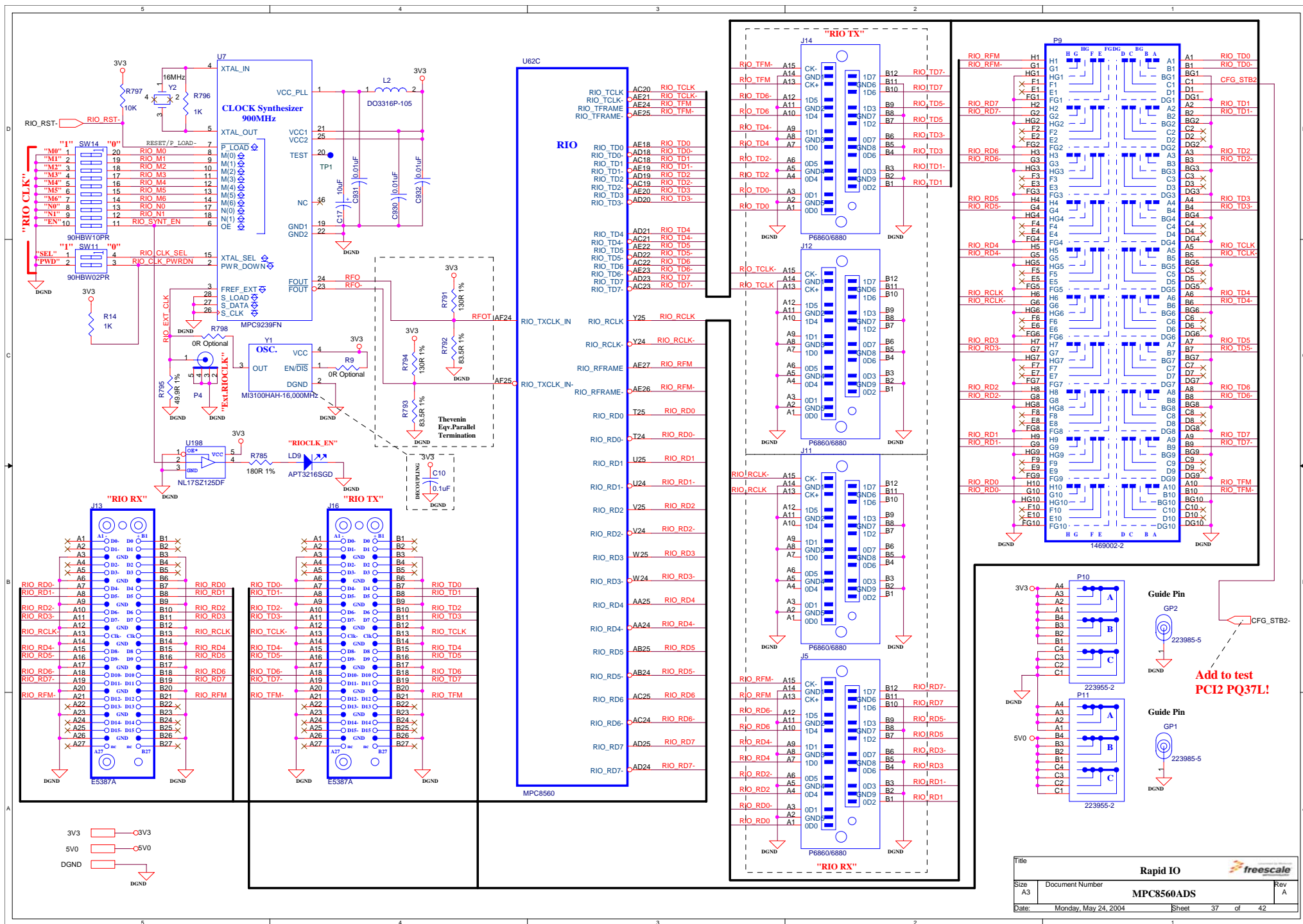


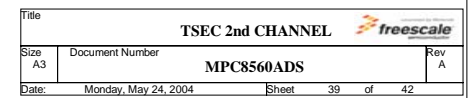


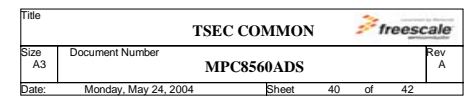
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Gnd	Gnd	Open	Add-in Card, 25Wmax
Open	Open	Gnd	Add-in Card, 15Wmax
Gnd	Gnd	Gnd	Add-in Card, 7.5Wmax
Open	Open	Open	Add-in Card >=66MHz
Gnd	Gnd	Gnd	Add-in Card <=33MHz



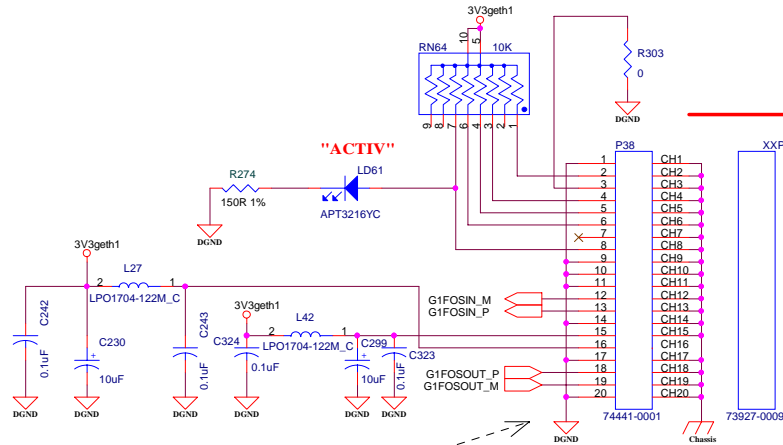




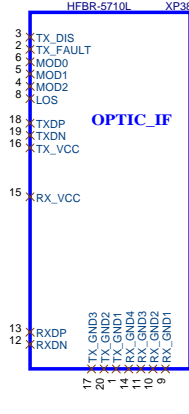




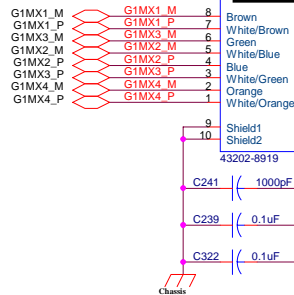
TSEC CHANNEL 1



Insert into the Socket

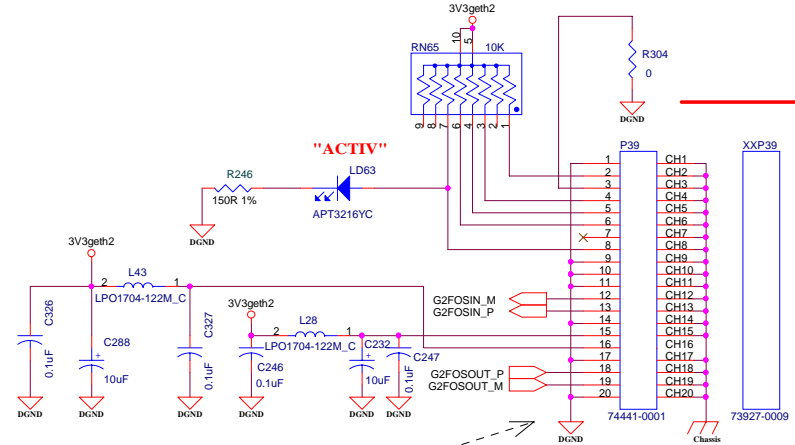


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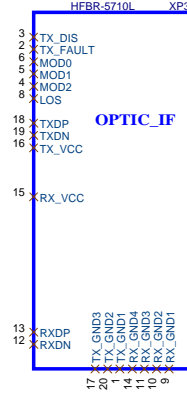


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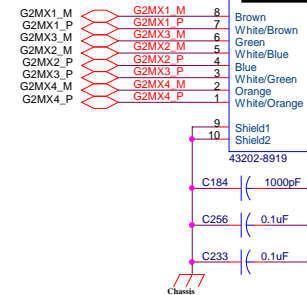
TSEC CHANNEL 2



Insert into the Socket



"10/100/1000 BASE-T"



"TSEC2"

