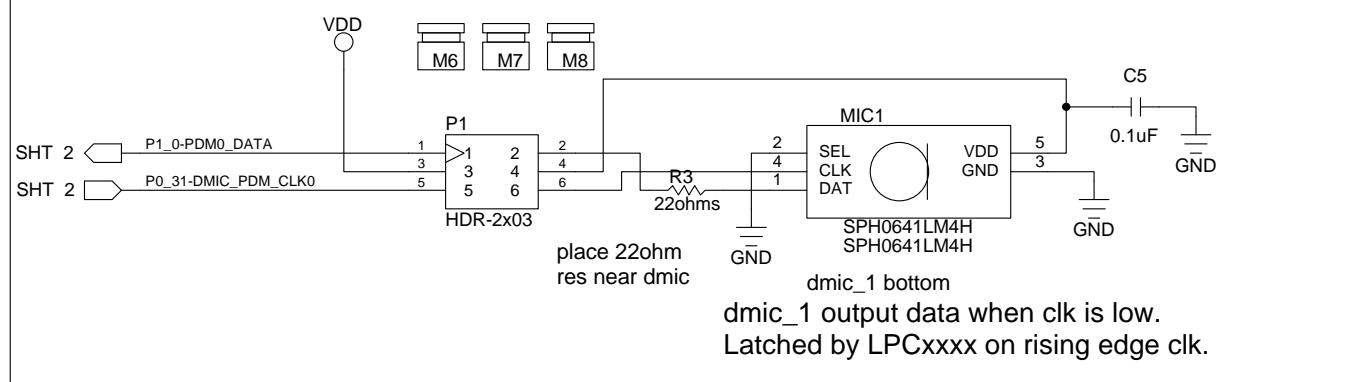
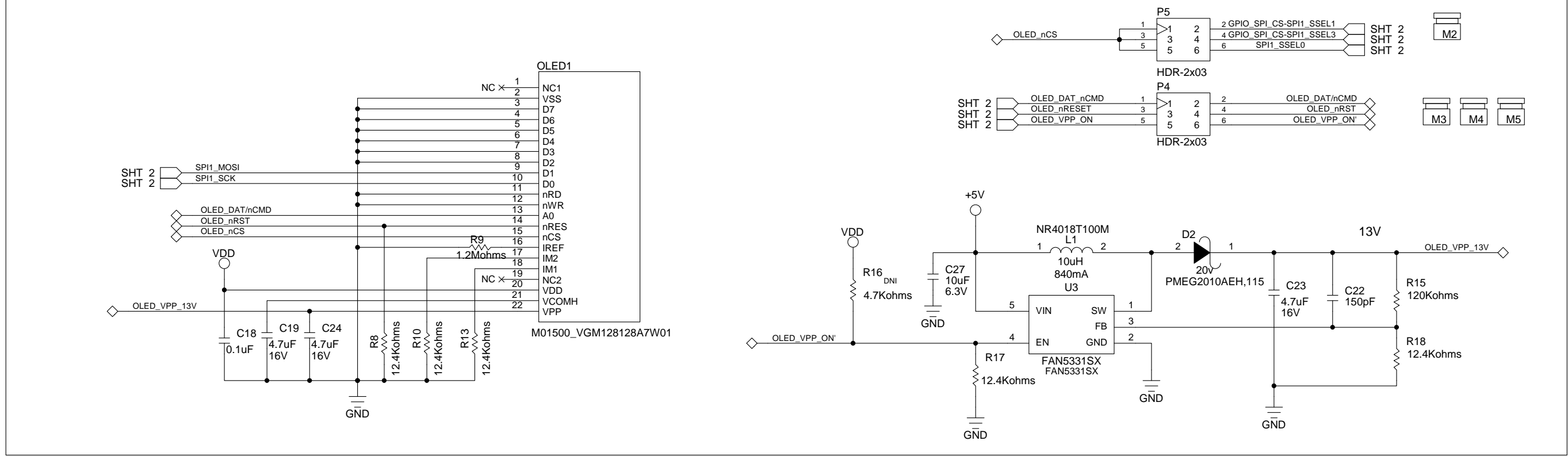


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	- Initial release; renumber refdes	6/16/2015	
A	- Move "OLED_VPP_ON" to P0_29 - Add audio codec circuit	10/23/2015	
A1	- Add M1 hdr shunt; sht 2	12/05/2015	

DMIC circuit

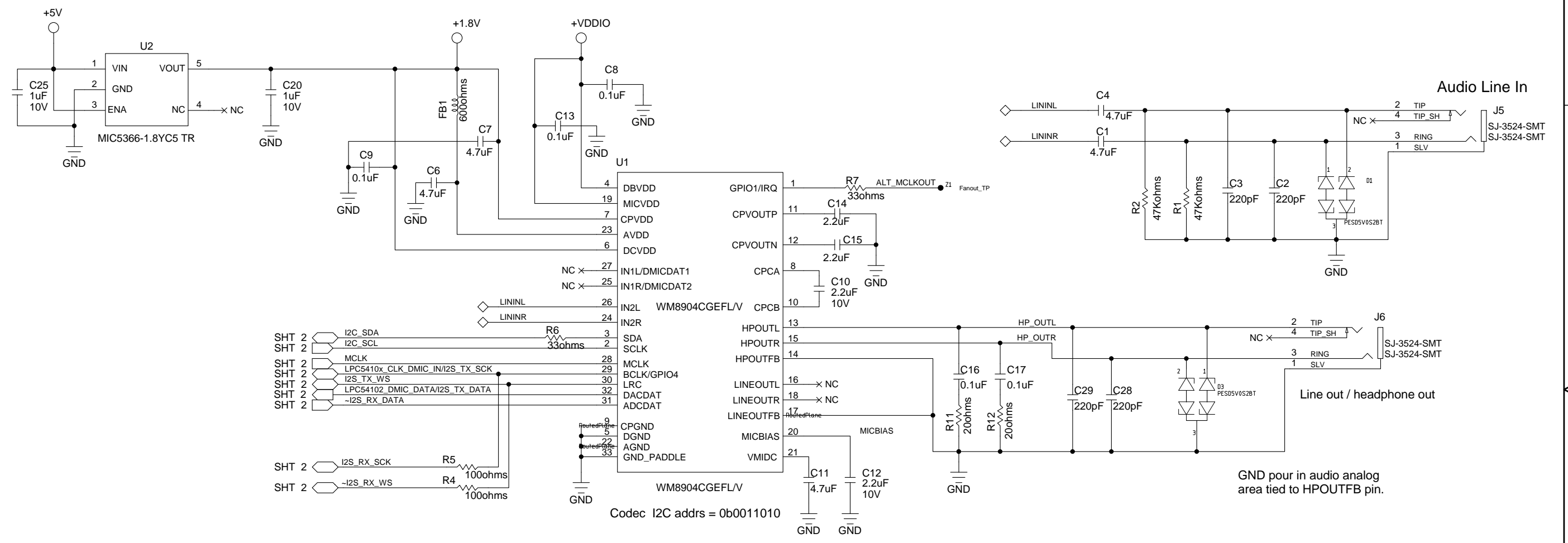


OLED circuit



CONTRACT NO.		Sensor Hub Block Diagram	
APPROVALS	DATE	NXP Semiconductors	
DRAWN D. Consiglio	12/5/2015	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE D	DWG. NO. MicAudOled-SHLD-01 REV. A1
ISSUED	12/05/15	SCALE	SHEET 1 OF 03

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



CONTRACT NO.		Audio Codec			
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
DRAWN	12/5/2015				
CHECKED					
ISSUED	12/05/15				
		SIZE	FSCM NO.	DWG. NO.	REV
		D		MicAudOled-SHLD-01	A1
		SCALE			SHEET 3 OF 03