2 - DDR SDRAM

NOTE: Keep DDR SDRAM close to CPU, so that Address, Data, and Control signal paths are very short, and the same length.

32MB DDR SDRAM

NOTE: Locate bypasses between VDD & VSS pin pairs.
8 - KEYPAD & ZIGBEE INTERFACES

NOTE: The processor scans the rows and reads the columns. When not scanning, the processor should hold the rows low, if Keypad Interrupt is being used.

KEYPAD INTERRUPT

TO GRAYHILL 88JB2
4 ROW x 5 COLUMN KEYPAD

TO FREESCALE MC13192U
ZIGBEE TRANSCEIVER MODULE
NOTE: The USB differential pairs USB1_M, USB1_P, & USB2_M, USB2_P need to be routed such that they have a differential impedance of 90 ohms.

Please route the USB pairs on the top signal layer with a solid ground plane underneath.

NOTE: Keep 33.2 ohm resistors and 47 pF caps close to processor USB pins.

9 - USB HOST INTERFACES
POWER & PWM BRIGHTNESS
CONTROL FOR ERG 8mA23003
BACKLIGHT INVERTER

NOTE: The "C" Jumper needs to be removed from the ERG 8mA23003 Board, to enable the Backlight PWM Control.

INTERFACE TO
SHARP LQ084S3DG01
8.4" TFT SVGA LCD