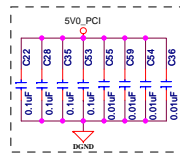
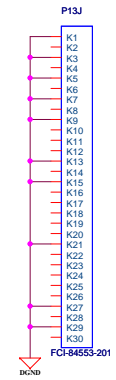
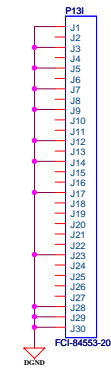
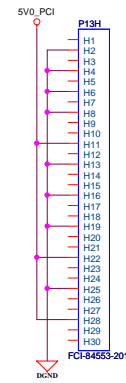
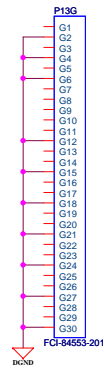
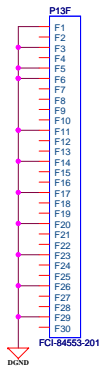
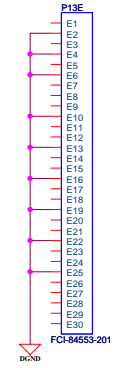
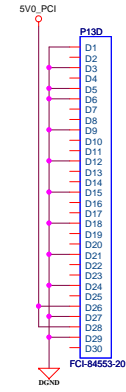
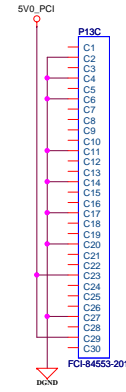
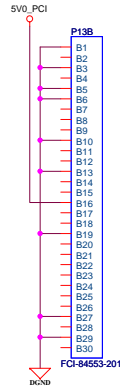
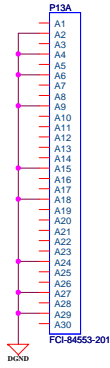
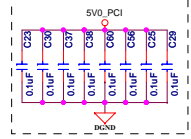
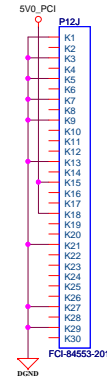
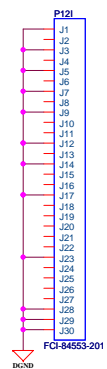
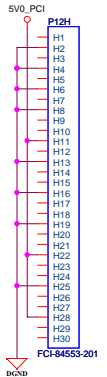
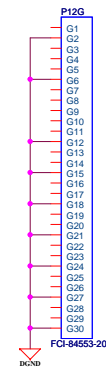
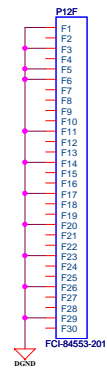
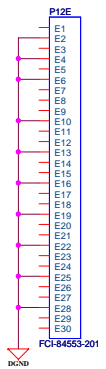
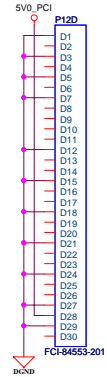
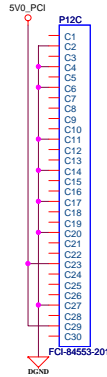
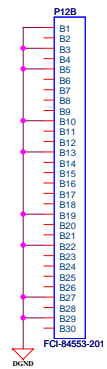
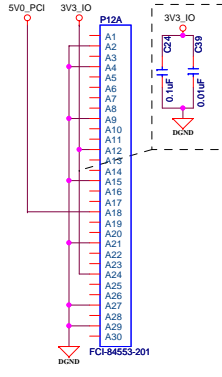




PCI1 Bus, UART, GPIO

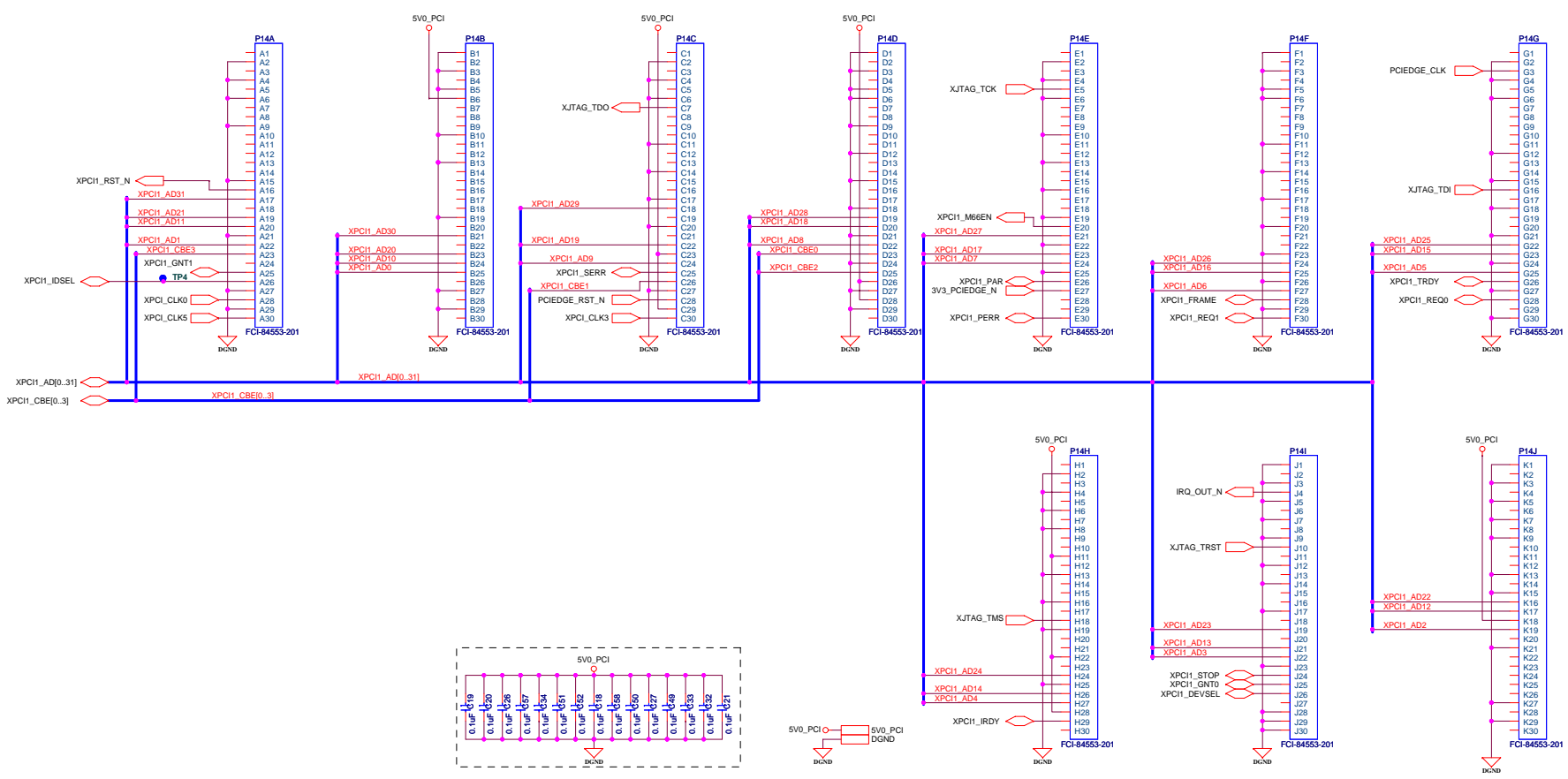


PCI1 Bus, UART, GPIO



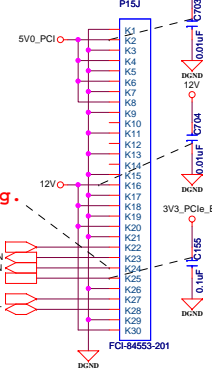
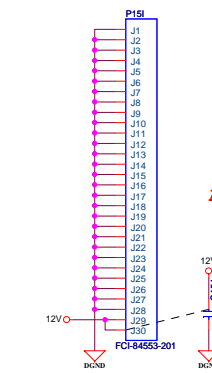
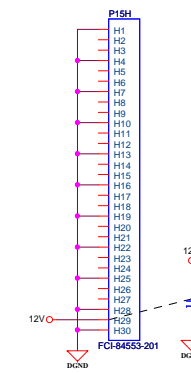
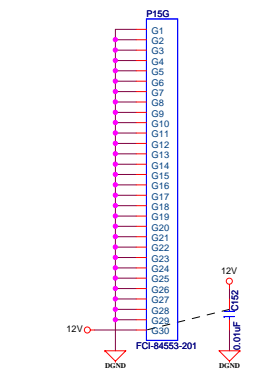
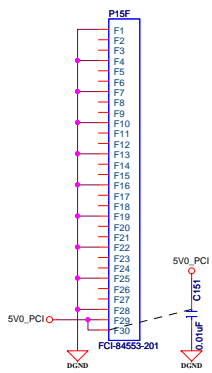
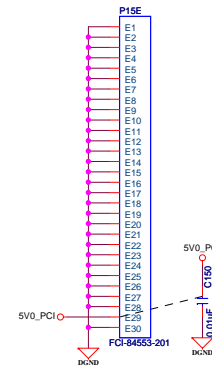
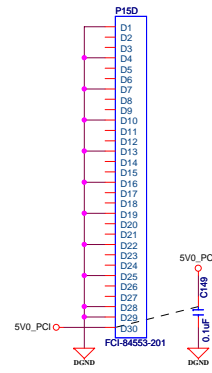
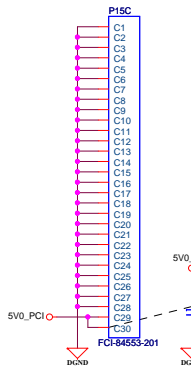
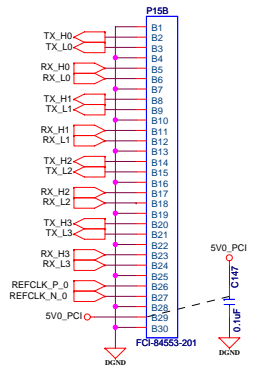
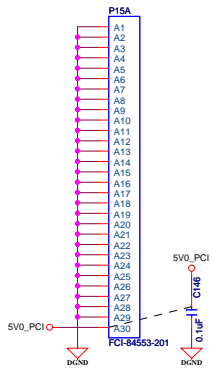


PCI2 Bus, Local Bus

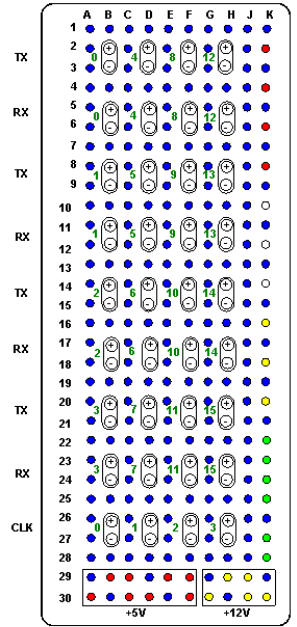




LYNX lines



RR 8568 Proto1



Add Cntr. Sig.

PWRGD_MB
WAKE_IN
ADPTR_PCI_ON
3V3_PCIe_EDGE
SMBCLK
SMBDAT