1. Unless Otherwise Specified:
   All resistors are in ohms, 1% and 5%
   All capacitors are in uF, 10%, 20% and 5%
   All voltages are DC
   All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:
   _B Denotes - Active-Low Signal
   <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.
OpenSDA Interface

Note: You can power openSDA with your own power supplies by replacing this rail (P3V3_SDA) with your 3.3V power supply rail. 3.3VDC, 10mA should be provided to this rail (P3V3_SDA) in order to power openSDA module.

SDA_VOUT33 can provide up to 120mA of power at 3.3VDC to your system.

OpenSDA INTERFACE JTAG CONNECTOR

PAK SWITCH (To enable the TAJm USB connector).

Isolation and level shift stage (for 1.8V for compatibility).