

S32K146EVB-Q1XX

CUSTOMER EVB

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Revisions				
Rev	Description	Designer	Date	Approved
X1	First Release	J.Sanchez		TBD

CAUTION:

This schematic is provided for reference purposes only. As such, NXP does not make any warranty, implied or otherwise, as to the suitability of circuit design or component selection (type or value) used in these schematics for hardware design using the NXP S32K family of Microprocessors. Customers using any part of these schematics as a basis for hardware design, do so at their own risk and Freescale does not assume any liability for such a hardware design.

3 Different test points used in design:

TPVx - Through Hole Pad small

TPHx - Through Hole Pad Large (for standard 0.1" header). Also used on IO Matrix (IOMx)

TPx - Surface Mount Wire Loop

 TPV?

 TPH5

 TP?


Notes:

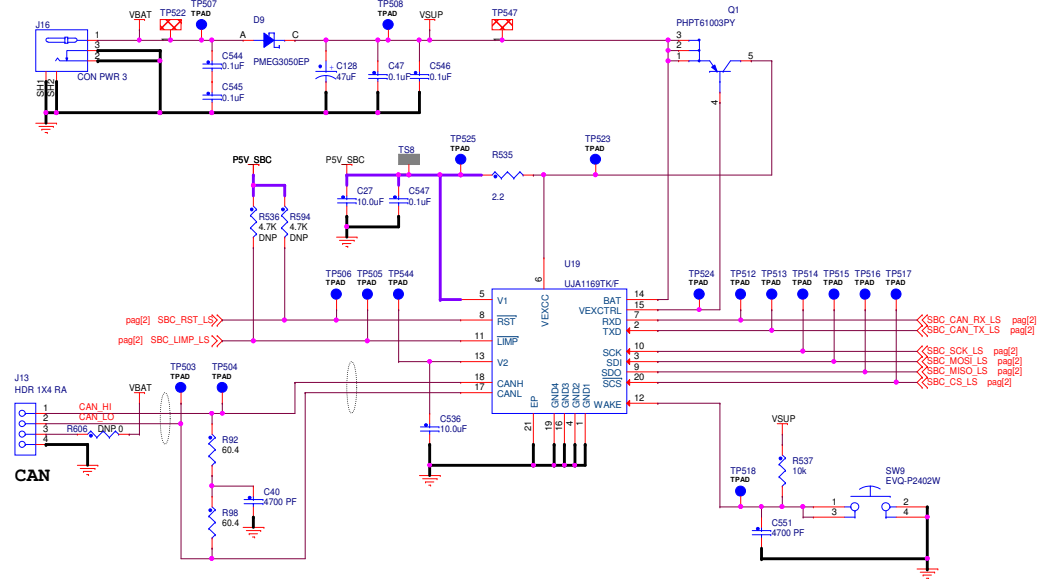
- All components and board processes are to be ROHS compliant
- All connectors and headers are denoted Jx/Px and are 2.54mm pitch unless otherwise stated
- All jumpers are denoted Jx. Jumpers are 2mm pitch
- Jumper default positions are shown in the schematics. For 3 way jumpers, default is always posn 1-2.
- 2 Pin jumpers generally have the "source" on pin 1.
- All switches are denoted SWx
- All test points (SMT wire loop style) are denoted TPx
- Test point Vias (just through hole pads) are denoted TPVx

Signals (ports) have not been routed via busses as this makes it harder to determine where each signal goes.

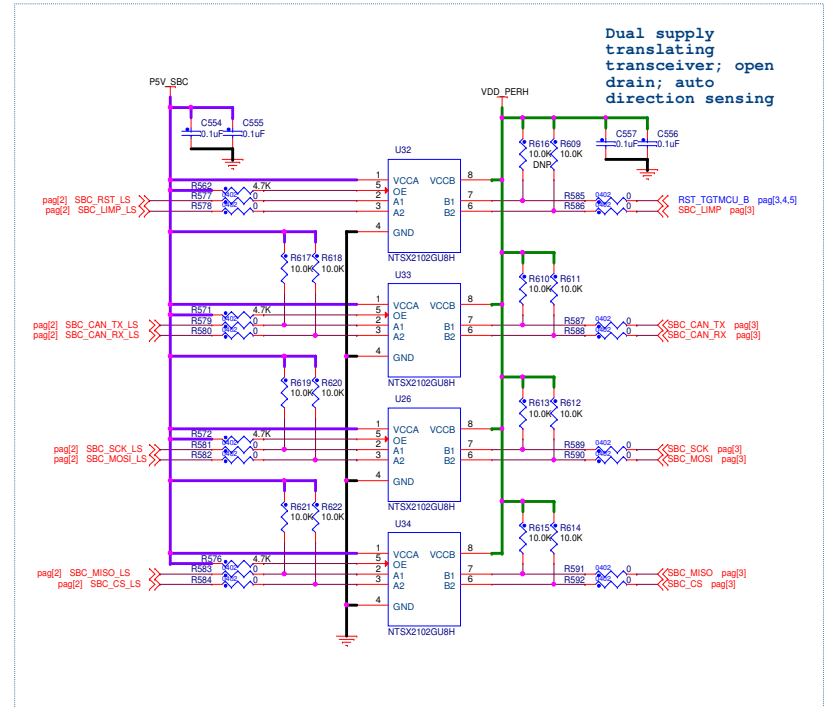
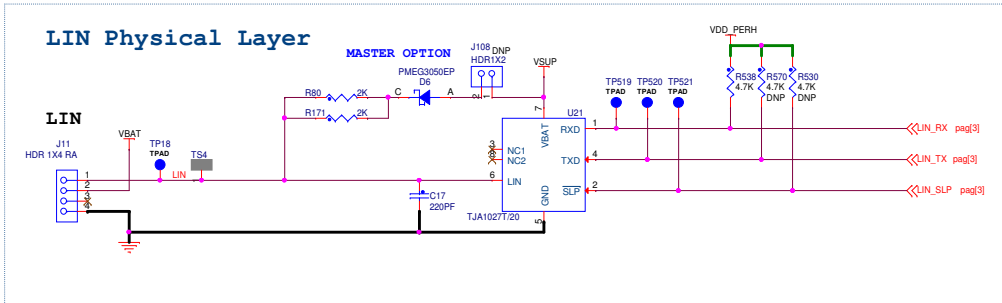
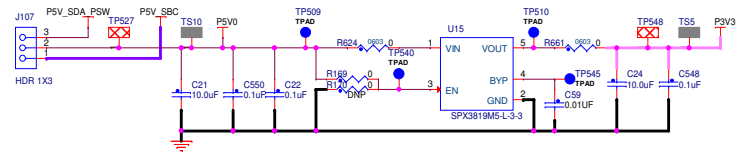
User notes are given throughout the schematics.

Specific PCB LAYOUT notes are detailed in *ITALICS*

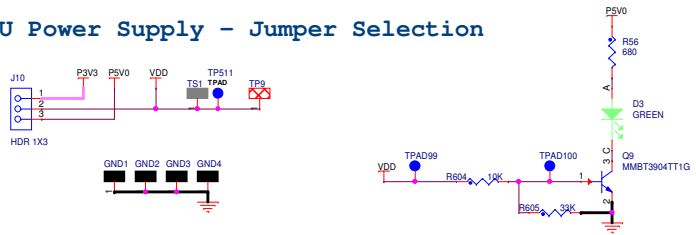
		Automotive Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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Drawn by: Jesus Sanchez		Document Number SCH-29844 PDF: SPF-29844	
Approved: APPROVER		Size B	
Date: Friday, July 07, 2017		Rev X2	
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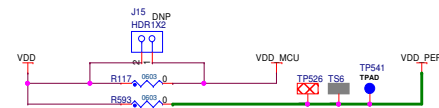
3.3V LDO Power Supply



MCU Power Supply - Jumper Selection

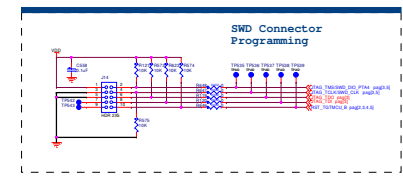
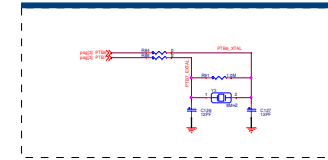
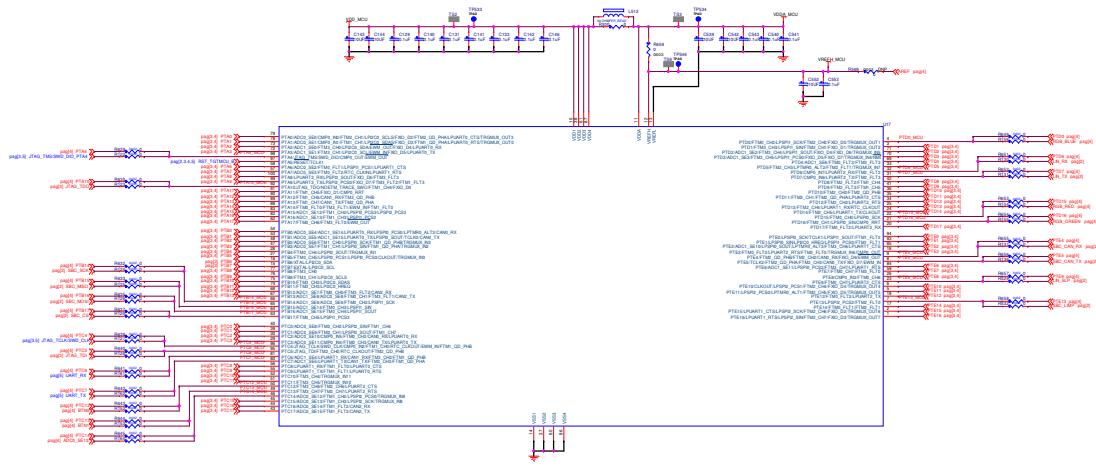


MCU Current Measurement

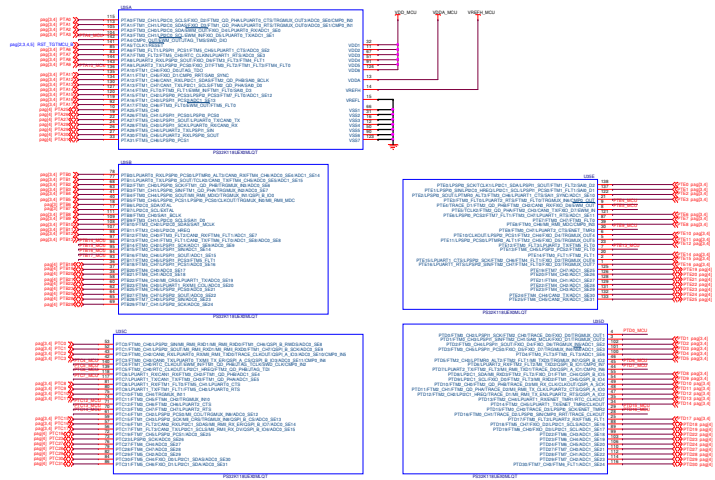


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Drawing Title:					
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Power Supply/SWD/Reset					
Size	Document Number				Rev
C	SCH-29844 PDF: SPF-29844				X2
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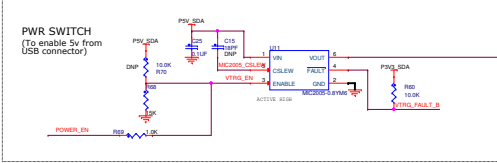
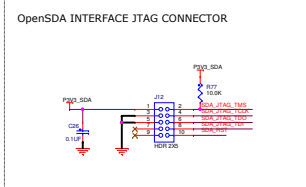
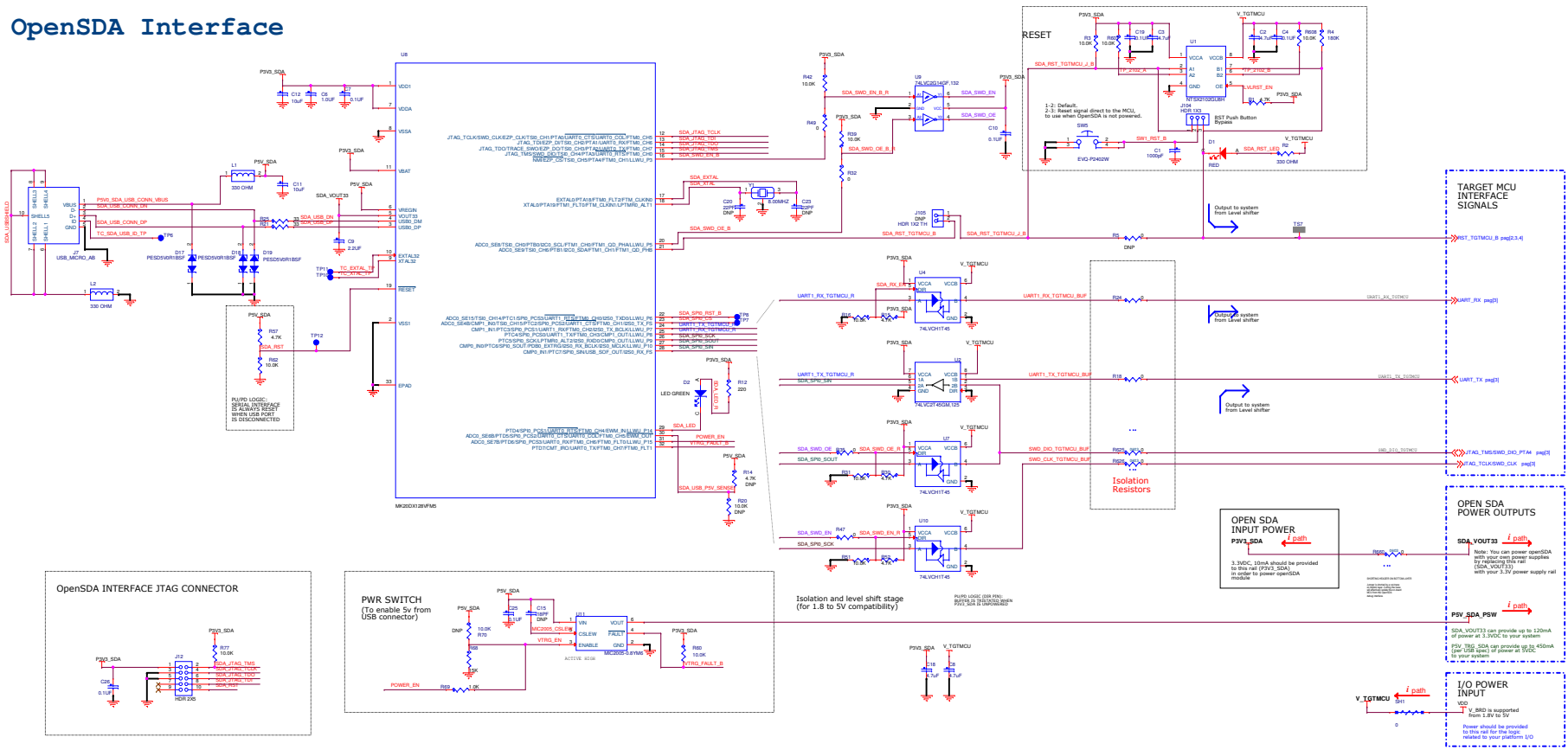
S32K146 Microcontroller 100pins LQFP



S32K146 Microcontroller 144pins LQFP



OpenSDA Interface



Isolation and level shift stage (for 1.8 to 5V compatibility)

OPEN SDA INPUT POWER
PWR_SDA

OPEN SDA POWER OUTPUTS
SDA_VOUT3

I/O POWER INPUT
VCC

