CAUTION:

This schematic is provided for reference purposes only. As such, NXP does not make any warranty, implied or otherwise, as to the suitability of circuit design or component selection (type or value) used in these schematics for hardware design using the NXP S32K family of Microprocessors. Customers using any part of these schematics as a basis for hardware design, do so at their own risk and Freescale does not assume any liability for such a hardware design.

Notes:
- All components and board processes are to be ROHS compliant
- All connectors and headers are denoted Jx/Px and are 2.54mm pitch unless otherwise stated
- All jumpers are denoted Jx. Jumpers are 2mm pitch
- Jumper default positions are shown in the schematics. For 3 way jumpers, default is always posn 1-2.
- 2 Pin Jumpers generally have the "source" on pin 1.
- All switches are denoted SWx
- All test points (SMT wire loop style) are denoted TPx
- Test point Vias (just through hole pads) are denoted TPVx

Signals (ports) have not been routed via busses as this makes it harder to determine where each signal goes.

User notes are given throughout the schematics.

Specific PCB LAYOUT notes are detailed in ITALICS
OpenSDA Interface
This is a diagram of an Ethernet Daughter Card, showing the connections and components used in the design. The diagram includes various labels and values, such as capacitors (C517 0.1UF), resistors (R156 10K), and voltage references (VDD_MCU_PERH, VDD, VCCB). The diagram is detailed, showing the layout of the daughter card components, including power supply connections, signal paths, and other electrical elements.

The diagram is labeled with a reference number (07. ETHERNET) and includes a title indicating the nature of the content (Compensated Ethernet Daughter Card). The diagram is part of a larger technical document, as indicated by the NXP logo and the mention of NXP Semiconductors in the footer.
CMOS FLASH Memory 64M-BIT
SAI Audio

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Designer: Walker

Drawing Title: X-S32K148EVB-Q176

Date: Wednesday, July 12, 2017

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