

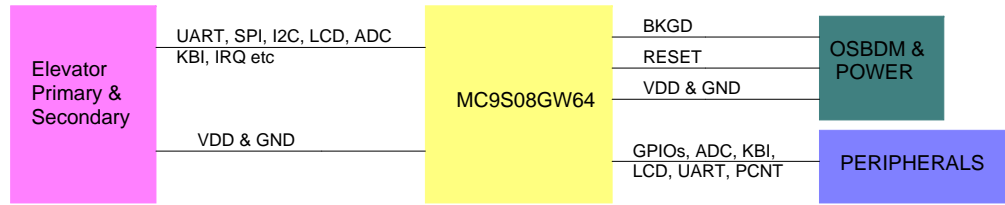


Contents	
	ICU
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5	ELEVATOR CONNECTIONS

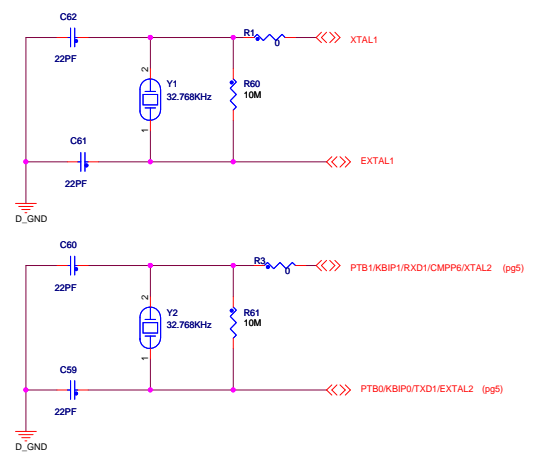
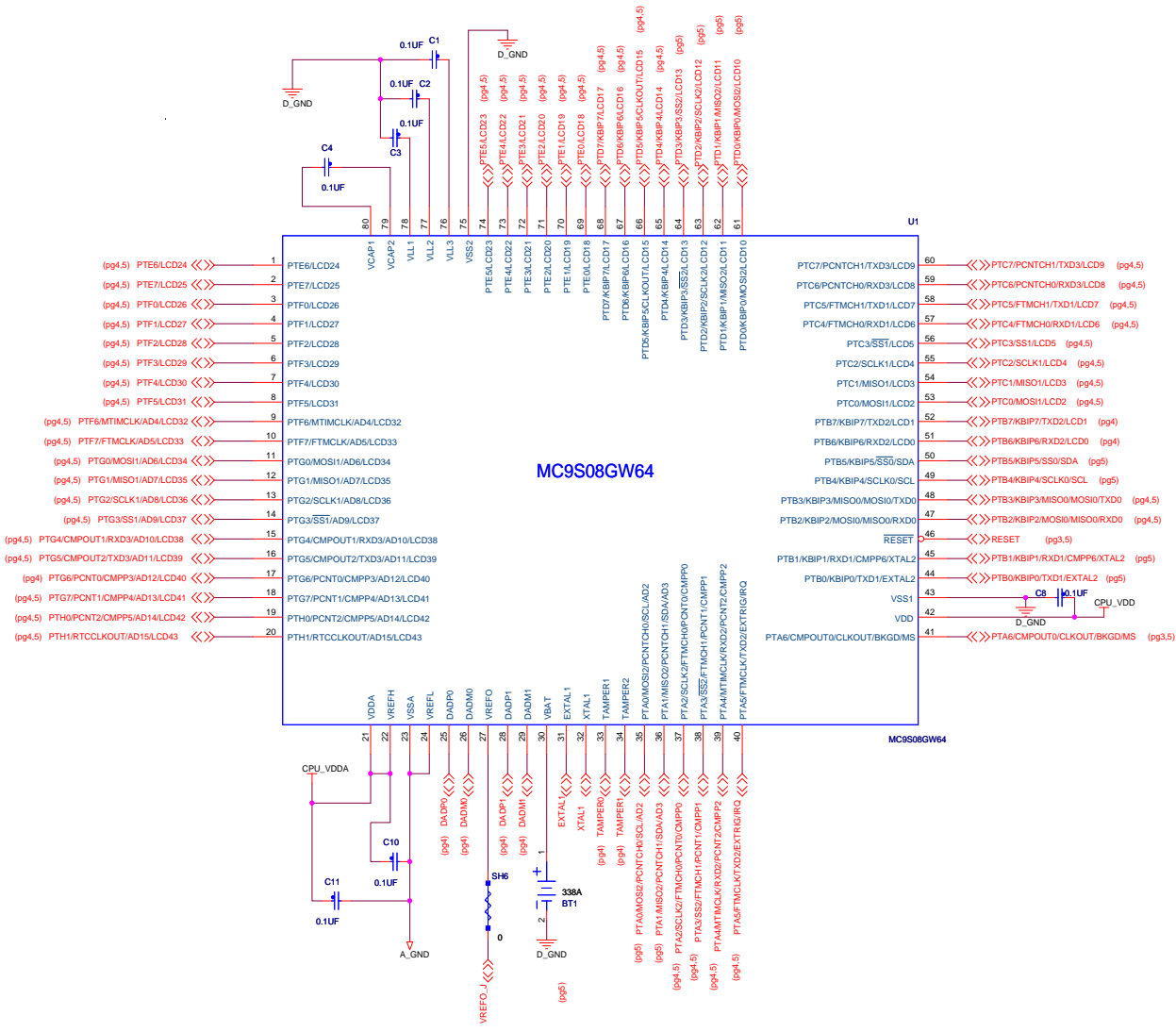
Revisions			
Rev	Description	Date	Approved
X1	Original Release	Nov 21, 2009	Sunaina Srivastava
A	Release to Production	Dec 4, 2009	Sunaina Srivastava
A1	swapped PTG5 with PTG6. 480-77973 footprint update	Sep 8, 2010	Sunaina Srivastava
B	Release to Production	Sep 10, 2010	Sunaina Srivastava
B1	Update symbol for MC9S08GW64. J2 and J5 to be populated	Oct 21, 2010	Sunaina Srivastava

**NOTES**

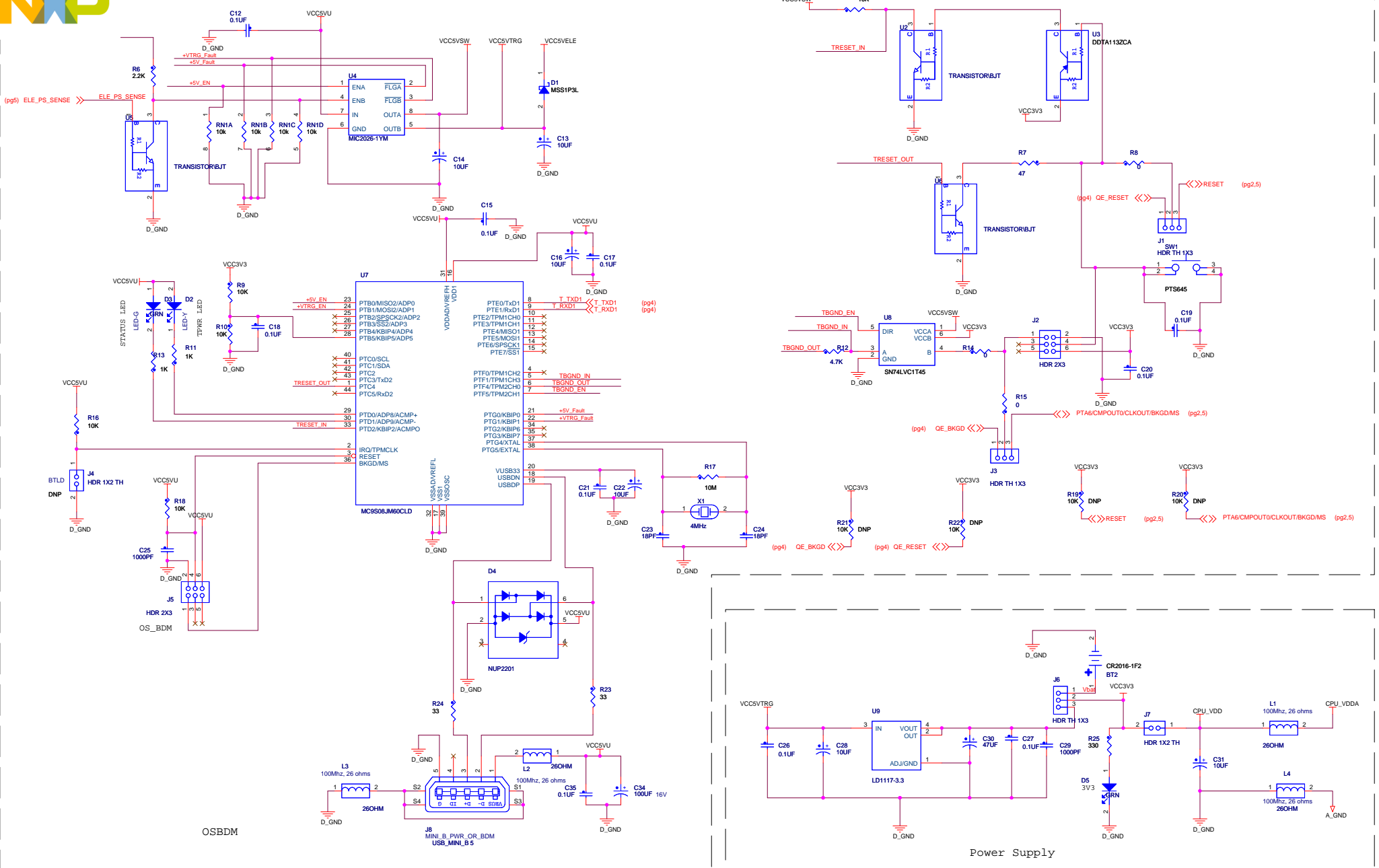
- Unless Otherwise Specified:  
All resistors are in ohms, 5%, 1/8 Watt  
All capacitors are in uF, 20%, 50V  
All voltages are DC  
All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:  
\_B Denotes - Active-Low Signal  
<> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



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Designer: Sunaina Srivastava	Drawing Title: <b>XTWR-S08GW64</b>	ICAP Classification: FCP:	FLUQ: X PUBL:
Drawn by: Sunaina Srivastava	Page Title: <b>Cover Page</b>		
Approved: Sunaina Srivastava	Size C	Document Number SCH-26361 PDF: SPF-26361	Rev B1
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Drawing Title:	<b>XTWR-S08GW64</b>
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OSBDM

Power Supply

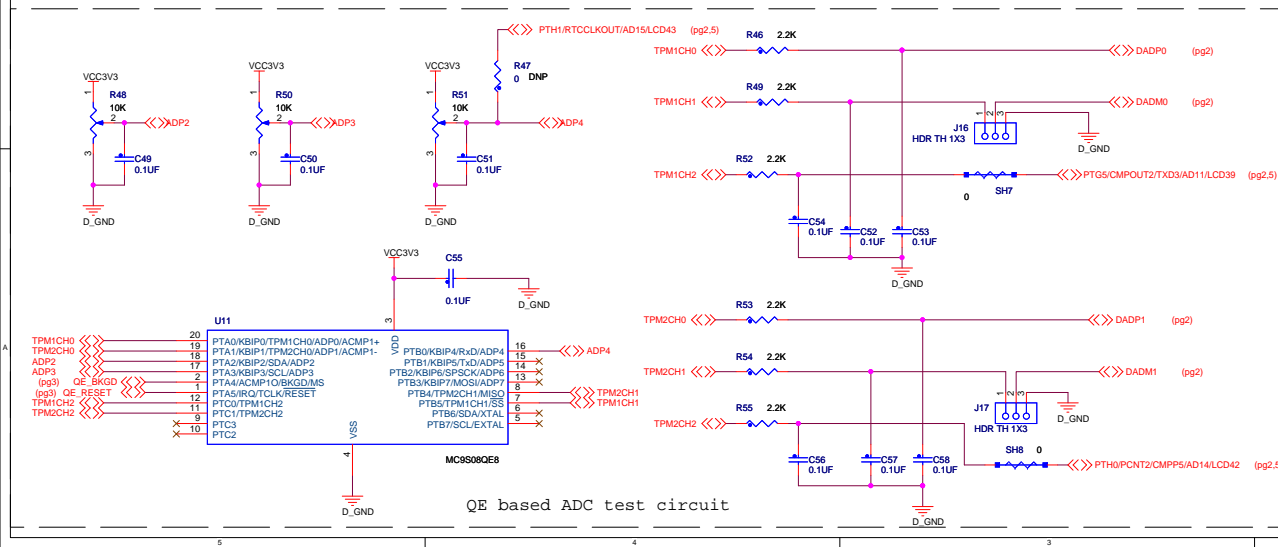
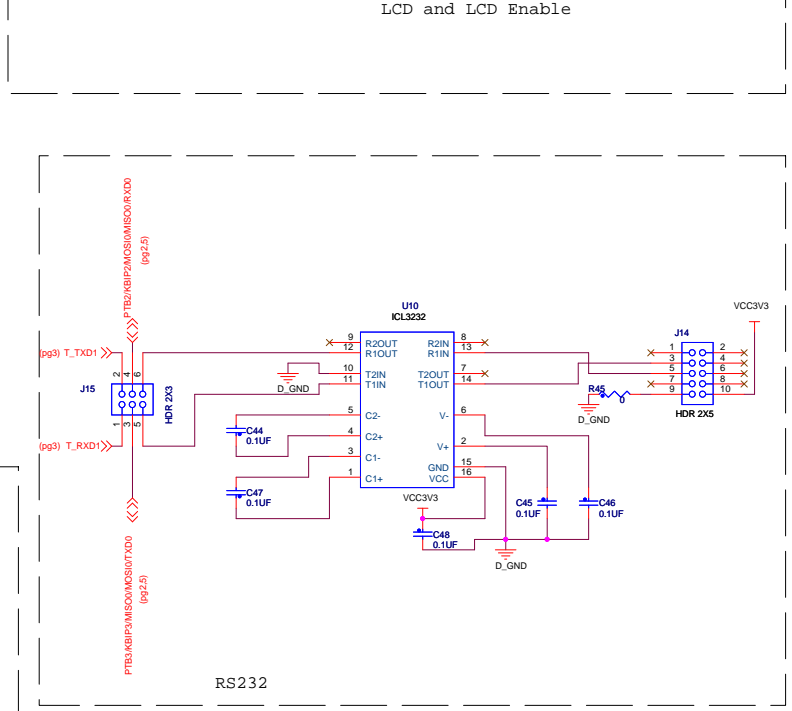
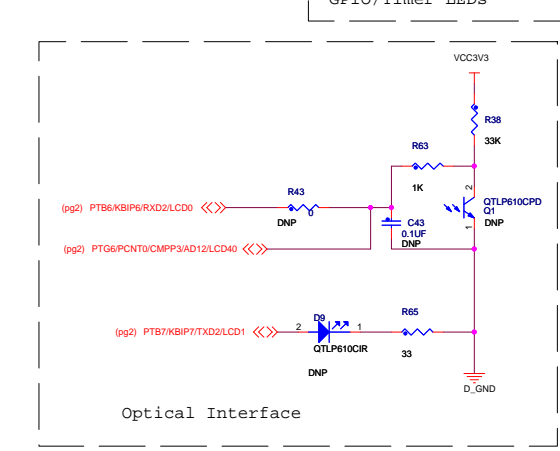
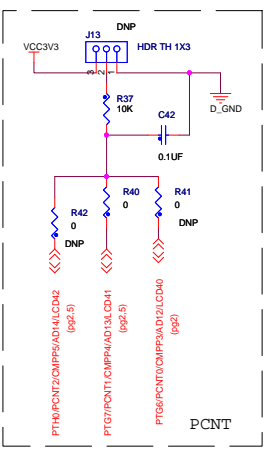
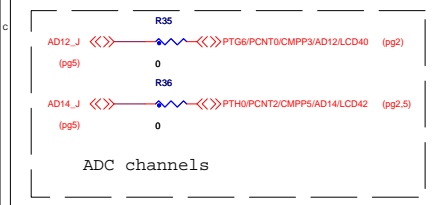
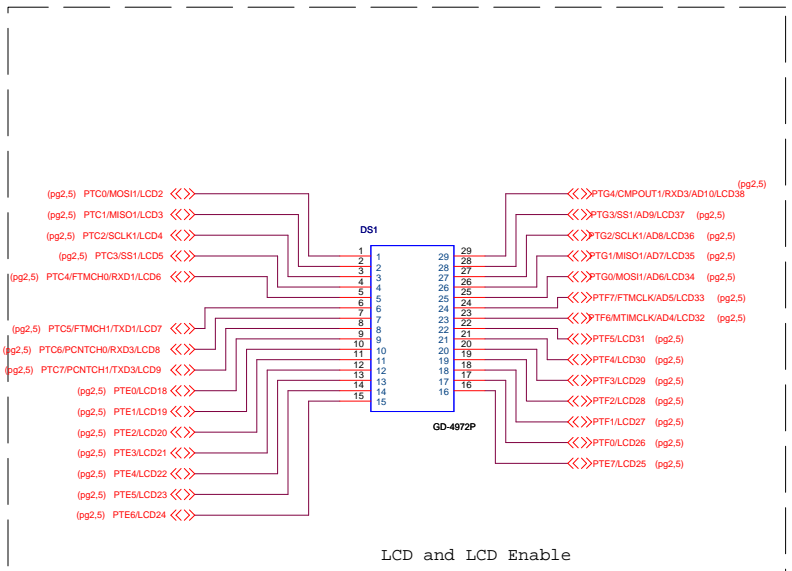
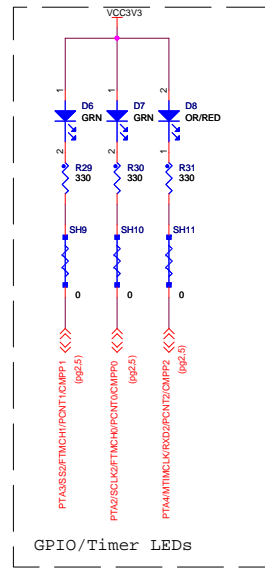
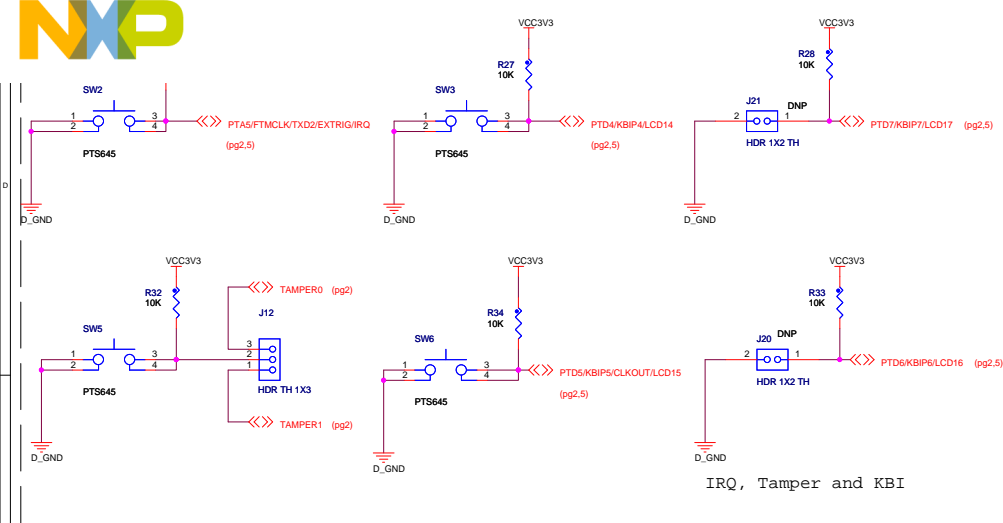
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semiconductor

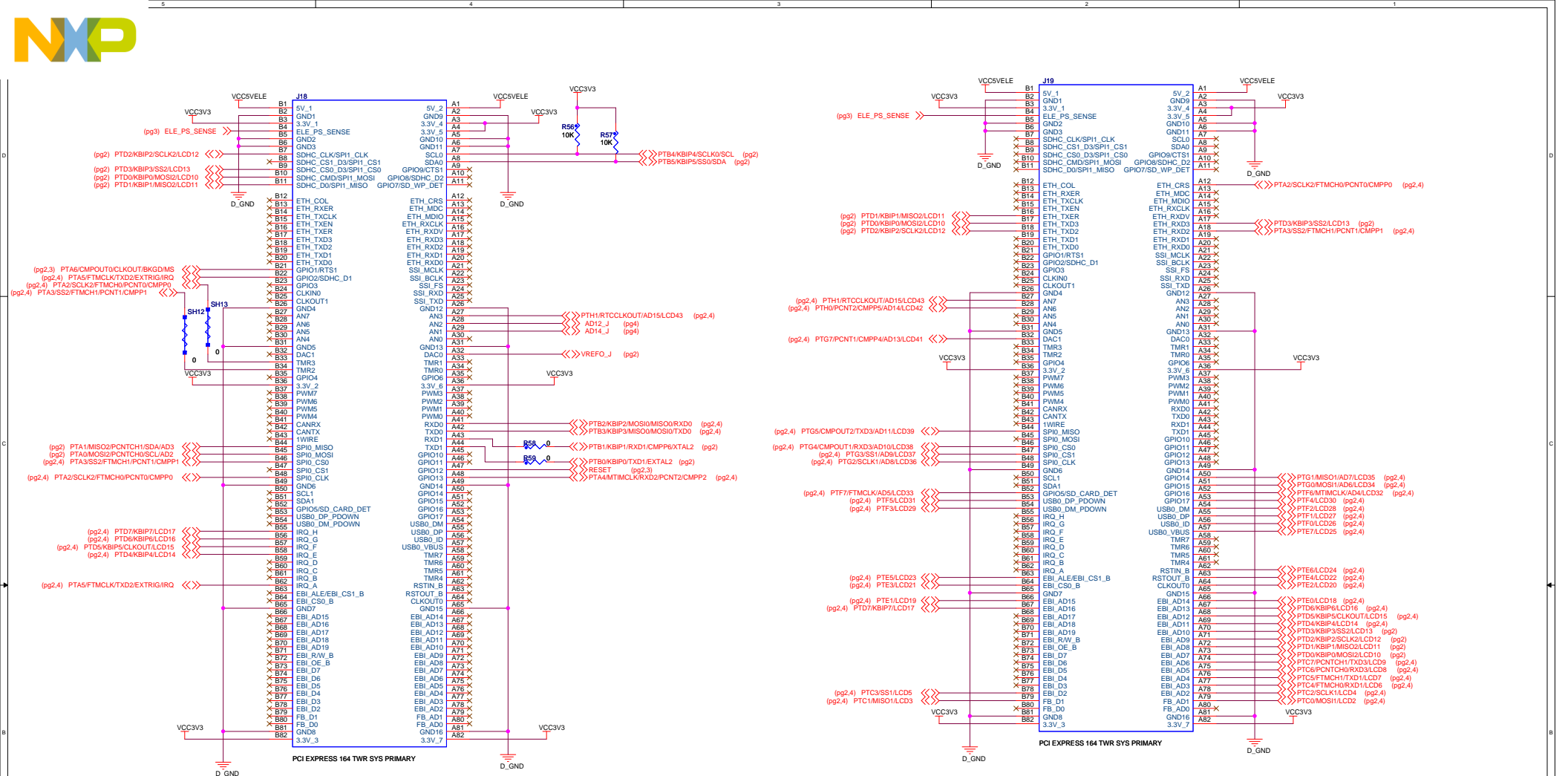
ICAP Classification: FCP: FIUC: X PUBL: \_\_\_\_\_  
Drawing Title: **XTWR-S08GW64**

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Primary Elevator

Secondary Elevator



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