Revisions

<table>
<thead>
<tr>
<th>Date</th>
<th>Description</th>
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<tr>
<td>31-May-2012</td>
<td>X1 Initial Release</td>
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<tr>
<td>13-June-2012</td>
<td>Flash RD/BY LED redesigned</td>
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<tr>
<td>13-June-2012</td>
<td>AC Coupling capacitor for Nexus Clock values changed from 220pf to 250pf</td>
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<tr>
<td>28-June-2012</td>
<td>Mictor Connector's connection updated</td>
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<tr>
<td>03-Oct-2012</td>
<td>A070 Release</td>
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<tr>
<td>24-Dec-2012</td>
<td>C2 and C4 values are changed from 250pF to 10pF</td>
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<tr>
<td>04-Jan-2013</td>
<td>BX1 Assembly option for jumpers J12, J16, J17, J18, J22 and J26 changed</td>
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<tr>
<td>12-Mar-2013</td>
<td>D2, D3 and D4 manufacturers are changed</td>
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<tr>
<td>13-June-2014</td>
<td>BX2 Assembly option for jumpers J12, J16, J17, J18, J22 and J26 changed</td>
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MPC5777M_416DS
- All Jumpers are 2mm spaced
- All port headers are 2.54mm spaced
- All caps are 10% accurate
- All res are 1% accurate
Place CAPS as close to connector pins as possible but do NOT fit caps at board assembly.

JTAG, Nexus & SIPI Interface

JTAG Connector

VSS

JTAG TDI, JTAG TDO, JTAG TCK, JTAG TMS & JTAG COMP are not connected to daughter card.

PORST_B, ESR0_B, EVTI and EVTO are connected to daughter card.

Nexus interface

SIPI interface
**Clock and Reset Circuitry**

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### External Oscillator

- **Loop Controlled Pierce Oscillator Circuit**
  - REMOVE XTAL jumper when driving EXTAL from Oscillator Module or External Source (PLL Enabled)
  - Connect XTAL jumper to GND when driving EXTAL from Oscillator Module or External Source (PLL Bypass Mode)

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### Reset Circuit

- **Note - External Pull-Up on Pin 1**
- **Note - External 3.3V regulator MUST be enabled when using oscillator module**

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**Additional Notes**:
- **Added 4.7k ohm pull-up resistor to VDD_HV_IO_MAIN for U1B pin 6**
- **Added 4.7k ohm pull-up resistor to VDD_HV_IO_MAIN for U1C pin 8**