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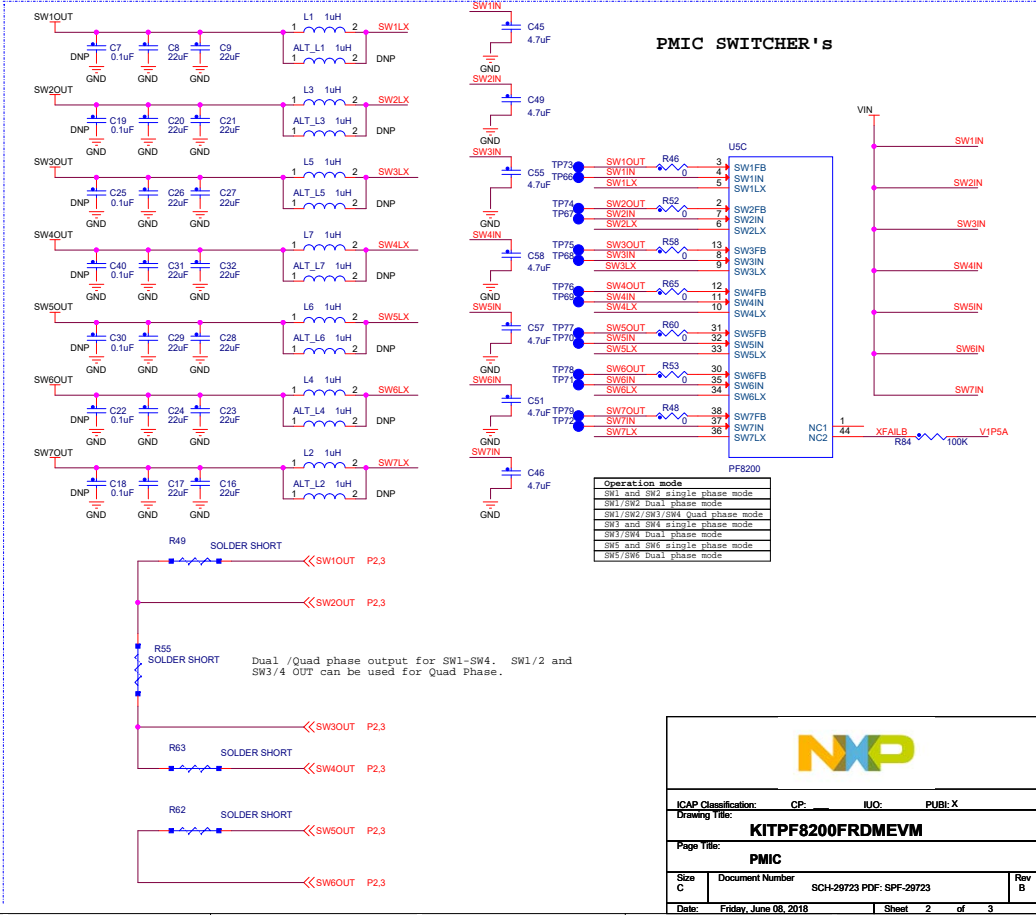
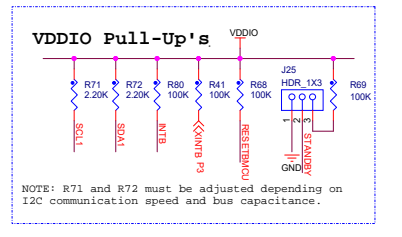
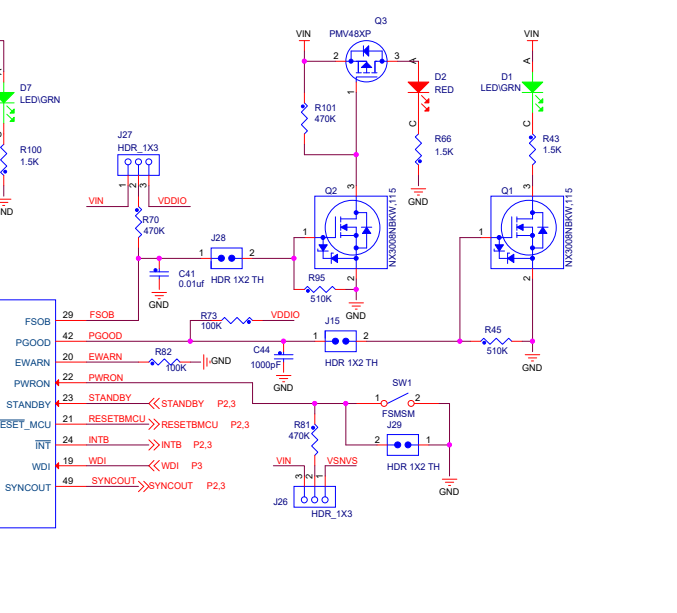
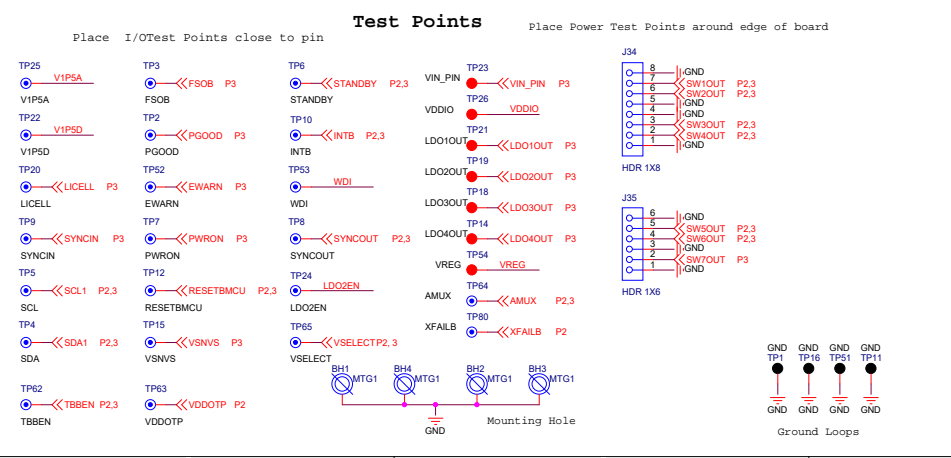
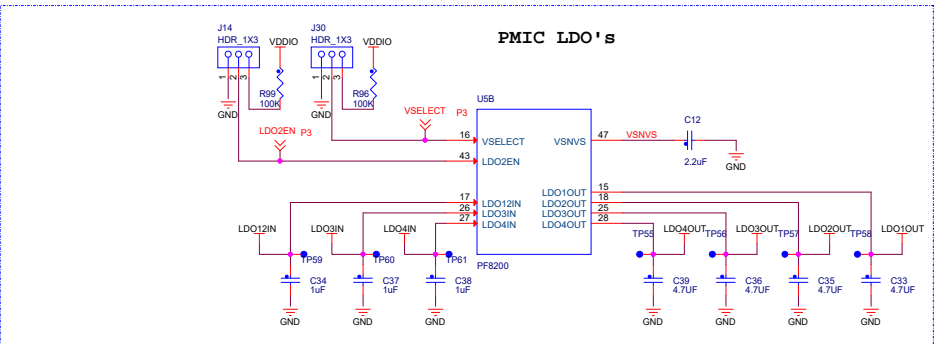
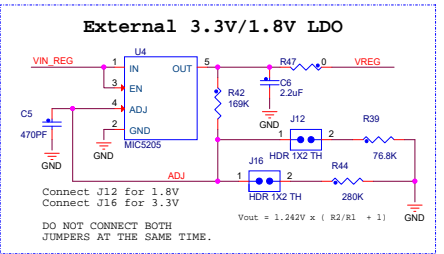
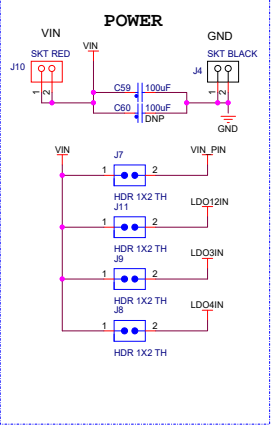
1	TITLE, TOC & REV
2	PMIC
3	KL25, Connector, AMUX

Revisions

Rev	Description	Date	Approved
A	Initial schematic	6-APR-17	Eric Pritchett
A1	Mark as DNP R11, R10, R1 and R2	13-JUN-17	Joaquin Romo
A2	Mark as DNP R50	12-JUL-17	Joaquin Romo
B	<ol style="list-style-type: none"> 1. Replace 1.27mm Jumpers with 2mm 2. Add N/P FET array to drive the FSOB LED (active Low) 3. Remove J19. XINTB is always pulled up to VDDIO. And connected to MCU. 4. Remove J24. RESETMCU is always pulled up to VDDIO. 5. Remove J23. WDI is connected directly to MCU interface through a 100K resistor. 6. Added 0ohm resistors on all I/O 7. Add Diode array to allow best of supply for External LDO. 8. Add Diode array to allow best of supply for External Analog Mux Circuits. 9. Add 100K pull down on J20 (VDDOTP) 10. Change J14 and J30 from 2 x3 header to 1 x 3 header. (LDO2EN, VSELECT) 11. Add Test pad on LDOxIN and LDOxOUT. Update LDOxOUT capacitance to 4.7uF 12. Add test pad on SWxLX trace to allow external Current loop sense. 13. Add Kalvin Sense test pads on SWxOUT and SWxIN 14. Update symbol to support XFAILB with 100k Pull up to V1P5A 15. Simplify Multiphase shunt connection with 0805 Solder Shorts 16. Replace SWx Power test points with Power Pin stripe (J34/ J35) 17. Add missing Test points. (AMUX, VDDOTP, TBBEN) 18. Add support for XFAILB on Master/Slave interface. 19. Add PWRON LED 20. Replace U5 with PF8200 device 21. Merge SWxVIN to one plane. Remove SWxVIN resistors and testpoint. 22. Add Alternate Inductor Footprints 	16-MAY-18	Joaquin Romo

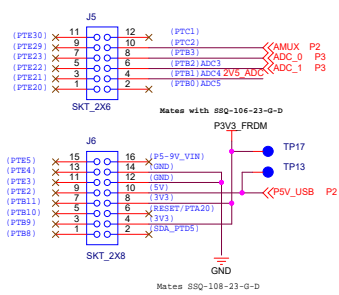
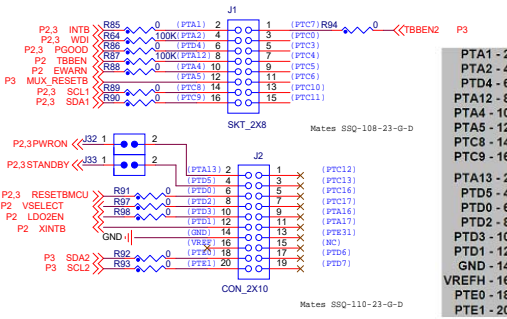
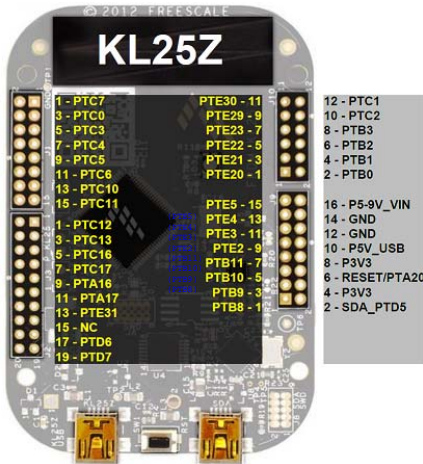
KITPF8200FRDMEVM

		Analog Sensor Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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Approved: Saku Y.	Size C	Document Number SCH-29723 PDF: SPF-29723	Rev B
Date: Friday, June 09, 2018		Sheet	1 of 3



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 Drawing Title: K1TFP8200FRDMEV
 Page Title: PMIC
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 Date: Friday, June 08, 2018 Sheet 2 of 3

KL25Z INTERFACE



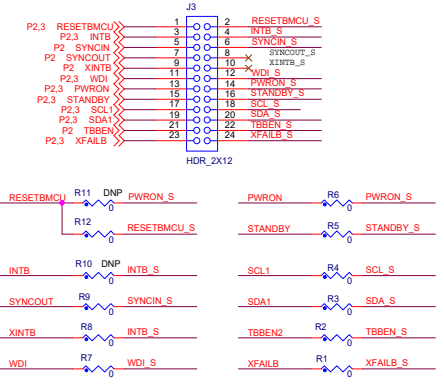
Populate the following connectors on the Freedom board (female with long pins, low insertion):
 SSQ-106-23-G-D / 210-80515
 SSQ-110-23-G-D / 210-80513
 SSQ-108-23-G-D (x2) / 210-80247

VOLTAGE SENSE

RAIL	VOLTAGE RANGE	AMUX/CHANNEL
SW1	0.4V to 1.8V, 2500mA	AMUX 1/CH 1
SW2	0.4V to 1.8V, 2500mA	AMUX 1/CH 2
SW3	0.4V to 1.8V, 2500mA	AMUX 1/CH 3
SW4	0.4V to 1.8V, 2500mA	AMUX 1/CH 4
SW5	0.4V to 1.8V, 2500mA	AMUX 1/CH 5
SW6	0.4V to 1.8V, 2500mA	AMUX 1/CH 6
RESERVED		AMUX 1/CH 7
RESERVED		AMUX 1/CH 8
VIN	2.7V to 5.5V	AMUX 2/CH 1
VSNVS	1.8V, 3.0V, 3.3V, 10mA	AMUX 2/CH 2
LICELL	1.8V, 3.0V, 3.3V, 10mA	AMUX 2/CH 3
SW7	1.2V to 4.5V, 2500mA	AMUX 2/CH 4
LDO1	1.5V to 5V, 400mA	AMUX 2/CH 5
LDO2	1.5V to 5V, 400mA	AMUX 2/CH 6
LDO3	1.5V to 5V, 400mA	AMUX 2/CH 7
LDO4	1.5V to 5V, 400mA	AMUX 2/CH 8

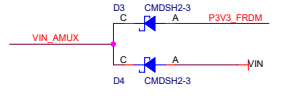
Master/Slave Connector

NOTE: Use single 12 Row connector to connect Slave side from primary EVB to Master side of 2nd EVB. Resistors can be configured on primary board for specific configurations.

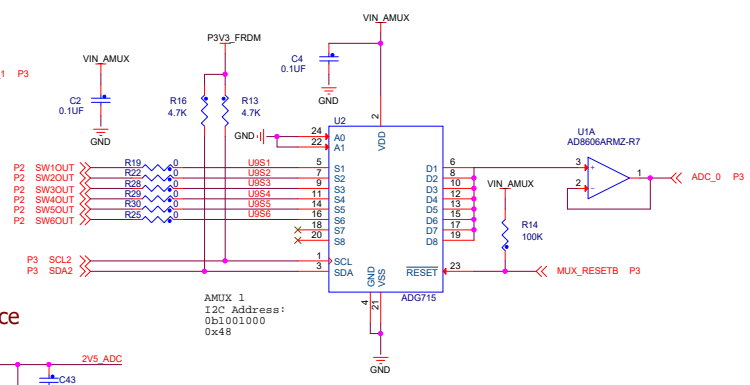


SECONDARY AMUX

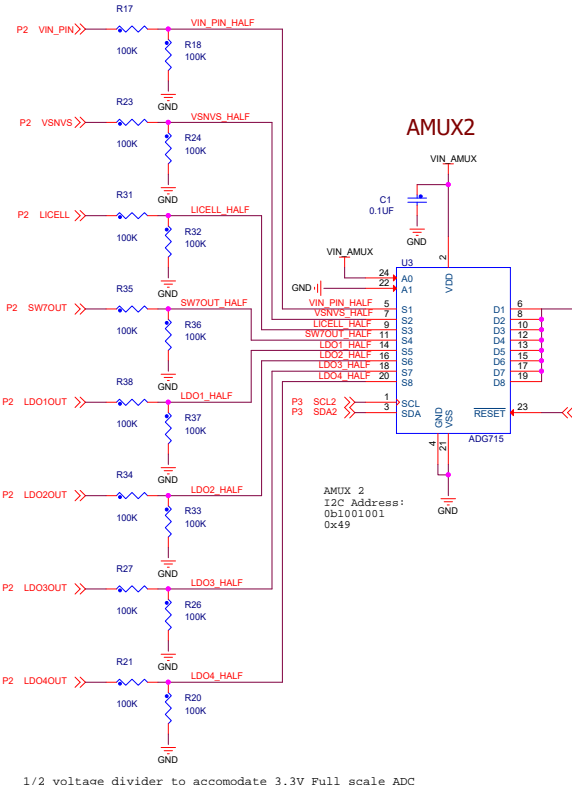
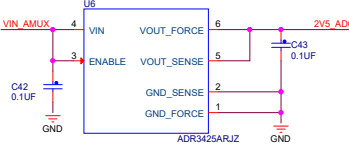
NOTE: Secondary AMUX is intended for Software purposes. GUI can use this to enable live monitoring instead of using AMUX on P8100/8200. AMUX uses the best of supply from P3V3 Freedom supply and VIN.



AMUX1



2.5V Voltage reference



1/2 voltage divider to accommodate 3.3V Full scale ADC



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