
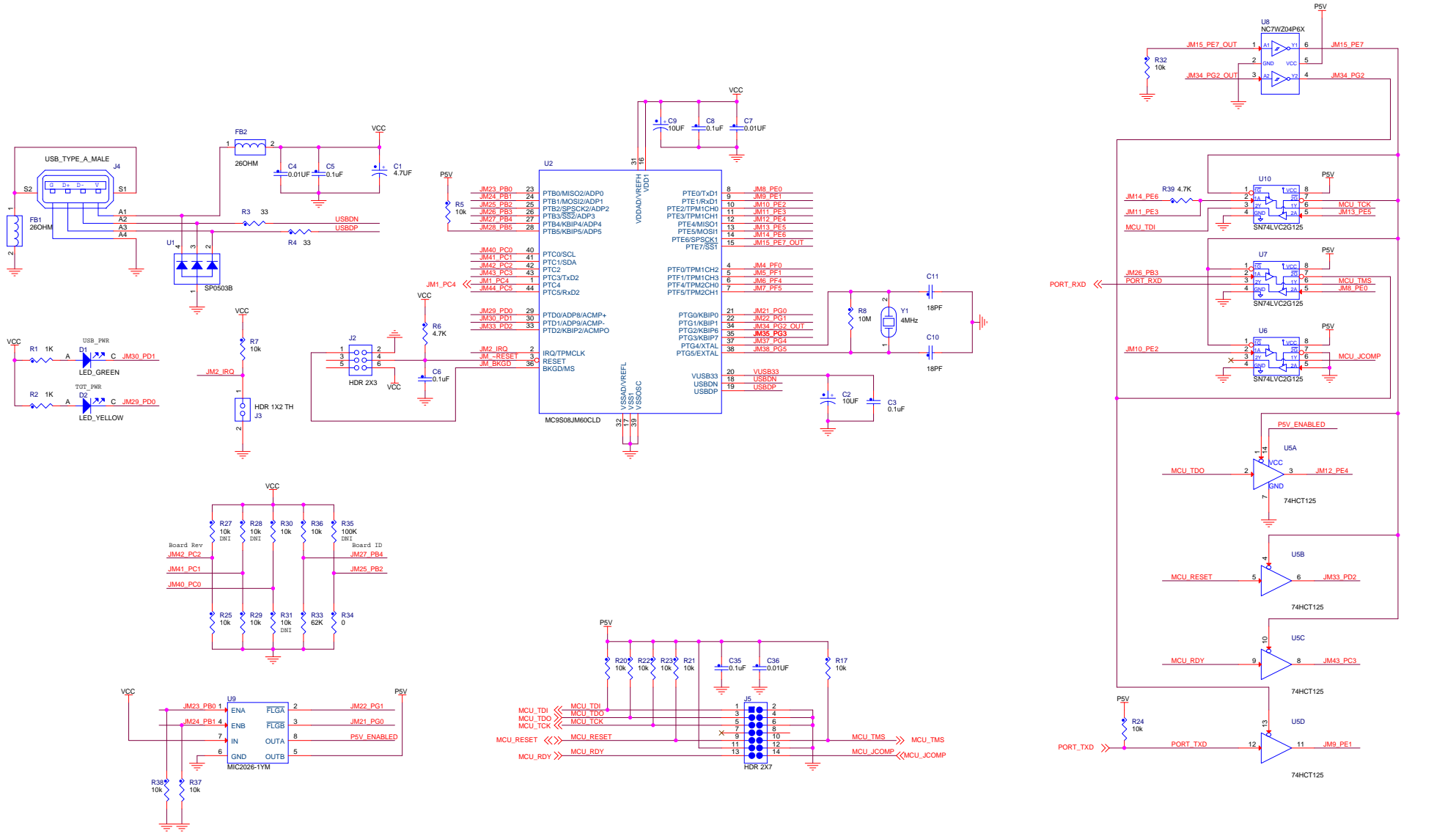
		<b>Microcontroller Solutions Group</b> 6501 William Cannon Drive West Austin, TX 78735-8508	
<small>This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.</small>			
Designer: DESIGNER		ICAP Classification: FCP: FUG: X PUB:	
Drawn by: DRAWN_BY		<b>TRK-USB-MPC5604B</b>	
Approved: APPROVER		<b>TITLE PAGE</b>	
Size C	Document Number SCH-27550 PDF: SPF-27550	Date: Friday, April 20, 2012	Rev A
		Sheet 1 of 4	

All polarized capacitors are Tantalum

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:  
  \_B Denotes - Active-Low Signal  
  <> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

			
ICAP Classification:		FCP: _____	FIUC: X    PUBI: _____
Drawing Title:			
<b>TRK-USB-MPC5604B</b>			
Page Title:			
<b>NOTES</b>			
Size C	Document Number SCH-27550 PDF: SPF-27550	Rev A	
Date:	Friday, April 20, 2012	Sheet 2	of 4

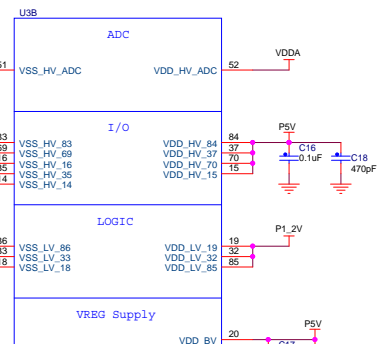
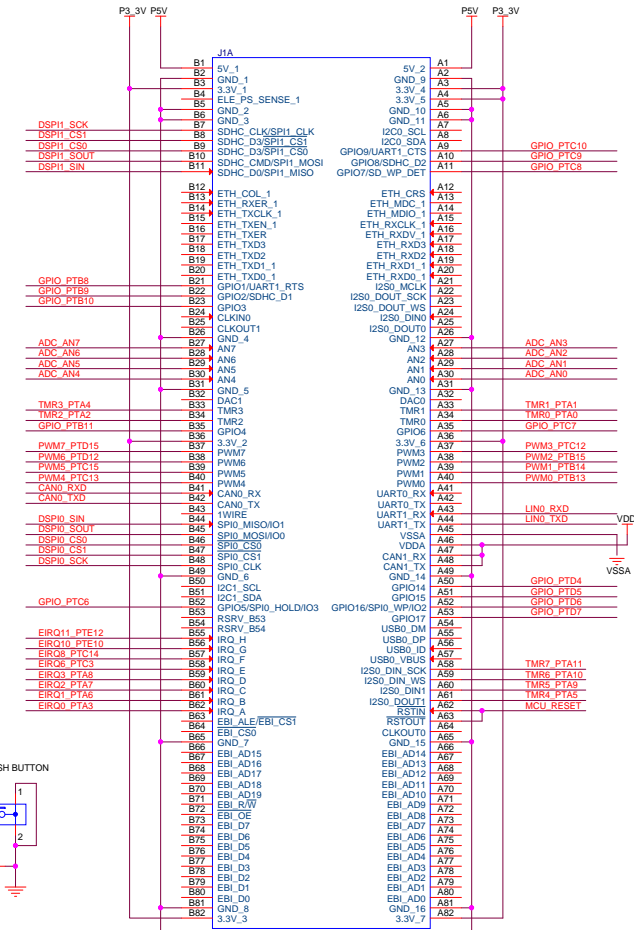
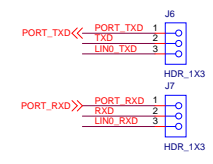
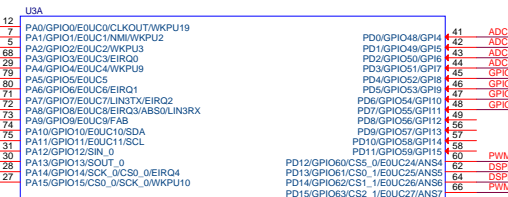
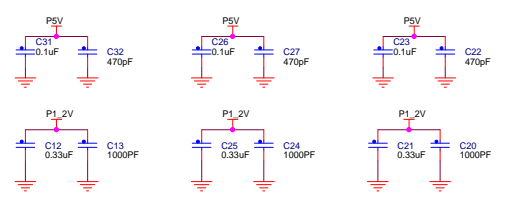
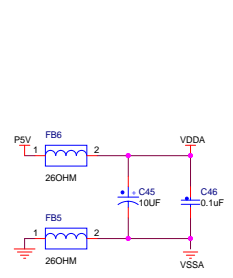


**freescale**  
semiconductor

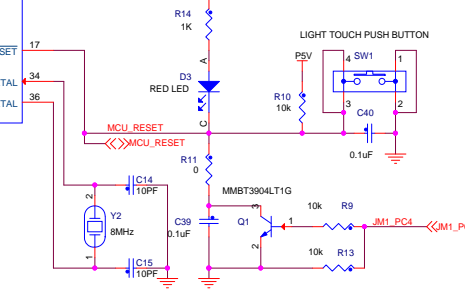
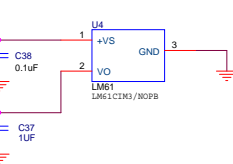
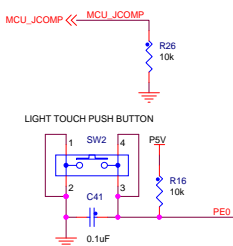
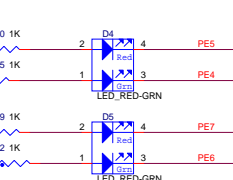
ICAP Classification: FCP: \_\_\_\_\_ FIUC: X PUI: \_\_\_\_\_  
Drawing Title: **TRK-USB-MPC5604B**

Page Title: **Schematic - OSJTAG**

Size C	Document Number SCH-27550 PDF: SPF-27550	Rev A
Date: Friday, April 20, 2012	Sheet 3 of 4	



Pin	Signal	MCU Pin	Signal
12	TMRO_PTA0	12	TMRO_PTA0
7	TMR1_PTA1	7	TMR1_PTA1
5	TMR2_PTA2	5	TMR2_PTA2
68	TMR3_PTA3	68	TMR3_PTA3
29	TMR4_PTA4	29	TMR4_PTA4
79	TMR5_PTA5	79	TMR5_PTA5
60	EIRO1_PTA6	60	EIRO1_PTA6
71	EIRO2_PTA7	71	EIRO2_PTA7
72	EIRO3_PTA8	72	EIRO3_PTA8
73	TMR6_PTA9	73	TMR6_PTA9
74	TMR7_PTA10	74	TMR7_PTA10
75	TMR8_PTA11	75	TMR8_PTA11
31	DSP10_SIN	31	DSP10_SIN
28	DSP10_SOUT	28	DSP10_SOUT
29	DSP10_SCK	29	DSP10_SCK
27	DSP10_CS0	27	DSP10_CS0
23	CAN0_TXD	23	CAN0_TXD
24	CAN0_RXD	24	CAN0_RXD
100	TXD	100	TXD
1	RXD	1	RXD
50	ADC_AN0	50	ADC_AN0
51	ADC_AN1	51	ADC_AN1
52	ADC_AN2	52	ADC_AN2
53	ADC_AN3	53	ADC_AN3
54	GPIO_PTB10	54	GPIO_PTB10
59	DSP11_CS0	59	DSP11_CS0
61	PWM0_PTB13	61	PWM0_PTB13
65	PWM1_PTB14	65	PWM1_PTB14
67	PWM2_PTB15	67	PWM2_PTB15
39	GPIO_PTB8	39	GPIO_PTB8
38	GPIO_PTB9	38	GPIO_PTB9
87	MCU_TDI	87	MCU_TDI
82	MCU_TDO	82	MCU_TDO
77	EIRO6_PTC3	77	EIRO6_PTC3
62	DSP11_SIN	62	DSP11_SIN
63	DSP11_SOUT	63	DSP11_SOUT
25	GPIO_PTC6	25	GPIO_PTC6
26	GPIO_PTC7	26	GPIO_PTC7
99	GPIO_PTC8	99	GPIO_PTC8
2	GPIO_PTC9	2	GPIO_PTC9
22	GPIO_PTC10	22	GPIO_PTC10
21	PWM3_PTC12	21	PWM3_PTC12
97	PWM4_PTC13	97	PWM4_PTC13
98	PWM5_PTC15	98	PWM5_PTC15
3	EIRO8_PTC14	3	EIRO8_PTC14
4	EIRO9_PTC15	4	EIRO9_PTC15
41	ADC_AN4	41	ADC_AN4
42	ADC_AN5	42	ADC_AN5
43	ADC_AN6	43	ADC_AN6
44	ADC_AN7	44	ADC_AN7
45	GPIO_PTD1	45	GPIO_PTD1
46	GPIO_PTD5	46	GPIO_PTD5
47	GPIO_PTD6	47	GPIO_PTD6
48	GPIO_PTD7	48	GPIO_PTD7
49	DSP10_CS1	49	DSP10_CS1
56	PWM6_PTD12	56	PWM6_PTD12
57	DSP10_CS2	57	DSP10_CS2
58	DSP10_CS3	58	DSP10_CS3
62	DSP10_CS4	62	DSP10_CS4
64	DSP11_CS1	64	DSP11_CS1
66	PWM7_PTD15	66	PWM7_PTD15
6	PE0	6	PE0
8	PE1	8	PE1
89	PE2	89	PE2
90	PE3	90	PE3
93	PE4	93	PE4
94	PE5	94	PE5
95	PE6	95	PE6
96	PE7	96	PE7
9	EIRO10_PTE10	9	EIRO10_PTE10
10	EIRO11_PTE12	10	EIRO11_PTE12
11	PH0_GPIO121/TK	11	PH0_GPIO121/TK
13	PH10_GPIO122/TMS	13	PH10_GPIO122/TMS
17	RESET	17	RESET
34	XTAL	34	XTAL
36	EXTAL	36	EXTAL
88	MCU_TCK	88	MCU_TCK
81	MCU_TMS	81	MCU_TMS
11	MCU_RESET	11	MCU_RESET



**freescale**  
semiconductor

ICAP Classification: FCP: FIUC: X PUBL: \_\_\_\_\_  
Drawing Title: **TRK-USB-MPC5604B**

Page Title: **Schematic - MCU**

Size C	Document Number SCH-27550 PDF: SPF-27550	Rev A
Date: Friday, April 20, 2012	Sheet 4	of 4