## Revisions

<table>
<thead>
<tr>
<th>Description</th>
<th>Date</th>
<th>Approved</th>
</tr>
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<tbody>
<tr>
<td>A Original Release</td>
<td>2/03/2010</td>
<td>M.N.</td>
</tr>
<tr>
<td>B Parts changed: J3-J5, C19 and C30</td>
<td>2/23/2010</td>
<td>M.N.</td>
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<tr>
<td>C Changes as per ECR04862</td>
<td>6/07/2010</td>
<td>M.N.</td>
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</tbody>
</table>
1. Unless Otherwise Specified:
   - All resistors are in ohms, 5%, 1/8 Watt
   - All capacitors are in uF, 20%, 50V
   - All voltages are DC
   - All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:
   - _B Denotes - Active-Low Signal
   - <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.