



Table of Contents

2	NOTES
3	MAIN SCHEMATIC
4	USB INTERFACE

Revisions

Rev	Description	Date	Approved
X1	Initial Release	05/20/2015	A. QUIROZ

X-USB-KW40Z

		Wireless Connectivity Operation	
		6501 William Cannon Drive West Austin, TX 78735-8598	
<small>This document contains information proprietary to Freescale Semiconductor and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of Freescale Semiconductor.</small>			
		ICAP Classification: FCP: FIUO: X PUBI:	
Designer: A. QUIROZ	Drawing Title: X-USB-KW40Z		
Drawn by: A. QUIROZ	Page Title: TITLE PAGE		
Approved: L. KELVIN	Size C	Document Number SCH-xxxxx PDF: SPF-xxxxx	Rev X1
Date:	Wednesday, May 20, 2015	Sheet 1 of 4	

1. Unless Otherwise Specified:

All resistors are in ohms, 5%, 1/8 Watt

All capacitors are in uF, 20%, 50V

All voltages are DC

All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.

4. Special signal usage:

B Denotes - Active-Low Signal

<> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

NOTE A

C20: use only Murata GRM1555C1H1R8CA01D

L3: use only Murata LQP15MN2N2B02



ICAP Classification: FCP: FIUO: X PUBI:

Drawing Title:
X-USB-KW40ZPage Title:
NOTES

Size	Document Number	Rev
C	SCH-xxxxx PDF: SPF-xxxxx	X1
Date:	Wednesday, May 13, 2015	Sheet 2 of 4

