



J?

7 89 48 21 38

	VDD1	VDD2	VDD3	VDDA	VBAT	
39						VREFH 22
40						TSIO_CH1/PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK VREFL 23
41						TSIO_CH2/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI 77
42						TSIO_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO VLL1 76
43						TSIO_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO VLL2 75
44						TSIO_CH5/PTA4/FTM0_CH1/NMI/EZP_CS VLL3 79
45						CMP2_IN0/PTA12/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA VCAP1 78
46						CMP2_IN1/PTA13/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB VCAP2 12
47						PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK VREGIN 11
50						PTA15/SPI0_SCK/UART0_RX/I2S0_RXD VOUT33 17
51						EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0 18
						XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1 19
53						LCD_P0/ADC0_SE8/ADC1_SE8/TSIO_CH0/PTB0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA 20
54						LCD_P1/ADC0_SE9/ADC1_SE9/TSIO_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB 13
55						LCD_P2/ADC0_SE12/TSIO_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3 14
56						LCD_P3/ADC0_SE13/TSIO_CH8/PTB3/I2C0_SDA/UART0_CTS/FTM0_FLT0 15
57						LCD_P7/ADC1_SE13/PTB7 16
58						LCD_P8/PTB8/UART3_RTS 17
59						LCD_P9/PTB9/SPI1_PCS1/UART3_CTS PGA0_DP/ADC0_DP0/ADC1_DP3 18
60						LCD_P10/ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FTM0_FLT1 PGA0_DM/ADC0_DM0/ADC1_DM3 19
61						LCD_P11/ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FTM0_FLT2 PGA1_DP/ADC1_DP0/ADC0_DP3 20
62						LCD_P12/TSIO_CH9/PTB16/SPI1_SOUT/UART0_RX/EWM_IN 13
63						LCD_P13/TSIO_CH10/PTB17/SPI1_SIN/UART0_TX/EWM_OUT 14
64						LCD_P14/TSIO_CH11/PTB18/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA ADC0_DP1/OP0_DP0 15
65						LCD_P15/TSIO_CH12/PTB19/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB ADC0_DM1/OP0_DM0 16
66						LCD_P16/PTB20/SPI2_PCS0/CMP0_OUT ADC1_DP1/OP1_DP0/OP1_DM1 16
67						LCD_P17/PTB21/SPI2_SCK/CMP1_OUT ADC1_DM1/OP1_DM0
68						LCD_P18/PTB22/SPI2_SOUT/CMP2_OUT
69						LCD_P19/PTB23/SPI2_SIN/SPI0_PCS5
70						USB0_DP 9
71						LCD_P20/ADC0_SE14/TSIO_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD USB0_DM 10
72						LCD_P21/ADC0_SE15/TSIO_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0
73						LCD_P22/ADC0_SE4B/CMP1_IN0/TSIO_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1
80						LCD_P23/CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2
81						LCD_P24/PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT
82						LCD_P25/PTC5/SPI0_SCK/LPT0_ALT2/CMP0_OUT TRI1_DP 32
83						LCD_P26/CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG TRI1_DM 31
84						LCD_P27/CMP0_IN1/PTC7/SPI0_SIN
85						LCD_P28/ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN
86						LCD_P29/ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FTM2_FLT0 TRI0_DP 30
87						LCD_P30/ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS TRI0_DM 28
90						LCD_P31/ADC1_SE7B/PTC11/I2C1_SDA/I2S0_RXD TRI0_OUT/OP1_DM2 33
91						LCD_P36/PTC16/UART3_RX TRI1_OUT/CMP2_IN5/ADC1_SE22
92						LCD_P37/PTC17/UART3_TX
92						LCD_P38/PTC18/UART3_RTS
93						LCD_P40/PTD0/SPI0_PCS0/UART2_RTS
94						LCD_P41/ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS
95						LCD_P42/PTD2/SPI0_SOUT/UART2_RX
96						LCD_P43/PTD3/SPI0_SIN/UART2_TX
97						LCD_P44/PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN EXTAL32 37
98						LCD_P45/ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/EWM_OUT XTAL32 36
99						LCD_P46/ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0
100						LCD_P47/PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
1						ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA RESET 52
2						ADC1_SE5A/PTE1/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL
3						ADC1_SE6A/PTE2/SPI1_SCK/UART1_CTS/SDHC0_DCLK
4						ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD
5						PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3
6						PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2
25						ADC1_SE16/OP1_OUT/CMP2_IN2/ADC0_SE22/OP0_DP2/OP1_DP2
26						ADC0_SE16/OP0_OUT/CMP1_IN2/ADC0_SE21/OP0_DP1/OP1_DP1
27						VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
34						DAC0_OUT/CMP1_IN3/ADC0_SE23/OP0_DP4/OP1_DP4
35						DAC1_OUT/CMP2_IN3/ADC1_SE23/OP0_DP5/OP1_DP5

VSS1 VSS2 VSS3 VSS4 VSSA

<Value>

8 49 74 88 24