



E5 E6 E7 E8 E9 F6 F8 H5

J5	TSIO_CH1/PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	VDD1	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	A5
J6	TSIO_CH2/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI	VDD2	ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS/FB_CS0	D4
K6	TSIO_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	VDD3	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4	C4
K7	TSIO_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	VDD4	PTD3/SPI0_SIN/UART2_TX/FB_AD3	B4
L7	TSIO_CH5/PTA4/LLWU_P3/FTM0_CH1/NMI/EZP_CS	VDD5	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	A4
M8	PTA5/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VDD6	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A3
J7	PTA6/FTM0_CH3/TRACE_CLKOUT	VDDA	ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT	A2
J8	ADC0_SE10/PTA7/FTM0_CH4/TRACE_D3		PTD10/UART5_RTS/FB_A18	A1
K8	ADC0_SE11/PTA8/FTM1_CH0/FTM1_QD_PHA/TRACE_D2		PTD8/I2C0_SCL/UART5_RX/FB_A16	C9
L8	PTA9/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1		PTD9/I2C0_SDA/UART5_TX/FB_A17	B9
M9	PTA10/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0		PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	B3
L9	PTA11/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB		PTD12/SPI2_SCK/SDHC0_D4/FB_A20	B2
K9	CMP2_IN0/PTA12/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD/FTM1_QD_PHA		PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	B1
J9	CMP2_IN1/PTA13/LLWU_P4/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB		PTD14/SPI2_SIN/SDHC0_D6/FB_A22	C3
L10	PTA14/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_TX_BCLK		PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	C2
L11	PTA15/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD			C1
K10	PTA16/SPI0_SOUT/UART0_CTS/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS			
K11	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK/I2S0_CLKIN		ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	D3
M12	EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0		ADC1_SE5A/PTE1/LLWU_P0/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL	D2
M11	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1		ADC1_SE6A/PTE2/LLWU_P1/SPI1_SCK/UART1_CTS/SDHC0_DCLK	D1
K12	PTA24/MII0_TXD2/FB_A29		ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD	E4
J12	PTA25/MII0_TXCLK/FB_A28		PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	E3
J11	PTA26/MII0_TXD3/FB_A27		PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	E2
J10	PTA27/MII0_CRS/FB_A26		PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN	E1
H12	PTA28/MII0_TXER/FB_A25		PTE7/UART3_RTS/I2S0_RXD	F4
H11	PTA29/MII0_COL/FB_A24		PTE8/UART5_TX/I2S0_RX_FS	F3
H10			PTE9/UART5_RX/I2S0_RX_BCLK	F2
H9	ADC0_SE8/ADC1_SE8/TSIO_CH0/PTB0/LLWU_P5/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA		PTE10/UART5_CTS/I2S0_TXD	F1
G12	ADC0_SE9/ADC1_SE9/TSIO_CH6/PTB1/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB		PTE11/UART5_RTS/I2S0_TX_FS	G4
G11	ADC0_SE12/TSIO_CH7/PTB2/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3		PTE12/I2S0_TX_BCLK	G3
G10	ADC0_SE13/TSIO_CH8/PTB3/I2C0_SDA/UART0_CTS/ENET0_1588_TMR1/FTM0_FLT0		ADC0_SE17/PTE24/CAN1_TX/UART4_TX/EWM_OUT	M4
G9	ADC1_SE10/PTB4/ENET0_1588_TMR2/FTM1_FLT0		ADC0_SE18/PTE25/CAN1_RX/UART4_RX/EWM_IN	K5
F12	ADC1_SE11/PTB5/ENET0_1588_TMR3/FTM2_FLT0		PTE26/UART4_CTS/ENET_1588_CLKIN/RTC_CLKOUT/USB_CLKIN	K4
F11	ADC1_SE12/PTB6/FB_AD23		PTE27/UART4_RTS	J4
F10	ADC1_SE13/PTB7/FB_AD22		PTE28	H4
F9	PTB8/UART3_RTS/FB_AD21			
E12	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20		USB0_DM	H2
E11	ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1		USB0_DP	H1
E10	ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2			
E9	TSIO_CH9/PTB16/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN		DAC0_OUT/CMP1_IN3/ADC0_SE23	L3
D12	TSIO_CH10/PTB17/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT		DAC1_OUT/CMP2_IN3/ADC1_SE23	L4
D11	TSIO_CH11/PTB18/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA			
D10	TSIO_CH12/PTB19/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB		ADC0_SE16/CMP1_IN2/ADC0_SE21	J3
D9	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT		ADC1_SE16/CMP2_IN2/ADC0_SE22	K3
C12	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT			
C11	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT		PGA0_DM/ADC0_DM0/ADC1_DM3	L2
B12	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28		PGA0_DP/ADC0_DP0/ADC1_DP3	L1
B11	ADC0_SE14/TSIO_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14		PGA1_DM/ADC1_DM0/ADC0_DM3	M2
A12	ADC0_SE15/TSIO_CH14/PTC1/LLWU_P6/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13		PGA1_DP/ADC1_DP0/ADC0_DP3	M1
A11	ADC0_SE4B/CMP1_IN0/TSIO_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12			
A9	CMP1_IN1/PTC3/LLWU_P7/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT		ADC0_DM1	J2
D8	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT		ADC0_DP1	J1
C8	PTC5/LLWU_P9/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT			
B8	CMP0_IN0/PTC6/LLWU_P10/SPI0_SOUT/PDB0_EXTRG/FB_AD9		ADC1_DM1	K2
A8	CMP0_IN1/PTC7/SPI0_SIN/FB_AD8		ADC1_DP1	K1
D7	ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN/FB_AD7			
C7	ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	M3
B7	ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS/FB_AD5		VOUT33	G1
A7	ADC1_SE7B/PTC11/LLWU_P11/I2C1_SDA/I2S0_RXD/FB_RW		VREGIN	G2
D6	PTC12/UART4_RTS/FB_AD27			
C6	PTC13/UART4_CTS/FB_AD26		VREFH	G5
B6	PTC14/UART4_RX/FB_AD25		VREFL	G6
A6	PTC15/UART4_TX/FB_AD24			
D5	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSIZ1/FB_BE23_16_BLS15_8		VBAT	L6
C5	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSIZ0/FB_BE31_24_BLS7_0			
B5	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8_BLS23_16		EXTAL32	M6
	PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0_BLS31_24/FB_TA		XTAL32	M7

VSS1 VSS2 VSS3 VSS4 VSS5 VSS6 VSS7 VSS8 VSSA NC1 NC2 NC3 NC4 RSVD

F6 F7 H3 G7 G8 H7 H8 M10 H6 M5 A10 B10 C10 L5

&lt;Value&gt;

RESET L12