



34	TSI0_CH1/PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	VDD1	8
35	TSI0_CH2/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI	VDD2	40
36	TSI0_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	VDD3	48
37	TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	VDD4	61
38	TSI0_CH5/PTA4/FTM0_CH1/NMI/EZP_CS	VDD5	75
39	PTA5/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VDD6	89
42	CMP2_IN0/PTA12/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD/FTM1_QD_PHA		
43	CMP2_IN1/PTA13/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB	VDDA	22
44	PTA14/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_TX_BCLK		
45	PTA15/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD	VBAT	30
46	PTA16/SPI0_SOUT/UART0_CTS/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS		
47	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK/I2S0_CLKIN	VREFH	23
50	XTAL/PTA18/FTM0_FLT2/FTM_CLKIN0		
51	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1	VREFL	24
53	ADC0_SE8/ADC1_SE8/TSI0_CH0/PTB0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA		
54	ADC0_SE9/ADC1_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB		
55	ADC0_SE12/TSI0_CH7/PTB2/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3		
56	ADC0_SE13/TSI0_CH8/PTB3/I2C0_SDA/UART0_CTS/ENET0_1588_TMR1/FTM0_FLT0		
57	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20		
58	ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1		
59	ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	ADC0_DM1	15
62	TSI0_CH9/PTB16/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN	ADC0_DP1	14
63	TSI0_CH10/PTB17/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT		
64	TSI0_CH11/PTB18/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	ADC1_DM1	17
65	TSI0_CH12/PTB19/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	ADC1_DP1	16
66	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT		
67	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	PGA1_DM/ADC1_DM0/ADC0_DM3	21
68	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	PGA1_DP/ADC1_DP0/ADC0_DP3	20
69	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	PGA0_DM/ADC0_DM0/ADC1_DM3	19
		PGA0_DP/ADC0_DP0/ADC1_DP3	18
70	ADC0_SE14/TSI0_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14		
71	ADC0_SE15/TSI0_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13		
72	ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12		
73	CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT	USB0_DM	11
76	PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	USB0_DP	10
77	PTC5/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT		
78	CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG/FB_AD9		
79	CMP0_IN1/PTC7/SPI0_SIN/FB_AD8		
80	ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN/FB_AD7		
81	ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0		
82	ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS/FB_AD5		
83	ADC1_SE7B/PTC11/I2C1_SDA/I2S0_RXD/FB_RW		
84	PTC12/UART4_RTS/FB_AD27	DAC0_OUT/CMP1_IN3/ADC0_SE23	27
85	PTC13/UART4_CTS/FB_AD26	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	26
86	PTC14/UART4_RX/FB_AD25		
87	PTC15/UART4_TX/FB_AD24		
90	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSI21/FB_BE23_16_BLS15_8		
91	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSI20/FB_BE31_24_BLS7_0	EXTAL32	29
92	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8_BLS23_16	XTAL32	28
93	PTD0/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS		
94	ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS/FB_CS0		
95	PTD2/SPI0_SOUT/UART2_RX/FB_AD4		
96	PTD3/SPI0_SIN/UART2_TX/FB_AD3	RESET	52
97	PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN		
98	ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT		
99	ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	VREGIN	13
100	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	VOUT33	12
1	ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA		
2	ADC1_SE5A/PTE1/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL		
3	ADC1_SE6A/PTE2/SPI1_SCK/UART1_CTS/SDHC0_DCLK	VSSA	25
4	ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD		
5	PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	VSS1	9
6	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	VSS2	41
7	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN	VSS3	49
31	ADC0_SE17/PTE24/CAN1_TX/UART4_TX/EWM_OUT	VSS4	60
32	ADC0_SE18/PTE25/CAN1_RX/UART4_RX/EWM_IN	VSS5	74
33	PTE26/UART4_CTS/ENET_1588_CLKIN/RTC_CLKOUT/USB_CLKIN	VSS6	88