



U?A

K6	VBAT		VDDA	F5
E6	VDD1	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD		B9
E5	VDD2	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13		D8
L10	VDD3	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12		C8
E7	VDD4	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT		B8
J6	PTA0/TSI0_CH1/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT		A8
H8	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI	PTC5/LLWU_P9/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT		D7
J7	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/FB_AD9		C7
H9	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	PTC7/CMP0_IN1/SPI0_SIN/FB_AD8		B7
J8	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/I2S0_CLKIN/FB_AD7		A7
K7	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0		D6
K8	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA	PTC10/ADC1_SE6B/CMP0_IN4/I2C1_SCL/I2S0_RX_FS/FB_AD5		C6
L8	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD/FB_RW		C5
K9	PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16_BLS15_8		D5
L9	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24_BLS7_0		C4
J10	PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS			K1
H10	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN	PGA0_DP/ADC0_DP0/ADC1_DP3		K2
L11	PTA18/EXTAL/FTM0_FLT2/FTM_CLKIN0	PGA0_DM/ADC0_DM0/ADC1_DM3		L1
K11	PTA19/XTAL/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1	PGA1_DP/ADC1_DP0/ADC0_DP3		L2
		PGA1_DM/ADC1_DM0/ADC0_DM3		L2
G11	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS		D4
G10	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0		D3
G9	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4		C3
G8	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/FTM0_FLT0	PTD3/SPI0_SIN/UART2_TX/FB_AD3		B3
D10	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN		A3
C10	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT		A2
B10	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0		B2
E9	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		A1
D9	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA			E4
C9	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHCO_D1/I2C1_SDA		E3
L5	EXTAL32	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHCO_D0/I2C1_SCL		E2
L4	XTAL32	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHCO_DCLK		F4
L7	RSVD	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHCO_CMD		H7
J11	RESET	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHCO_D3		G4
F7	VSS1	PTE5/SPI1_PCS2/UART3_RX/SDHCO_D2		F1
G7	VSS2	PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3		F2
L6	VSS3	PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPT0_ALT3		G1
G3	VSS4	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA		G2
K10	VSS5	PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL		L3
		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18		K5
		DAC0_OUT/CMP1_IN3/ADC0_SE23		G5
		VREFH		G6
		VREFL		F6
		VSSA		

Kinetis
K10

<Value>

A11	NC_A11
B11	NC_B11
C11	NC_C11
K3	NC_K3
H4	NC_H4
F3	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN
H1	ADC0_DP1
H2	ADC0_DM1
J1	ADC1_DP1
J2	ADC1_DM1
J3	ADC1_SE16/CMP2_IN2/ADC0_SE22
H3	ADC0_SE16/CMP1_IN2/ADC0_SE21
K4	DAC1_OUT/CMP2_IN3/ADC1_SE23
H5	PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT
J5	PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN
H6	PTE26/UART4_CTS/RTC_CLKOUT
J9	PTA10/FTM2_CH0/FTM2_QD_PHA/TRACE_D0
J4	PTA11/FTM2_CH1/FTM2_QD_PHB
H11	PTA29/FB_A24
F11	PTB6/ADC1_SE12/FB_AD23
E11	PTB7/ADC1_SE13/FB_AD22
D11	PTB8/UART3_RTS/FB_AD21
E10	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20
F10	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT
F9	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT
F8	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT
E8	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28
B6	PTC12/UART4_RTS/FB_AD27
A6	PTC13/UART4_CTS/FB_AD26
A5	PTC14/UART4_RX/FB_AD25
B5	PTC15/UART4_TX/FB_AD24
B4	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8_BLS23_16
A4	PTC19/UART3_CTS/FB_CS3/FB_BE7_0_BLS31_24/FB_TA
A10	PTD8/I2C0_SCL/UART5_RX/FB_A16
A9	PTD9/I2C0_SDA/UART5_TX/FB_A17
B1	PTD10/UART5_RTS/FB_A18
C2	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19
C1	PTD12/SPI2_SCK/SDHC0_D4/FB_A20
D2	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21
D1	PTD14/SPI2_SIN/SDHC0_D6/FB_A22
E1	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23