Pin	Signal
L6	VBAT
E5	VDD1
E6	VDD2
E7	VDD3
E8	VDD4
F5	VDD5
F8	VDD6
H5	VDDA
G5	VREFH
M7	EXTAL32
	XTAL32
J5	PTA0/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK
J6	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI
K6	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO
K7	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO
L7	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS
M8	PTA5/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST
J7	PTA6/FTM0_CH3/TRACE_CLKOUT
J8	PTA7/ADC0_SE10/FTM0_CH4/TRACE_D3
K8	PTA8/ADC0_SE11/FTM1_CH0/FTM1_QD_PHA/TRACE_D2
L8	PTA9/FTM1_CH1/FTM1_QD_PHB/TRACE_D1
M9	PTA10/FTM2_CH0/FTM2_QD_PHA/TRACE_D0
L9	PTA11/FTM2_CH1/FTM2_QD_PHB
K9	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA
J9	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
L10	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1
L11	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0
K10	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1
K11	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK
M12	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
M11	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
K12	PTA24/FB_A29
J12	PTA25/FB_A28
J11	PTA26/FB_A27
J10	PTA27/FB_A26
H12	PTA28/FB_A25
H11	PTA29/FB_A24
H10	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
H9	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
G12	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
G11	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
G10	PTB4/ADC1_SE10/FTM1_FLT0
G9	PTB5/ADC1_SE11/FTM2_FLT0
F12	PTB6/ADC1_SE12/FB_AD23
F11	PTB7/ADC1_SE13/FB_AD22
F10	PTB8/UART3_RTS/FB_AD21
F9	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20
E12	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1
E11	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2
E10	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN
E9	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT
D12	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA
D11	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB
D10	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT
D9	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT
C12	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT
C11	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28
M1	PGA1_DP/ADC1_DP0/ADC0_DP3
M2	PGA1_DM/ADC1_DM0/ADC0_DM3
L1	PGA0_DP/ADC0_DP0/ADC1_DP3
L2	PGA0_DM/ADC0_DM0/ADC1_DM3
J1	ADC0_DP1
J2	ADC0_DM1
K1	ADC1_DP1
K2	ADC1_DM1
J3	ADC0_SE16/CMP1_IN2/ADC0_SE21
K3	ADC1_SE16/CMP2_IN2/ADC0_SE22
L3	DAC0_OUT/CMP1_IN3/ADC0_SE23
L4	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23
M3	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
L12	RESET
B12	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1
B11	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0
A12	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS
A11	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK
A9	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT
D8	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT
C8	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK
B8	PTC7/CMP0_IN1/SPI0_SIN/I2S0_RX_FS/FB_AD8
A8	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7
D7	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0
C7	PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5
B7	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW
A7	PTC12/UART4_RTS/FB_AD27
D6	PTC13/UART4_CTS/FB_AD26
C6	PTC14/UART4_RX/FB_AD25
B6	PTC15/UART4_TX/FB_AD24
A6	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16
D5	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24
C5	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8
B5	PTC19/UART3_CTS/FB_CS3/FB_BE7_0/FB_TA
A5	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS
D4	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0
C4	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4
B4	PTD3/SPI0_SIN/UART2_TX/FB_AD3
A4	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN
A3	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT
A2	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0
A1	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
C9	PTD8/I2C0_SCL/UART5_RX/FB_A16
B9	PTD9/I2C0_SDA/UART5_TX/FB_A17
B3	PTD10/UART5_RTS/FB_A18
B2	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19
B1	PTD12/SPI2_SCK/SDHC0_D4/FB_A20
C3	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21
C2	PTD14/SPI2_SIN/SDHC0_D6/FB_A22
C1	PTD15/SPI2_PCS1/SDHC0_D7/FB_A23
D3	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA/RTC_CLKOUT
D2	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL/SPI1_SIN
D1	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK
E4	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/SPI1_SOUT
E3	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3
E2	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2
E1	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK
F4	PTE7/UART3_RTS/I2S0_RXD0
F3	PTE8/I2S0_RXD1/UART5_TX/I2S0_RX_FS
F2	PTE9/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK
F1	PTE10/UART5_CTS/I2S0_TXD0
G4	PTE11/UART5_RTS/I2S0_TX_FS
G3	PTE12/I2S0_TX_BCLK
H1	PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3
H2	PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3
G1	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA
G2	PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL
M4	PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT
K5	PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN
K4	PTE26/UART4_CTS/RTC_CLKOUT
J4	PTE27/UART4_RTS
H4	PTE28
M5	NC_M5
A10	NC_A10
B10	NC_B10
C10	NC_C10
L5	RTC_WAKEUP
F6	VSS1
M10	VSS2
F7	VSS3
H3	VSS4
G7	VSS5
G8	VSS6
H7	VSS7
H8	VSS8
H6	VSSA
G6	VREFL

<Value>