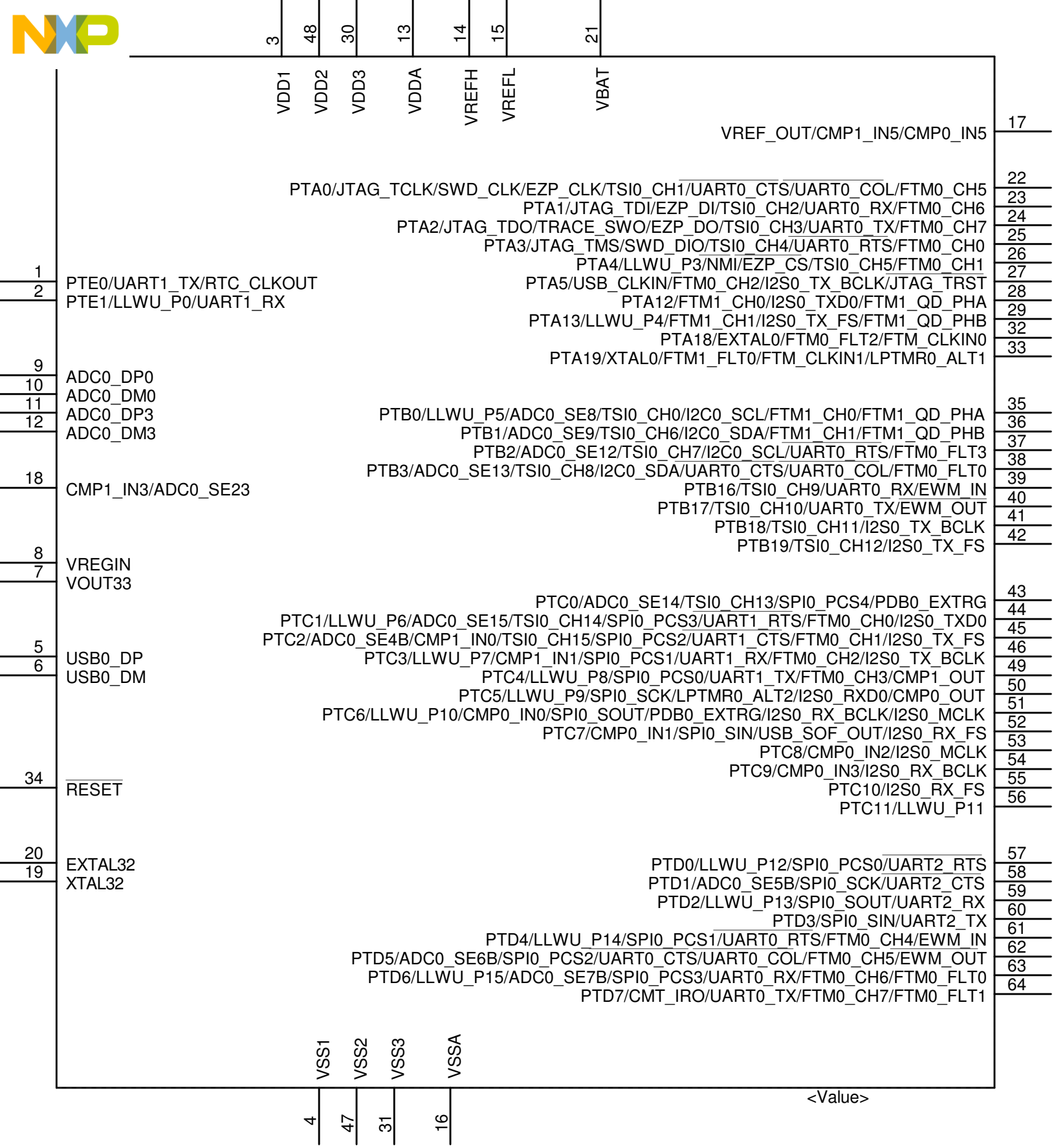
3 VDD1
48 VDD2
30 VDD3
13 VDDA
14 VREFH
15 VREFL
21 VBAT
17 VREF_OUT/CMP1_IN5/CMP0_IN5
22 PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5
23 PTA1/JTAG_TDI/EZP_DI/TSI0_CH2/UART0_RX/FTM0_CH6
24 PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/UART0_TX/FTM0_CH7
25 PTA3/JTAG_TMS/SWD_DIO/TSI0_CH4/UART0_RTS/FTM0_CH0
26 PTA4/LLWU_P3/NMI/EZP_CS/TSI0_CH5/FTM0_CH1
1 PTE0/UART1_TX/RTC_CLKOUT
27 PTA5/USB_CLKIN/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST
2 PTE1/LLWU_P0/UART1_RX
28 PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA
29 PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
32 PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
33 PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
9 ADC0_DP0
10 ADC0_DM0
11 ADC0_DP3
12 ADC0_DM3
35 PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
36 PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
37 PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
38 PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
18 CMP1_IN3/ADC0_SE23
39 PTB16/TSI0_CH9/UART0_RX/EWM_IN
40 PTB17/TSI0_CH10/UART0_TX/EWM_OUT
41 PTB18/TSI0_CH11/I2S0_TX_BCLK
42 PTB19/TSI0_CH12/I2S0_TX_FS
8 VREGIN
7 VOUT33
43 PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG
44 PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0
45 PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS
5 USB0_DP
46 PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK
6 USB0_DM
49 PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT
50 PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT
51 PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK
52 PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS
53 PTC8/CMP0_IN2/I2S0_MCLK
54 PTC9/CMP0_IN3/I2S0_RX_BCLK
34 RESET
55 PTC10/I2S0_RX_FS
56 PTC11/LLWU_P11
20 EXTAL32
19 XTAL32
57 PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS
58 PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS
59 PTD2/LLWU_P13/SPI0_SOUT/UART2_RX
60 PTD3/SPI0_SIN/UART2_TX
61 PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN
62 PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT
63 PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0
64 PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
VSS1 4
VSS2 47
VSS3 31
VSSA 16
<Value>