



	C5	E4	G7	F4	G4	G3	H5		
	VDD1	VDD2	VDD3	VDDA	VREFH	VREFL	VBAT		
A1 B1	PTE0/UART1_TX/RTC_CLKOUT						VREF_OUT/CMP1_IN5/CMP0_IN5		H1
	PTE1/LLWU_P0/UART1_RX								
G1 F1	ADC0_DP0						JTAG_TCLK/SWD_CLK/EZP_CLK/TSI0_CH1/PTA0/UART0_CTS/UART0_COL/FTM0_CH5		D3
G2 F2	ADC0_DM0						JTAG_TD0/EZP_DI/TSI0_CH2/PTA1/UART0_RX/FTM0_CH6		D4
	ADC0_DP3						JTAG_TDO/TRACE_SWO/EZP_DO/TSI0_CH3/PTA2/UART0_TX/FTM0_CH7		E5
	ADC0_DM3						JTAG_TMS/SWD_DIO/TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0		D5
							NMI/EZP_CS/TSI0_CH5/PTA4/LLWU_P3/FTM0_CH1		G5
H2	CMP1_IN3/ADC0_SE23						PTA5/USB_CLKIN/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST		F5
							PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA		H6
							PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB		G6
							EXTAL0/PTA18/FTM0_FLT2/FTM_CLKIN0		H8
							XTAL0/PTA19/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1		G8
D2 E2	VREGIN								
	VOUT33								
E1 D1	USB0_DP						ADC0_SE8/TSI0_CH0/PTB0/LLWU_P5/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA		F7
	USB0_DM						ADC0_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB		F6
							ADC0_SE12/TSI0_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3		E7
							ADC0_SE13/TSI0_CH8/PTB3/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0		E8
							TSI0_CH9/PTB16/UART0_RX/EWM_IN		E6
							TSI0_CH10/PTB17/UART0_TX/EWM_OUT		D7
F8	RESET						TSI0_CH11/PTB18/I2S0_TX_BCLK		D6
							TSI0_CH12/PTB19/I2S0_TX_FS		C7
H4 H3	EXTAL32						ADC0_SE14/TSI0_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG		D8
	XTAL32						ADC0_SE15/TSI0_CH14/PTC1/LLWU_P6/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0		C6
							ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS		B7
							CMP1_IN1/PTC3/LLWU_P7/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK		C8
							PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT		B8
							PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT		A8
							CMP0_IN0/PTC6/LLWU_P10/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK		A7
							CMP0_IN1/PTC7/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS		B6
							CMP0_IN2/PTC8/I2S0_MCLK		A6
							CMP0_IN3/PTC9/I2S0_RX_BCLK		B5
							PTC10/I2S0_RX_FS		B4
							PTC11/LLWU_P11		A5
							PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS		C3
							ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS		A4
							PTD2/LLWU_P13/SPI0_SOUT/UART2_TX		C2
							PTD3/SPI0_SIN/UART2_TX		B3
							PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN		A3
							ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT		C1
							ADC0_SE7B/PTD6/LLWU_P15/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		B2
							PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		A2
	VSS1								
	VSS2								
	VSS3								
	VSSA								
C4									
E3									
H7									
F3									
							<Value>		