



		VREGIN	G2
		VOUT33	G1
L10	VDD2		F2
E5	VDD3	USB0_DM	F1
F5	VDDA	USB0_DP	
G5	VREFH		J6
L3	VREF_OUT/CMP1_IN5/CMP0_IN5	PTA0/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK	H8
K1		PTA1/UART0_RX/FTM0_CH6/JTAG_TD/EZP_DI	J7
K2	ADC0_DP0	PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	H9
	ADC0_DM0	PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	J8
L1		PTA4/LLWU_P3/FTM0_CH1/NMI/EZP_CS	K7
L2	ADC0_DP3	PTA5/USB_CLKIN/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST	K8
	ADC0_DM3	PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA	L8
K5	DAC0_OUT/CMP1_IN3/ADC0_SE23	PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB	K9
J11	RESET	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1	L9
L7	RTC_WAKEUP	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0	J10
		PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1	H10
L5	EXTAL32	PTA17/SPI0_SIN/UART0_RTS/I2S0_MCLK	L11
L4	XTAL32	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	K11
		PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	
A10	NC_A10	PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	G11
A11	NC_A11	PTB1/ADC0_SE9/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	G10
A4	NC_A4	PTB2/ADC0_SE12/I2C0_SCL/UART0_RTS/FTM0_FLT3	G9
A5	NC_A5	PTB3/ADC0_SE13/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0	G8
A9	NC_A9	PTB10/SPI1_PCS0/UART3_RX/FTM0_FLT1	D10
B1	NC_B1	PTB11/SPI1_SCK/UART3_TX/FTM0_FLT2	C10
B4	NC_B4	PTB12/UART3_RTS/FTM1_CH0/FTM0_CH4/FTM1_QD_PHA	B11
B5	NC_B5	PTB13/UART3_CTS/FTM1_CH1/FTM0_CH5/FTM1_QD_PHB	C11
C1	NC_C1	PTB16/SPI1_SOUT/UART0_RX/EWM_IN/FTM_CLKIN0	B10
C2	NC_C2	PTB17/SPI1_SIN/UART0_TX/EWM_OUT/FTM_CLKIN1	E9
D1	NC_D1	PTB18/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA	D9
D11	NC_D11	PTB19/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB	C9
D2	NC_D2		
E1	NC_E1	PTC0/ADC0_SE14/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1	B9
E10	NC_E10	PTC1/LLWU_P6/ADC0_SE15/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0	D8
E11	NC_E11	PTC2/ADC0_SE4B/CMP1_IN0/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS	C8
E7	NC_E7	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK	B8
E8	NC_E8	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT	A8
F10	NC_F10	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT/FTM0_CH2	D7
F11	NC_F11	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK	C7
F3	NC_F3	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS	B7
F7	NC_F7	PTC8/CMP0_IN2/I2S0_MCLK	A7
F8	NC_F8	PTC9/CMP0_IN3/I2S0_RX_BCLK/FTM2_FLT0	D6
F9	NC_F9	PTC10/I2C1_SCL/I2S0_RX_FS	C6
H1	NC_H1	PTC11/LLWU_P11/I2C1_SDA/I2S0_RXD1	C5
H11	NC_H11	PTC12	B6
H2	NC_H2	PTC13	A6
H3	NC_H3	PTC16/UART3_RX	D5
H4	NC_H4	PTC17/UART3_TX	C4
H5	NC_H5		
H6	NC_H6		
J1	NC_J1	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS	D4
J2	NC_J2	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS	D3
J3	NC_J3	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/I2C0_SCL	C3
J4	NC_J4	PTD3/SPI0_SIN/UART2_TX/I2C0_SDA	B3
J5	NC_J5	PTD4/LLWU_P14/ADC0_SE21/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN	A3
J9	NC_J9	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	A2
K3	NC_K3	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0	B2
K4	NC_K4	PTD7/ADC0_SE22/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	A1
G7	VSS1	PTE0/ADC0_SE10/SPI1_PCS1/UART1_TX/TRACE_CLKOUT/I2C1_SDA/RTC_CLKOUT	E4
L6	VSS2	PTE1/LLWU_P0/ADC0_SE11/SPI1_SOUT/UART1_RX/TRACE_D3/I2C1_SCL/SPI1_SIN	E3
K10	VSS3	PTE2/LLWU_P1/ADC0_DP1/SPI1_SCK/UART1_CTS/TRACE_D2	E2
G3	VSS4	PTE3/ADC0_DM1/SPI1_SIN/UART1_RTS/TRACE_D1/SPI1_SOUT	F4
		PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/TRACE_D0	H7
F6	VSSA	PTE5/SPI1_PCS2/UART3_RX	G4
G6	VREFL		

<Value>