



28	TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5	VBAT	25
29	TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6		7
30	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7	VDD1	38
31	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	VDD2	
32	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1		17
33	PTA5/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST	VDDA	18
34	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA	VREFH	
35	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB		62
36	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1	VLL1	61
37	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0	VLL2	60
40	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1	VLL3	
41	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK		
43	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0		
44	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1		
45	PTB0/LLWU_P5/LCD_P0/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	VCAP1	64
46	PTB1/LCD_P1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB	VCAP2	63
47	PTB2/LCD_P2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3		
48	PTB3/LCD_P3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0		
49	PTB8/LCD_P8/UART3_RTS		21
50	PTB9/LCD_P9/SPI1_PCS1/UART3_CTS	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	
51	PTB10/LCD_P10/ADC1_SE14/SPI1_PCS0/UART3_RX/FTM0_FLT1		22
52	PTB11/LCD_P11/ADC1_SE15/SPI1_SCK/UART3_TX/FTM0_FLT2	DAC0_OUT/CMP1_IN3/ADC0_SE23	
53	PTB16/LCD_P12/TSIO_CH9/SPI1_SOUT/UART0_RX/EWM_IN		
54	PTB17/LCD_P13/TSIO_CH10/SPI1_SIN/UART0_TX/EWM_OUT		
55	PTB18/LCD_P14/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA		
56	PTB19/LCD_P15/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB		
57	PTC0/LCD_P20/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1	XTAL32	23
58	PTC1/LLWU_P6/LCD_P21/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0	EXTAL32	24
59	PTC2/LCD_P22/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS		42
60	PTC3/LLWU_P7/LCD_P23/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	RESET	
61	PTC4/LLWU_P8/LCD_P24/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT		
62	PTC5/LLWU_P9/LCD_P25/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT		
63	PTC6/LLWU_P10/LCD_P26/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK		13
64	PTC7/LCD_P27/CMP0_IN1/SPI0_SIN/I2S0_RX_FS	PGA0_DP/ADC0_DP0/ADC1_DP3	14
65	PTC8/LCD_P28/ADC1_SE4B/CMP0_IN2/I2S0_MCLK	PGA0_DM/ADC0_DM0/ADC1_DM3	
66	PTC9/LCD_P29/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FTM2_FLT0		15
67	PTC10/LCD_P30/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS	PGA1_DP/ADC1_DP0/ADC0_DP3	16
68	PTC11/LLWU_P11/LCD_P31/ADC1_SE7B/I2C1_SDA/I2S0_RXD1	PGA1_DM/ADC1_DM0/ADC0_DM3	
73	PTD0/LLWU_P12/LCD_P40/SPI0_PCS0/UART2_RTS		
74	PTD1/LCD_P41/ADC0_SE5B/SPI0_SCK/UART2_CTS		
75	PTD2/LLWU_P13/LCD_P42/SPI0_SOUT/UART2_RX		
76	PTD3/LCD_P43/SPI0_SIN/UART2_TX		
77	PTD4/LLWU_P14/LCD_P44/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN		
78	PTD5/LCD_P45/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT		
79	PTD6/LLWU_P15/LCD_P46/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0		
80	PTD7/LCD_P47/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		
1	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/I2C1_SDA/RTC_CLKOUT		8
2	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/I2C1_SCL/SPI1_SIN	VSS1	39
3	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS	VSS2	59
4	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SPI1_SOUT	VSS3	
5	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX		
6	PTE5/SPI1_PCS2/UART3_RX		
9	PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	VSSA	20
10	PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3	VREFL	19
11	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA		
12	PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL		