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34	X1AL32			
35	PTA0/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK		PTD0/LLWU_P12/LCD_P40/SPI0_PCS0/UART2_RTS	93
36	PTA1/TSIO_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI		PTD1/LCD_P41/ADC0_SE5B/SPI0_SCK/UART2_CTS	94
37	PTA2/TSIO_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO		PTD2/LLWU_P13/LCD_P42/SPI0_SOUT/UART2_RX	95
38	PTA3/TSIO_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO		PTD3/LCD_P43/SPI0_SIN/UART2_TX	96
39	PTA4/LLWU_P3/TSIO_CH5/FTM0_CH1/NMI/EZP_CS		PTD4/LLWU_P14/LCD_P44/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN	97
42	PTA5/FTM0_CH2/CMP2_OUT/I2S0_TX_BCLK/JTAG_TRST		PTD5/LCD_P45/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT	98
43	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA		PTD6/LLWU_P15/LCD_P46/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0	99
44	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB		PTD7/LCD_P47/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	100
45	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1			
46	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0			
47	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1		PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/I2C1_SDA/RTC_CLKOUT	1
50	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK		PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/I2C1_SCL/SPI1_SIN	2
51	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0		PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS	3
	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1		PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SPI1_SOUT	4
			PTE4/LLWU_P2/SPI1_PCS0/UART3_TX	5
53	PTB0/LLWU_P5/LCD_P0/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA		PTE5/SPI1_PCS2/UART3_RX	6
54	PTB1/LCD_P1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB		PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK	7
55	PTB2/LCD_P2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3		PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	10
56	PTB3/LCD_P3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0		PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3	11
57	PTB7/LCD_P7/ADC1_SE13		PTE18/ADC0_SE6A/SPI0_SOUT/UART2_CTS/I2C0_SDA	12
58	PTB8/LCD_P8/UART3_RTS		PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL	13
59	PTB9/LCD_P9/SPI1_PCS1/UART3_CTS		PTE24/ADC0_SE17/UART4_TX/EWM_OUT	31
60	PTB10/LCD_P10/ADC1_SE14/SPI1_PCS0/UART3_RX/FTM0_FLT1		PTE25/ADC0_SE18/UART4_RX/EWM_IN	32
61	PTB11/LCD_P11/ADC1_SE15/SPI1_SCK/UART3_TX/FTM0_FLT2		PTE26/UART4_CTS/RTC_CLKOUT	33
62	PTB16/LCD_P12/TSIO_CH9/SPI1_SOUT/UART0_RX/EWM_IN			
63	PTB17/LCD_P13/TSIO_CH10/SPI1_SIN/UART0_TX/EWM_OUT			
64	PTB18/LCD_P14/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA		PTC0/LCD_P20/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1	70
65	PTB19/LCD_P15/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB		PTC1/LLWU_P6/LCD_P21/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0	71
66	PTB20/LCD_P16/CMP0_OUT		PTC2/LCD_P22/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS	72
67	PTB21/LCD_P17/CMP1_OUT		PTC3/LLWU_P7/LCD_P23/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	73
68	PTB22/LCD_P18/CMP2_OUT		PTC4/LLWU_P8/LCD_P24/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT	80
69	PTB23/LCD_P19/SPI0_PCS5		PTC5/LLWU_P9/LCD_P25/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT	81
18	PGA0_DP/ADC0_DP0/ADC1_DP3		PTC6/LLWU_P10/LCD_P26/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK	82
19	PGA0_DM/ADC0_DM0/ADC1_DM3		PTC7/LCD_P27/CMP0_IN1/SPI0_SIN/I2S0_RX_FS	83
20	PGA1_DP/ADC1_DP0/ADC0_DP3		PTC8/LCD_P28/ADC1_SE4B/CMP0_IN2/I2S0_MCLK	84
21	PGA1_DM/ADC1_DM0/ADC0_DM3		PTC9/LCD_P29/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FTM2_FLT0	85
14	ADC0_DP1		PTC10/LCD_P30/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS	86
15	ADC0_DM1		PTC11/LLWU_P11/LCD_P31/ADC1_SE7B/I2C1_SDA/I2S0_RXD1	87
16	ADC1_DP1		PTC16/LCD_P36/UART3_RX	90
17	ADC1_DM1		PTC17/LCD_P37/UART3_TX	91
			PTC18/LCD_P38/UART3_RTS	92
52	RESET		VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	26
			DAC0_OUT/CMP1_IN3/ADC0_SE23	27

9	VSS1
41	VSS2
49	VSS3
74	VSS4
88	VSS5
25	VSSA
24	VREFL

<Value>