NXP

Pin	Top power pins
12	VREFH
11	VDDA
52	VCAP1
51	VCAP2
50	VLL1
49	VLL2
48	VLL3
1	VDD1
30	VDD2
24	VBAT
6	VREGIN
5	VOUT33

Left side:

Pin	Signal
23	EXTAL32
22	XTAL32
25	PTA0/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK
26	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI
27	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO
28	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO
29	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS
32	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
33	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
35	PTB0/LLWU_P5/LCD_P0/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
36	PTB1/LCD_P1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
37	PTB2/LCD_P2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
38	PTB3/LCD_P3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
39	PTB16/LCD_P12/TSI0_CH9/UART0_RX/EWM_IN
40	PTB17/LCD_P13/TSI0_CH10/UART0_TX/EWM_OUT
41	PTB18/LCD_P14/TSI0_CH11/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA
42	PTB19/LCD_P15/TSI0_CH12/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB
10	PGA0_DM/ADC0_DM0/ADC1_DM3
9	PGA0_DP/ADC0_DP0/ADC1_DP3
8	ADC1_DM1/OP1_DM0
7	ADC1_DP1/OP1_DP0/OP1_DM1
20	TRI0_DP
19	TRI0_DM
18	TRI0_OUT/OP1_DM2
34	RESET

Right side:

Pin	Signal
4	USB0_DM
3	USB0_DP
43	PTC0/LCD_P20/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1
44	PTC1/LLWU_P6/LCD_P21/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/I2S0_TXD0
45	PTC2/LCD_P22/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS
46	PTC3/LLWU_P7/LCD_P23/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK
53	PTC4/LLWU_P8/LCD_P24/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT
54	PTC5/LLWU_P9/LCD_P25/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT
55	PTC6/LLWU_P10/LCD_P26/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK
56	PTC7/LCD_P27/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS
57	PTD0/LLWU_P12/LCD_P40/SPI0_PCS0/UART2_RTS
58	PTD1/LCD_P41/ADC0_SE5B/SPI0_SCK/UART2_CTS
59	PTD2/LLWU_P13/LCD_P42/SPI0_SOUT/UART2_RX
60	PTD3/LCD_P43/SPI0_SIN/UART2_TX
61	PTD4/LLWU_P14/LCD_P44/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN
62	PTD5/LCD_P45/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT
63	PTD6/LLWU_P15/LCD_P46/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0
64	PTD7/LCD_P47/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
17	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
21	DAC0_OUT/CMP1_IN3/ADC0_SE23/OP0_DP4/OP1_DP4
15	ADC1_SE16/OP1_OUT/CMP2_IN2/ADC0_SE22/OP0_DP2/OP1_DP2
16	ADC0_SE16/OP0_OUT/CMP1_IN2/ADC0_SE21/OP0_DP1/OP1_DP1

Bottom pins:

Pin	Signal
2	VSS1
31	VSS2
47	VSS3
14	VSSA
13	VREFL

<Value>