



5 16 43 56 70 94 108 122 135 31 32 42

63	ADC0_DP1	VDD1	VREFH	VBAT	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	37
26	ADC1_DM1	VDD2			VREGIN	22
25	ADC1_DP1	VDD3			VOUT33	21
36	ADC0_SE16/CMP1_IN2/ADC0_SE21	VDD4			USB0_DM	20
35	ADC1_SE16/CMP2_IN2/ADC0_SE22	VDD5			USB0_DP	19
28	PGA0_DM/ADC0_DM0/ADC1_DM3	VDD6				
27	PGA0_DP/ADC0_DP0/ADC1_DP3	VDD7				
30	PGA1_DM/ADC1_DM0/ADC0_DM3	VDD8				
29	PGA1_DP/ADC1_DP0/ADC0_DP3	VDD9				
38	DAC0_OUT/CMP1_IN3/ADC0_SE23	VDDA				
39	DAC1_OUT/CMP2_IN3/ADC1_SE23	VDDA				
50	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/FTM0_CH5	VDDA			PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14	103
51	PTA1/JTAG_TDI/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6	VDDA			PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13	104
52	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7	VDDA			PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12	105
53	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	VDDA			PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT	106
54	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1	VDDA			PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT	109
55	PTA5/FTM0_CH2/RMII0_RXER/MII0_RXER/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VDDA			PTC5/LLWU_P9/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT	110
58	PTA6/FTM0_CH3/TRACE_CLKOUT	VDDA			PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/FB_AD9	111
59	PTA7/ADC0_SE10/FTM0_CH4/TRACE_D3	VDDA			PTC7/CMP0_IN1/SPI0_SIN/FB_AD8	112
60	PTA8/ADC0_SE11/FTM1_CH0/FTM1_QD_PHA/TRACE_D2	VDDA			PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/I2S0_CLKIN/FB_AD7	113
61	PTA9/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1	VDDA			PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0	114
62	PTA10/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0	VDDA			PTC10/ADC1_SE6B/CMP0_IN4/I2C1_SCL/I2S0_RX_FS/FB_AD5	115
63	PTA11/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB	VDDA			PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD/FB_RW	116
64	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD/FTM1_QD_PHA	VDDA			PTC12/UART4_RTS/FB_AD27	117
65	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB	VDDA			PTC13/UART4_CTS/FB_AD26	118
66	PTA14/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_TX_BCLK	VDDA			PTC14/UART4_RX/FB_AD25	119
67	PTA15/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD	VDDA			PTC15/UART4_TX/FB_AD24	120
68	PTA16/SPI0_SOUT/UART0_CTS/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS	VDDA			PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSIZ1/FB_BE23_16	123
69	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK/I2S0_CLKIN	VDDA			PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSIZ0/FB_BE31_24	124
72	PTA18/EXTAL/FTM0_FLT2/FTM_CLKIN0	VDDA			PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8	125
73	PTA19/XTAL/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1	VDDA			PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0/FB_TA	126
75	PTA24/MII0_TXD2/FB_A29	VDDA				
76	PTA25/MII0_TXCLK/FB_A28	VDDA			PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS	127
77	PTA26/MII0_TXD3/FB_A27	VDDA			PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0	128
78	PTA27/MII0_CRS/FB_A26	VDDA			PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4	129
79	PTA28/MII0_TXER/FB_A25	VDDA			PTD3/SPI0_SIN/UART2_TX/FB_AD3	130
80	PTA29/MII0_COL/FB_A24	VDDA			PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	131
81	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA	VDDA			PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT	132
82	PTB1/ADC0_SE9/ADC1_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB	VDDA			PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	133
83	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3	VDDA			PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	136
84	PTB3/ADC0_SE13/TSIO_CH8/I2C0_SDA/UART0_CTS/ENET0_1588_TMR1/FTM0_FLT0	VDDA			PTD8/I2C0_SCL/UART5_RX/FB_A16	137
85	PTB4/ADC1_SE10/ENET0_1588_TMR2/FTM1_FLT0	VDDA			PTD9/I2C0_SDA/UART5_TX/FB_A17	138
86	PTB5/ADC1_SE11/ENET0_1588_TMR3/FTM2_FLT0	VDDA			PTD10/UART5_RTS/FB_A18	139
87	PTB6/ADC1_SE12/FB_AD23	VDDA			PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19	140
88	PTB7/ADC1_SE13/FB_AD22	VDDA			PTD12/SPI2_SCK/SDHC0_D4/FB_A20	141
89	PTB8/UART3_RTS/FB_AD21	VDDA			PTD13/SPI2_SOUT/SDHC0_D5/FB_A21	142
90	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	VDDA			PTD14/SPI2_SIN/SDHC0_D6/FB_A22	143
91	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1	VDDA			PTD15/SPI2_PCS1/SDHC0_D7/FB_A23	144
92	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2	VDDA				
95	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN	VDDA			PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	1
97	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT	VDDA			PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL	2
98	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	VDDA			PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK	3
99	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	VDDA			PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD	4
100	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT	VDDA			PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3	7
101	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT	VDDA			PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	8
102	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT	VDDA			PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN	9
102	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28	VDDA			PTE7/UART3_RTS/I2S0_RXD	10
40	XTAL32	VDDA			PTE8/UART5_TX/I2S0_RX_FS	11
41	EXTAL32	VDDA			PTE9/UART5_RX/I2S0_RX_BCLK	12
74	RESET	VDDA			PTE10/UART5_CTS/I2S0_TXD	13
		VDDA			PTE11/UART5_RTS/I2S0_TX_FS	14
		VDDA			PTE12/I2S0_TX_BCLK	15
		VDDA			PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT	45
		VDDA			PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN	46
		VDDA			PTE26/UART4_CTS/ENET_1588_CLKIN/RTC_CLKOUT/USB_CLKIN	47
		VDDA			PTE27/UART4_RTS	48
		VDDA			PTE28	49

VSS1  
VSS2  
VSS3  
VSS4  
VSS5  
VSS6  
VSS7  
VSS8  
VSS9  
VSS10  
VSSA  
VREFL

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