



22	PTA0/IRQ_0/LLWU_P7/ADC0_SE12/CMP0_IN2/TPM1_CH0/SWD_CLK	VDD/VREFH	PTB0/IRQ_8/LLWU_P4/ADC0_SE6/EXTRG_IN/SPI0_SCK	12
23	PTA1/IRQ_1/LPTMR0_ALT1/TPM_CLKIN0/RESET		PTB1/IRQ_9/ADC0_SE5/CMP0_IN3/UART0_TX/UART0_RX	13
24	PTA2/CMP0_OUT/SWD_DIO		PTB2/IRQ_10/LLWU_P5/ADC0_SE4/UART0_RX/UART0_TX	14
5	PTA3/EXTAL0/I2C0_SCL/I2C0_SDA		PTB3/IRQ_14/I2C0_SCL/UART0_TX	17
6	PTA4/LLWU_P0/XTAL0/I2C0_SDA/I2C0_SCL		PTB4/IRQ_15/LLWU_P6/I2C0_SDA/UART0_RX	18
7	PTA5/LLWU_P1/RTC_CLK_IN/TPM0_CH5/SPI0_SS		PTB5/IRQ_16/ADC0_SE1/CMP0_IN1/TPM1_CH1/NMI	19
8	PTA6/LLWU_P2/TPM0_CH4/SPI0_MISO		PTB6/IRQ_2/LPTMR0_ALT3/TPM0_CH3/TPM_CLKIN1	1
11	PTA7/IRQ_7/LLWU_P3/ADC0_SE7/SPI0_MISO/SPI0_MOSI		PTB7/IRQ_3/TPM0_CH2	2
15	PTA8/ADC0_SE3	VSS/VREFL	PTB10/ADC0_SE9/TPM0_CH1	9
16	PTA9/ADC0_SE2	EP_PAD	PTB11/ADC0_SE8/TPM0_CH0	10
20	PTA12/IRQ_17/LPTMR0_ALT2/ADC0_SE0/CMP0_IN0/TPM1_CH0/TPM_CLKIN0		PTB13/ADC0_SE13/TPM1_CH1/RTC_CLKOUT	21
			<Value>	
		4		
		25		