



7 38 60 17 18

26	PTA0/TPM0_CH5/SWD_CLK	VDD1							
27	PTA1/UART0_RX/TPM2_CH0	VDD2							
28	PTA2/UART0_TX/TPM2_CH1	VDD3							
29	PTA3/SWD_DIO/I2C1_SCL/TPM0_CH0							VREGIN	12
30	PTA4/NMI/I2C1_SDA/TPM0_CH1							VOUT33	11
31	PTA5/USB_CLKIN/TPM0_CH2								
32	PTA12/TPM1_CH0							USB0_DP	9
33	PTA13/TPM1_CH1							USB0_DM	10
34	PTA14/SPI0_PCS0/UART0_TX								
35	PTA15/SPI0_SCK/UART0_RX								
36	PTA16/SPI0_MOSI/SPI0_MISO								
37	PTA17/SPI0_MISO/SPI0_MOSI								
40	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0								
41	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1								
42	PTA20/RESET								80
									79
									78
43	PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/TPM1_CH0								77
44	PTB1/ADC0_SE9/I2C0_SDA/TPM1_CH1								76
45	PTB2/ADC0_SE12/I2C0_SCL/TPM2_CH0								75
46	PTB3/ADC0_SE13/I2C0_SDA/TPM2_CH1								74
47	PTB8/EXTRG_IN								73
48	PTB9								
49	PTB10/SPI1_PCS0								
50	PTB11/SPI1_SCK								
51	PTB16/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO								
52	PTB17/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI								
53	PTB18/TPM2_CH0								
54	PTB19/TPM2_CH1								1
									2
									3
									4
55	PTC0/ADC0_SE14/EXTRG_IN/CMP0_OUT								5
56	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/I2C1_SCL/TPM0_CH0								6
57	PTC2/ADC0_SE11/I2C1_SDA/TPM0_CH1								13
58	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT								14
61	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3								15
62	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT								16
63	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO								24
64	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI								25
65	PTC8/CMP0_IN2/I2C0_SCL/TPM0_CH4								21
66	PTC9/CMP0_IN3/I2C0_SDA/TPM0_CH5								22
67	PTC10/I2C1_SCL								23
68	PTC11/I2C1_SDA								
69	PTC12/TPM_CLKIN0								
70	PTC13/TPM_CLKIN1								
71	PTC16								
72	PTC17								

VSS1  
VSS2  
VSS3  
VSSA  
VREFL

<Value>

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