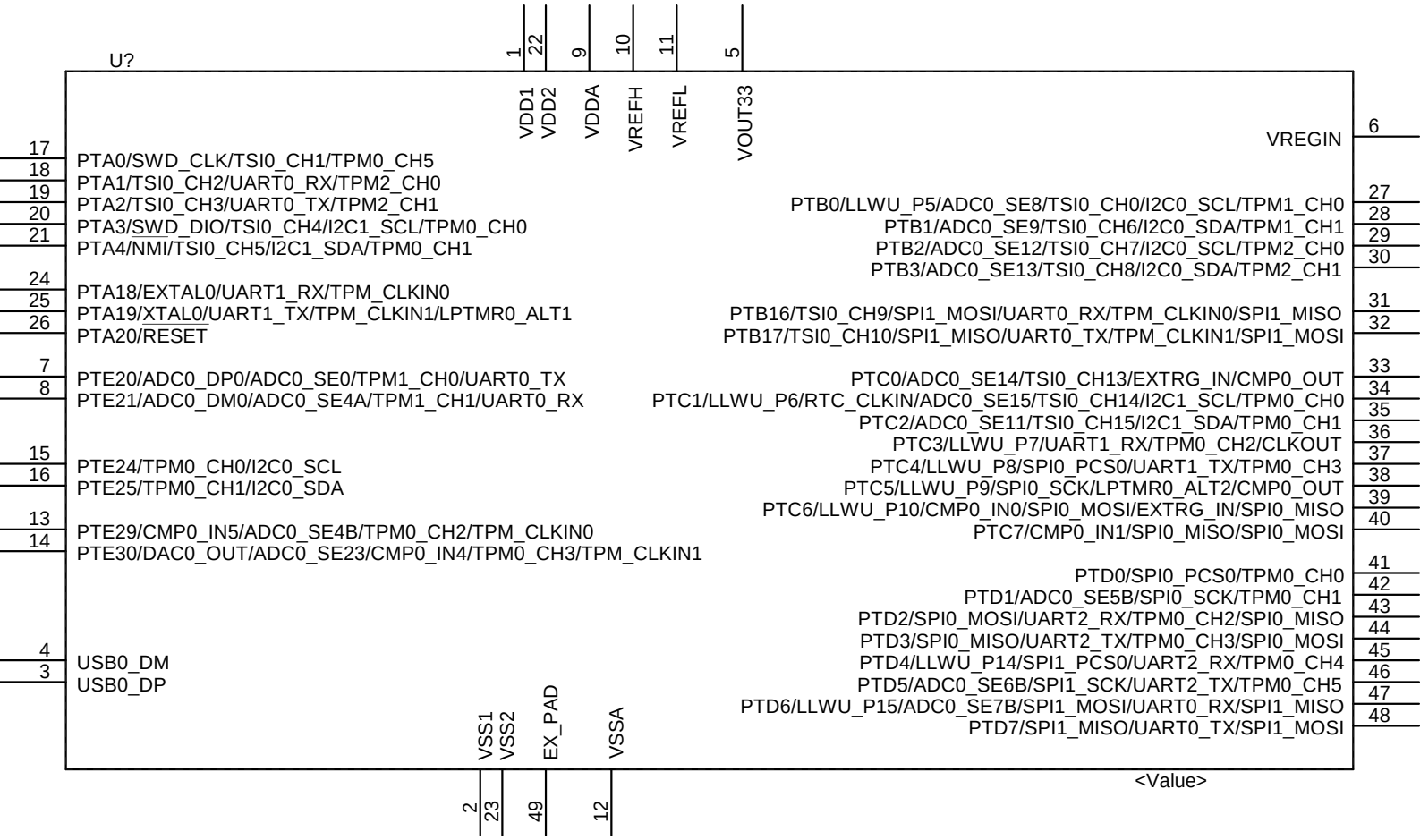
U?
1
22
9
10
11
5
VDD1
VDD2
VDDA
VREFH
VREFL
VOUT33
VREGIN
6
17
18
19
20
21
24
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8
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16
13
14
4
3
PTA0/SWD_CLK/TSI0_CH1/TPM0_CH5
PTA1/TSI0_CH2/UART0_RX/TPM2_CH0
PTA2/TSI0_CH3/UART0_TX/TPM2_CH1
PTA3/SWD_DIO/TSI0_CH4/I2C1_SCL/TPM0_CH0
PTA4/NMI/TSI0_CH5/I2C1_SDA/TPM0_CH1
PTA18/EXTAL0/UART1_RX/TPM_CLKIN0
PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1
PTA20/RESET
PTE20/ADC0_DP0/ADC0_SE0/TPM1_CH0/UART0_TX
PTE21/ADC0_DM0/ADC0_SE4A/TPM1_CH1/UART0_RX
PTE24/TPM0_CH0/I2C0_SCL
PTE25/TPM0_CH1/I2C0_SDA
PTE29/CMP0_IN5/ADC0_SE4B/TPM0_CH2/TPM_CLKIN0
PTE30/DAC0_OUT/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1
USB0_DM
USB0_DP
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35
36
37
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PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/TPM1_CH0
PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/TPM1_CH1
PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/TPM2_CH0
PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/TPM2_CH1
PTB16/TSI0_CH9/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO
PTB17/TSI0_CH10/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI
PTC0/ADC0_SE14/TSI0_CH13/EXTRG_IN/CMP0_OUT
PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/TSI0_CH14/I2C1_SCL/TPM0_CH0
PTC2/ADC0_SE11/TSI0_CH15/I2C1_SDA/TPM0_CH1
PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT
PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3
PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT
PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO
PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI
PTD0/SPI0_PCS0/TPM0_CH0
PTD1/ADC0_SE5B/SPI0_SCK/TPM0_CH1
PTD2/SPI0_MOSI/UART2_RX/TPM0_CH2/SPI0_MISO
PTD3/SPI0_MISO/UART2_TX/TPM0_CH3/SPI0_MOSI
PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4
PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5
PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO
PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI
VSS1
VSS2
EX_PAD
VSSA
2
23
49
12
<Value>